SECRET
NO FOREIGN DISSEM

50X1-HUM

AIRBORNE CONTROL

RADAR K-11M

TECHNICAL DESCRIPTION

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(English Language)

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AIRBORNE CONTROL RADAR K-IIM

Technical Description

CONTENTS

I. GENERAL

1. Purpose and Use

Airborne radar equipment K-IIM is designed for searching and detecting sea surface moving or stationary targets and large ground objects irrespective of the optical visibility.

It is also meant for controlling guided missiles from the moment they are released to the moment the target is destroyed.

Radar equipment K-IIM is installed on heavy bombers, types ТУ-4КС and ТУ-16КС, intended to attack and destroy sea surface targets with guided missiles.

Before release of a guided missile from the aircraft carrier the radar equipment allows preparation of the guided missile for release to be made and performance of the equipment installed on the guided missile to be checked.

2. Composition of Radar Equipment

Radar equipment K-IIM is completed as follows:

Nos	Name of unit	Unit designation
1	2	3
1	Homing antenna	Д-1
2	Receiver-transmitter	Д-2М

1	2	3
3	Receiver	Д-3
4	Sweep unit	Д-4М
5	Indicator unit	Д-5
6	Remote indicator (PPR)	Д-6М
7	Autoselector	Д-7
8	Regulated rectifier	Д-8
9	Bank and sighting stabilization unit	Д-9
10	Tracking unit	Д-10
11	Control panel	Д-11М
12	Distribution box	Д-12М
13	Automatic control box	Д-13М
14	Amplidynes	Д-14
15	Sighting antenna	Д-15М
16	Radio-frequency sighting unit	Д-16М
17	Inverters I and II	Д-18
18	Course and pitch stabilization unit	Д-19
19	Remote control panel	Д-20
20	Course indication unit	Д-21
21	Connection box	Д-22
22	Cables	Д-23М
23	Coupling waveguide	Д-24
24	High-voltage rectifier	Д-25
25	Azimuth gyro with converter ПАГ-1Ф	Д-27
26	Recording unit	Д-26
27	Sighting station (collimator sight)	Д-29М
28	Check board K-1	ДК-17
29	Indicator ИКО-42	
30	Spare parts, tools and accessories	

Note: 1. Units Д-15М and Д-16 are made constructionally as one unit

3. Main Characteristics

No.	Name of characteristics	Designation adopted in description	Value
1	2	3	4
1	Maximum flight altitude of which station operates in navigation condition	H	10 km.
2	Distance range	d_1 d_2 d_3 d_4	10 km. 50 km. 100 km. 200 km.
3	Beam width of homing antenna: (a) in H plane (b) in E plane	 α_1 α_2	 3.4^{o} 2.5^{o}
4	Frequency of radiation during search and homing	f_1	
5	Frequency of sighting system	f_2	
6	Pulse power	W_{pulse}	90 kW
7	Mean power	W_{mean}	60 W
8	Pulse duration	τ_1 τ_2	0.5 µsec. 1 µsec.
9	Pulse repetition rate during search	n_1	
10	Pulse repetition rate during homing	n_2	
11	Sensitivity of receiving channel	η_1	94 db/mW
12	Sensitivity of sighting channel	η_2	85 db/mW
13	Crystal oscillator frequency	F_2	60 Kc/s
14	Wobbulation frequency	D	c.p.s.
15	Main intermediate frequency	f_3	30 Mc/s
16	Sighting intermediate frequency	f_4	40 Mc/s
17	Beam width of sighting antenna	α_3	$44^{o} \pm 2^{o}$

II. OPERATING PRINCIPLES OF RADAR STATION K-IIM

1. Target Search

Sea surface and ground targets are detected in the same way as in ordinary panoramic radar stations. The underlying principle of target detection is reflection of radio waves.

The radiation pattern has the form of a beam 3.4° wide in plane H, and 2.5° wide in plane E.

The azimuth position of the antenna allows the direction to the reflecting object to be found.

The speed of antenna rotation is approximately 6 r p.m.

The indicator screen has a long afterglow property and despite comparatively low antenna speed the operator can observe reflected signals on the entire surface of the screen.

Apart from circular scanning the station is capable of "painting" space within an assigned sector.

During sector scanning the brightness and contrast of the image increase, since the objects within the scanned sector are more frequently illuminated by the radiated pulses.

Depending upon the distance to the target the operator can choose one of four range scales: 10 km., 50 km., 100 km., 200 km.

The search of a target to attack is started on the 200-km. scale.

On the plan position indicator there are bright range rings (range markers) the distance between which corresponds to 10 km., 20 km., 40 km. depending upon the sweep range scale used.

When the operator is required to focus his attention on a certain section (sector) of the terrain use is made of sector scanning.

The vertical diameter of the PPI screen corresponds to the projection of the aircraft longitudinal axis.

During the antenna rotation the moment it is trained along the longitudinal axis of the aircraft may be marked on the PPI screens with a bright radial line - course line.

By using the course line, the rotary light filter with scale (calibrated in degrees from 0° to 360°) the operator can determine the course angle of the target.

Having chosen the target for attack, the operator stops the antenna in the target direction.

Then the operator changes to manual target search.

The target distance is determined approximately by range markers. By means of potentiometer RANGE (ДАЛЬНОСТЬ) located on control panel Д-11М the operator matches the range marker with the target selected. After placing the function switch on the panel in position MANUAL II (РУЧНОЕ II) the operator makes the right and left pulse pips equal on the tracking screen by using handwheels AZIMUTH (АЗИМУТ) and ELEVATION (НАКЛОН). The target being tracked is located in the sweep centre of the tracking indicator.

By placing potentiometer RANGE on the control panel in the middle fixed position the operator locks on the target and starts tracking it automatically in range.

When setting the function switch located on the control panel to position AUTOMATIC (АВТ.) the operator transfers the target to automatic tracking in angular coordinates.

2. Automatic Tracking and Selection of Target in Range

Automatic tracking and selection of a target in range is accomplished by the following main elements: variable delay circuit, comparator, integrator and selector.

The variable delay circuit produces a variable-length pulse. The leading edge of this pulse corresponds to that of the pulse radiated by the antenna, and the trailing edge may be time-shifted through variation of the D.C voltage applied to the variable delay circuit. This voltage is generated by the integrator and its magnitude may be varied either manually, by potentiometer RANGE on control panel Д-11М during

manual lock-on and tracking of the target in range, or automatically by means of the comparator circuit during automatic target tracking in range. The trailing edge of the variable-length pulse triggers the sweep of the tracking indicator with 10-km. range scale and fixed delay multivibrator.

The multivibrator ensures generation of two gate pulses, range marker and a strobe pulse with a 5-km. range displacement with respect to the start of the sweep of the tracking indicator.

The gate pulses shifted by 0.7 µsec. with respect to each other are furnished one to the first valve and the other, to the second valve of the comparator. Apart from being furnished with gate pulses the both valves of the comparator are fed from the receiver output with signals of all targets picked-up by the antenna. Shifting the trailing edge of the variable-length pulse the operator matches the range marker on the tracking indicator with the wanted target echo and whereby locks on the target in range.

In this case, the signal from the selected target gets between the gate pulses. Further, if the target pulse is shifted in relation to the gate pulses, one of them is found overlapped by the target pulse more than the other and this results in positive or negative voltage at the comparator output (depending upon the displacement side of the target pulse).

These voltages are converted by the integrator into a positive control voltage. The magnitude of the control voltage is proportional to the target range, and the rate of its change - to the amount the gate pulses are overlapped by the target pulse.

When the target pulse moves towards "further" the control voltage increases and vice versa decreases when it moves towards "nearer".

This control voltage is applied to the variable delay circuit to control the length of the circuit pulse.

The trailing edge of the variable-length pulse and, consequently, of the gate pulse is shifted in time until

overlapping of the gate pulses by the target pulse is equal, i.e. until the target pulse is between the gate pulses.

Thus, during movement of the target or carrier aircraft flight, the target is being continuously tracked in range.

The selector allows only those target pulses to pass through which are time-coincident with the strobe pulse. As the strobe pulse is strictly time-coincident with the gate pulses, there are only signals from the target tracked in range at the selector output.

These pulses are furnished to the system of automatic tracking of the target in angular coordinates.

Thus, due to automatic tracking and selection of the target in angular coordinates the automatic tracking in direction functions only when triggered by the signals from the targets selected by the operator by covering them with the range marker.

3. Automatic Tracking of Target in Angular Coordinates

When the station operates in automatic tracking mode the radiator of the station antenna rotates about the axis of the parabolic mirror with high angular speed.

The antenna radiator is displaced from the electrical axis of the reflector, therefore the beam of radio waves radiated by the antenna describes a cone in space whose axis is that of the antenna.

Part of space in close proximity to this axis is known as the equisignal zone.

All objects being in equisignal zone will be illuminated by radio pulses whose power is independent of the radiator position during rotation and, consequently, one or another object in the equisignal zone will reflect echo pulses whose intensity does not change during radiator rotation.

Objects outside the equisignal zone will reflect echoes whose intensity is dependent upon the position of the radiator during rotation.

Thus, the reflected signals picked up will be modulated by the rotation frequency of the antenna radiator.

The phase and modulation depth of the reflected pulses will completely characterize the location of the object with respect to the antenna axis.

The phase of pulse modulation will change with the angular position of the object with respect to the antenna.

Thus, the pulses reflected from the target and picked up by the antenna after being amplified and detected, will produce low-frequency voltage which contains information both on angular coordinates and amount of deflection from the equisignal zone.

This voltage is known as the error voltage.

Obtainment of error signal

Fig.3 shows the antenna radiation pattern. On the right of the figure is diagrammed the amount of reflected energy at various positions of the target in relation to the paraboloid axis.

The antenna is so constructed that the axis of the antenna directed electromagnetic beam is off the paraboloid axis by 1.5°.

Driven by motor 2Д-60 the radiator rotates about the optical axis of the mirror at speed Ю r.p.m. causing rotation of the antenna beam about the optical axis of the mirror so, that its maximum describes a cone in space.

Fig. 4 is a representation of the radiation pattern and the cone formed by the maximum of radiation of the antenna when it is rotated. From Fig.3 it is seen that if the target is just on the reflector axis, the receiver will pick up 50 per cent of the maximum energy irrespective of the position of the radiation pattern.

If the target is 1.5° off the paraboloid axis the reflected signal coming to the receiver will vary within 100 to 20 per cent.

Fig.5,a shows the magnitude of the picked-up reflected signal for two positions (upper and lower) of the antenna, the target being 1.5° above the paraboloid axis.

Fig.5,b shows the same for the case, when the target is on the paraboloid axis.

Fig.5 shows only two extreme positions of the radiation pattern but since the antenna is rotated continuously, the signal intensity will vary continuously between the extremities.

Fig.6 shows the change of the signal intensity per one revolution of the radiation pattern, 16 pulses being shown assumingly, though much more pulses fall on one revolution.

Such an intensity modulation of the picked-up signals is the error signal.

Fig.7,a shows a signal of the error occurring when the target is displaced in azimuth only. Fig.7,b presents an error signal formed when the target is shifted in elevation.

As it is seen from Figs 7,a and 7,b the error signal caused by the azimuth displacement of the target lags by 90° from the error signal caused by the elevation displacement.

Fig.7,c shows the error signal in azimuth and elevation.

In Fig.7 the reflector axis is shown assumingly as a point located in the centre of the circle. The axis of the radiation pattern is shown as a point describing a circle during rotation.

The target is presented in the form of a cross-hatched circle.

Deflection of the target from the direction of the paraboloid axis changes the range of variation of the echo pulse amplitude.

If the target is in the direction of the paraboloid axis (equisignal zone), the intensity of the reflected signals is constant (error signal is equal to zero). The wider the measurement range of the pulse amplitude, the greater the modulation depth and, consequently, the greater the error signal.

The echo pulses picked up are modulated by the frequency of antenna radiator rotation. The phase and modulation depth of the echo pulses fully characterize the location of the target relative to the antenna axis; the farther the object from the equisignal zone, the greater the modulation factor the echo pulses will gain.

The phase of modulation of the pulses will vary with the angular position of the object relative to the antenna.

In synchronism with the radiator is rotated the rotor of the reference voltage generator (ГОН). The generator stator has two windings shifted by 90° with each other, therefore, the sine-wave voltages produced by these windings have the frequency equal to that of the radiator rotation and are 90° out of phase with each other.

One of these voltages is the azimuth reference voltage, the other - the elevation reference voltage.

The error voltage is compared with two reference voltages in the azimuth and elevation phase detectors of unit Д-10.

As a result azimuth and elevation control voltages are produced.

These voltages after being amplified act on the actuating motors, antenna Д-1 along the azimuth and elevation axes.

4. Automatic Tracking Monitoring

The automatic target tracking is observed on a special indicator. The indicator is also used for monitoring the selection of a target for tracking in range.

The voltage supplied from the reference voltage generator and switch through a transformer switches on one or the other video amplifier in unit Д-5 and sweep intensification unit.

The change-over is done so that the screen displays the target pulses corresponding to the passage of the right and left position by the beam of the antenna during its rotation.

The target pulse pips on the screen with the target in the equisignal zone are equal in amplitude and directed in opposition.

The target being tracked is always in the centre of the sweep on the tube screen.

The targets located closer than the one being tracked will be seen in the lower portion, and the targets located farther, in the upper portion of the indicator.

5. Sighting System

The sighting system ensures reception and observation of signals from the transponder located on the guided missile.

Per each pulse radiated by the main antenna of the station the transponder produces one reply pulse at wave length τ_2.

If the transponder is moved away from station K-IIM, the reply pulse observed on the screen of the sighting indicator will depart from the start of the sweep, and the distance to the transponder can be read off from the calibration markers on the indicator screen.

Besides, the transponder is so designed that when being moved away from the equisignal line of the K-IIM antenna, the reply pulses are modulated by phase-pulse modulation.

Therefore, these pulses are seen on the screen of the sighting indicator in the form of a pulse more or less blurred in range.

The blurring width depends upon how far the transponder is moved away from the equisignal zone.

6. Beam Capture System

To facilitate catching of the guided missile by the homing antenna beam after it has been released, provision is made in the carrier aircraft for a beam capture system consisting of collimator sight K10-T (unit Д-29M), phase

detector (unit Д-21) and beam capture indicator (pointer instrument ИКО-42).

Coupled to the optical system of the collimator sight is a selsyn, type A-3, which is connected electrically with azimuth selsyn A-3 of the homing antenna.

The optical system of the collimator sight is aimed at the missile after it has been released.

If the axes of the optical system and the homing antenna beam do not run in parallel, azimuth error signals will appear at the output of the sighting station selsyn.

These signals are fed to the phase-detector to be converted into a control voltage which deflects the pointer of the beam capture indicator.

Due to a program potentiometer with a timer being used in course indication unit Д-21 the deflection of instrument ИКО-42 is proportional to the distance between the carrier aircraft and equisignal zone.

On the basis of the data presented by the beam capture indicator the pilot manoeuvres the carrier aircraft so as to ensure beam capture of the missile.

7. Gyrostabilization of Antenna

The station employs bank, course and pitch stabilization of the antenna.

The bank stabilization ensures correct transmission of the phase of reference voltages to the guided missile irrespective of the carrier aircraft attitudes.

The course and pitch stabilization precludes the possibility of the automatic target tracking failure during aircraft manoeuvring or during flight in bumpy air in case of fading and other short-timed interference.

In addition, the course and pitch stabilization improves target detection during slight manoeuvres of the carrier aircraft.

Used as a transmitter of the bank, course and pitch stabilization system is a vertical gyro of autopilot АП-5.

The error signal of bank stabilization is picked off from the diagonal of a bridge formed by the bank potentiometer of the vertical gyro and test potentiometer on the antenna whose slider shaft is coupled through a reduction gear with the bank stabilization shaft.

The error signal is fed to the input of unit Д-9 and converted into a control voltage in the phase detector.

The control voltage is applied to the D.C. amplifier controlling the behavior of the relay amplifier which supplies an actuating motor, type ДК-11, turning the antenna round the bank gyrostabilization shaft.

The error signal of the antenna pitch stabilization is picked off from the diagonal of a bridge formed by a pitch potentiometer of the vertical gyro and test potentiometer whose shaft is coupled with the elevation shaft through the reduction gear and differential.

The error signal is communicated to the input of the phase detector of pitch stabilization (unit Д-19). The pitch stabilization relay amplifier of unit Д19 controls the operation of actuating motor ДК-11 coupled through a differential with the antenna elevation shaft.

In order to match the longitudinal and lateral shafts of the vertical gyro and antenna, the vertical gyro rotates in azimuth in step with the antenna.

The aircraft course data are presented by directional gyro ГПК-Д and selsyn-transformer (unit Д-27).

The receiving selsyn is mounted on the antenna and coupled with the azimuth shaft of the aircraft through the reduction gear and differential.

The error signal is passed from the transmitting selsyn rotor to the course stabilization phase detector of unit Д-19.

The relay amplifier controls the operation of actuating motor ДК-11, which turns the antenna in azimuth through the

reduction gear and differential following the change in the aircraft heading.

Sighting antenna Д-15М must be trained in the same direction with the homing antenna (unit Д-1).

To control the azimuth rotation of the sighting antenna in synchronism with the rotation of the homing antenna, unit Д-15М mounts a selsyn-transformer, and unit Д-1, a transmitting selsyn.

The error signal from the selsyn transformer is supplied to the antenna control phase-detector of sighting unit Д-9.

The relay amplifier controls the operation of actuating motor ДК-11, which turns the sighting antenna until it becomes matched with the homing antenna.

8. Functional Diagram of Radar Equipment

The functions performed by separate units and stages are most diversified.

The functional diagram presented in Fig.8 gives better understanding of the operation and interaction of the units.

(1) Time Relationships

Synchronous selector unit Д-7 is used to stabilize trigger pulse repetition frequency, produce repetition frequencies radiating pulses, coordinate trigger pulses in time, as well as to synchronize the operation of certain units of the station.

The frequency is stabilized by a crystal oscillator operating at a frequency of 60 Kc/s.

The crystal oscillator pulses pass through three frequency dividers and are applied directly to or through a variable delay circuit (wobbulation phantastron Л7-4 producing pulse train wobbulation) to the blocking oscillator functioning in the 1:1 or 1:2 division mode.

The wanted division is chosen when changing the range scale by means of range-scale selector 10, 50, 100 and 200 km. located on control panel Д-11M.

This is how the required pulse train frequency n_2 is formed (during target tracking 10 km., 50 km., 100 km.) or n_1 (in 200-km. search mode).

The first in time to go is the pilot pulse of K-1 circuit (time moment t_1) (See Fig.9).

The suppressor trigger pulses in unit Д3 and sweep trigger pulses in unit Д-4M are delayed by 90 μsec. in relation to the above pulse.

The transmitter trigger pulse in unit Д-2M is delayed by 1 μsec. more relative to the trigger pulse of the suppressor and sweep.

The synchronizing pulses are communicated to the external circuits through the cathode followers.

At time moment t_1 the phantastron variable-delay circuit is started; the trailing edge of the phantastron pulse controls the generation moment of the trigger pulse of the tracking indicator (in the sweep unit), as well as the operation moment of the 34 μsec. fixed delay multivibrator.

The trailing edge of the 34 μsec. multivibrator pulse forms the range marker and triggers the generator producing 1st and 2nd gate pulses, the 2nd gate pulse being delayed by 0.7 μsec. relative to the 1st pulse with the help of a delay line.

The trigger pulses of the tracking indicator, gate pulses and range marker can move simultaneously in relation to time t_1 with mutual time delays being preserved.

Since generation of the above pulses is connected with operation of the variable-delay phantastron the change in the duration of the phantastron pulse makes it possible to move the pulses to the entire distance range and to match the range mark and gate pulses with it upon appearance of the target pulse.

The target pulse through the comparator and integrator circuits controls the pulse duration of the variable-delay phantastron keeping the gate pulses automatically in the state of balance with respect to the target pulse.

The pulse from the target being tracked and the strobe pulse are communicated to the coincidence stage.

The coincidence stage allows the pulse of the tracked target to pass through the selector channels blocking the passage through the specified channels of all the pulses from other targets.

The selected pulse is amplified and fed through the cathode followers to the external circuits (to units Д3, Д4М and Д10) where it is used for tracking the target automatically in direction and for operation of the AGC system.

(2) Receiving-Transmitting Part

Transmitting-receiving unit Д2 (modulator part) is fed from the autoselector with positive-going pulses at repetition frequency n_1 or n_2.

The incoming pulses are amplified by the trigger amplifier utilizing valve 6Н8С and passed to the control grids of submodulator valve ГИ-30.

This stage operates as a biased blocking oscillator and shapes the pulses of the required amplitude, form and duration.

From the submodulator the shaped pulses are fed to the control grids of the modulator stage consisting of two valves ГМИ-83 operating in parallel.

When fed with a pulse, the modulator valves ground the plate of the reservoir capacitor kept at high potential through their low internal resistance.

Owing to this the reservoir capacitor discharges through the magnetron and at this moment radio frequency oscillations are built up in the magnetron with frequency f_1, Mc/s.

The receiving waveguide incorporates a crystal mixer

which apart from being fed with RF energy receives RF energy from the local oscillator using klystron K-19.

As a result of the action produced by two RF oscillations IF signals of 30 Mc/s are discriminated on the crystal mixer load.

These signals are fed to a three-stage IF amplifier using valves 6Ж1П.

To protect the crystal mixer from high-power pulses of RF energy propagating over the waveguide at the moment of transmission, the input of the T-R box incorporates gas discharger PP-49.

The main waveguide attaches the anti-T-R box with gas discharger PP-6, which ensures no losses of reflected signal in the magnetron channel.

Besides, the main waveguide attaches a waveguide length incorporating a crystal mixer and having elements coupled with the main waveguide and waveguide of the local oscillator.

Owing to these coupling elements the crystal mixer at the moment of transmission is supplied with two high-frequency oscillations (from the magnetron and klystron) which create IF signals across the load.

These signals are converted by the klystron AFC circuit into a control voltage being applied to the klystron repeller to make intermediate frequency constant.

The AFC circuit comprises a two-stage IF amplifier, frequency detector, two-stage video amplifier, search blocking oscillator and regulating stage.

The AFC system is of the search type.

When the intermediate frequency deviates from the wanted value the circuit produces the voltage which is used to control the oscillator frequency by varying the voltage at the klystron repeller.

The output of the IF amplifier of the transmitting-receiving unit is applied to IF amplifier unit Д3.

The output signals are amplified in the unit by the six-stage IF amplifier converted by the detector into video

signals to be amplified by the video amplifier and are communicated to two cathode followers operating in series.

The output signals of the first cathode follower are furnished to the selector of unit Д7.

The selected video pulse is used in unit Д10 for automatic tracking in direction and for automatic gain control of the IF amplifiers in unit Д3 (receiver).

The output pulses of the second cathode follower are passed to sweep unit Д4M, in indicator unit Д5 and are used for observing them on the indicator screens.

(3) Sweep and Presentation System

The pips are observed on four indicators, two of them being intended for observation of the ground and target selection.

They duplicate each other's operation and are used - one by the operator, the other by the navigator.

The third indicator is intended to check the antenna direction to the selected target.

The fourth one (sighting indicator) determines the position of the guided missile in the beam and its distance to the target.

The first two indicators use radial-circular sweep, the third indicator, L-type display and the fourth one, A-type display.

The sweep voltage is formed by the circuits of the sweep unit.

There are two sweep channels in the unit, one of them is intended for beam deflection in the search and sighting indicators, the other for beam deflection in the indicator for checking the homing antenna direction which is known as the tracking indicator.

The first sweep channel is triggered by the pulses coming from unit Д7 (autoselector) for one microsecond before the transmitter starting when the sweep range scales are 50 km.,

100 km., 200 km. or by the pulses with variable delay relative to the transmitter pulses when the range scale is 10 km.

For cases requiring all the range scales the variable-delay pulses are supplied for triggering the second sweep channel (tracking indicator).

The first sweep channel consists of a sweep trigger multivibrator, sweep generator and two three-stage sweep voltage amplifiers.

The required duration (range) of the saw-toothed voltage is obtained by switching range scales 10, 50, 100 and 200 on the control panel of the station.

This is accompanied by switching of resistors in the anode circuit of the sweep generator.

The first sweep channel has two outputs.

From one output the sweep voltage is applied to the deflection coils of operator's indicator Д5, from the other, to those of navigator's indicator Д6М.

The circular sweep in these indicators is obtained by rotating the deflection coils in synchronism with the rotation of the homing antenna through a selsyn drive.

The signals producing bright spots on the indicator screens are supplied from the output mixer located in the sweep unit to the video amplifiers mounted in the indicators and are further passed to the control electrodes of the cathode-ray tubes.

These signals consist of video pulses, range marks, course line marks and range calibration marks.

The latter are produced by the calibrator mounted in the sweep unit.

The first anodes of the cathode-ray tubes are fed from the sweep generator with brightening square pulses due to which the beam trace on the indicator screens is being left only during the sweep forward stroke.

The second channel comprises a driven multivibrator and a saw-toothed voltage generator.

The saw-toothed voltage is impressed on the Y-plates of the cathode-ray tube of the tracking indicator.

The X-plates of the tube are fed with video pulses.

Since the plates are fed with signals from various video amplifiers, the switching of these video amplifiers by the azimuth reference voltage results in left and right peaks being visible on the tube screen.

The right pulse corresponds to the right-hand position of the beam during the radiator rotation and the left, to the left-hand position of the beam.

The video pulses on the indicator screen are located on the left and right of the sweep trace.

If the target is exactly in the direction of the equisignal line, the amplitudes of the left and right signals are equal and their equality is used to check the target direction of the antenna.

Out of a number of video pulses arriving at the tracking indicator a signal from the selected target is marked with a range mark which is fed from unit Д7.

Being brightened by the range mark this signal is clearly visible on the screen and the range mark itself is presented on the screen as a bright spot located in the middle of the sweep trace.

The sighting indicator employs A-type display.

The sweep is formed in unit Д4M (sweep unit) and furnished to the sweep amplifier in the indicator unit (Д5) and further to the X-plates of ~~the~~ cathode-ray tube.

From the mixer in the sweep unit through the video amplifier in the indicator unit the Y-plates are furnished with the voltage of the signal which enables the distance between the missile and the target to be measured and gives estimation of the missile flight in the equisignal zone.

(4) Homing Antenna Control

The radiator system of the homing antenna may be rotated about three mutually perpendicular axes: one vertical and two horizontal.

Rotation about the vertical axis is known as rotation in azimuth, and rotation about horizontal axes is known as rotation in elevation and bank respectively.

The azimuth rotation is used to perform the following functions:

(a) Circular scanning of the ground.

(b) Sector scanning of the ground.

(c) Aiming the antenna manually to the selected target in azimuth.

(d) Automatic target tracking in azimuth.

The rotation in elevation is used for:

(a) Selection of a radius of the sweep area in the search mode.

(b) Aiming the antenna to the selected target during manual search in elevation.

(c) Automatic target tracking in elevation.

The position of the antenna in space is stabilized by the course, bank and pitch gyrostabilization system.

The gyrostabilization is effected by means of vertical gyro АП-5 and azimuth gyro Д-27 (ГПК-Д).

The antenna is rotated in azimuth by control motor Д-75 and gyrostabilization motor ДК-11.

From either motor motion is imparted through the reduction units and differential to the big azimuth gear of the antenna.

Motor Д-75 is coupled mechanically with selsyn КС-1 (transmitter), and motor ДК-11 with selsyn ПСД (flat selsyn transmitter).

During rotation of motor Д-75 angular difference occurs between the rotor of selsyn КС-1 located in the antenna (unit Д-1) and rotor of selsyn КС-2 mounted in the control panel (unit Д-11М).

The error voltage developed in the rotor of selsyn KC-2 is used for manual control of the antenna in azimuth and sector scanning.

Selsyn ПСД is connected electrically with a flat selsyn (receiver) located in the azimuth gyro (unit Д27).

The voltage developed in the rotor of this selsyn is used for course gyro stabilization.

Coupled mechanically with the azimuth rotation shaft of the antenna are selsyns СГС-1 and A-3.

Selsyn СГС-1 is designed to produce circular sweep in the plan position indicators.

Selsyn A-3 is a selsyn transmitter and operates into two selsyn receivers: receiving selsyn A-3 located in unit Д-15 and receiving selsyn A-3 located in unit Д-29M (sighting station) or into receiving selsyn A-3 in unit Д-21 (course indication unit).

The voltages developed in the rotors of these selsyns are used: one for controlling the sighting antenna in azimuth, and the other for checking the coincidence of the axes of the homing antenna and sighting station, or axis of the homing antenna with the structural axis of the aircraft.

The rotation in elevation is effected in essentially the same way as the azimuth rotation by elevation motor Д-75 and gyrostabilization motor ДК-11.

Motor Д-75 is coupled mechanically to elevation selsyn KC-1, and motor ДК-11 to the elevation potentiometer.

Selsyn KC-1 is connected electrically with elevation receiving selsyn KC-2 mounted in the control panel.

The rotor voltage of the selsyn is used for manually controlling the antenna in elevation.

The potential difference between the slider of the vertical gyro pitch potentiometer and that of the elevation potentiometer is applied to the pitch gyrostabilization circuit in unit Д-19 (pitch and course stabilization unit).

Selsyn СГСМ-1 of the homing antenna is connected electrically with selsyn СМСМ-1 of the control panel which serves as an elevation indicator.

The bank rotation is effected by motor ДК-11.

At the same time the motor rotates the slider of the bank potentiometer.

The potential difference between the slider of the bank potentiometer located in the vertical gyro and the slider of the bank potentiometer located in unit Д-1 (antenna) is supplied to unit Д-9(bank and sighting stabilization unit) for bank gyrostabilization.

The homing antenna radiator is rotated by a motor, type 2Д-60.

The radiator rotation shaft is coupled mechanically with the reference voltage generator.

The latter produces two sine-wave voltages 90° out of phase with each other.

The frequency of the reference voltages equals the frequency of the radiator rotation.

These voltages are used as reference voltages in the automatic direction tracking system.

One of these voltages is used for modulation of the repetition frequency of the radiated pulses in unit Д-7 (autoselector).

(a) Manual control

The rotor voltage of azimuth selsyn KC-2 incorporated in the control panel is furnished to the circuit in the tracking unit consisting of azimuth error signal amplifier, phase inverter, phase detector and azimuth D.C. amplifier.

The anode circuits of the azimuth D.C. amplifier contain the control windings of the azimuth amplidyne located in unit Д-14 (amplidynes) which produces voltage for azimuth motor Д-75.

Rotating handwheel AZIMUTH (АЗИМУТ) on the control panel causes unbalance of the D.C. amplifier, and motor Д-75 is supplied with the azimuth amplidyne output voltage whose polarity depends upon the direction of the handwheel rotation.

Manual control in elevation is achieved in the same way by rotating handwheel ELEVATION (НАКЛОН).

(b) Sector scanning

During sector scanning the voltages from selsyn KC-2 of the control panel are converted into a D.C. voltage varying periodically in polarity.

This voltage is being supplied to motor Д-75 and changes periodically the direction of the antenna rotation.

The sequence of conversion is as follows:

The rotor voltage of the selsyn is supplied to the circuit of unit Д-10 consisting of a sector scanning transformer, phase detector, multivibrator and sector scanning D.C. amplifier.

The anode currents of the sector scanning D.C. amplifier flow through the control windings of the azimuth amplidyne which is the one that supplies the required voltage to azimuth motor Д-75.

(c) Circular scanning

Circular scanning is effected by artificially unbalancing the D.C. amplifier from the control panel.

Setting the controls in the circular scanning mode changes the bias voltage on one half of the damping signal amplifier connected into the screen grid circuit of the D.C. amplifier, and causes unbalance of currents of the D.C. amplifier.

(d) Gyrostabilization

Gyrostabilization of the homing antenna is carried out by means of course and pitch stabilization unit (unit Д-19) and bank and sighting unit (unit Д-9).

Identical circuits of units Д-9 and Д-19 consist of

error signal amplifiers, phase detectors, differentiating cells, D.C. amplifiers and relay amplifiers.

The input of each unit is supplied with 400 c.p.s. error signal, and the output produces a D.C. voltage which is then applied to the armature of the actuating motor which rotates the corresponding axle of the homing antenna to reduce the error signal.

The error signal in course is caused by angular difference between the flat selsyns (ПСД) mounted in the homing antenna (unit Д-1) and in azimuth gyro (unit Д-27).

The bank and pitch error signal is caused by the mismatch between the corresponding check bank and pitch potentiometers located in unit Д-1 and between bank and pitch potentiometers located in vertical gyro АП-5.

(e) Additional control circuits

To eliminate possible spurious oscillations of the homing antenna during manual control or automatic tracking use is made of a special feedback circuit.

Variable polarity voltages built up by azimuth and elevation motors are passed through feedback filters located in unit Д-13М (automatic control box) and fed to the control grids of the azimuth and elevation damping signal amplifiers.

The currents built up by the damping signal amplifiers in the anode circuits of the azimuth and elevation D.C. amplifiers produce at the output of the amplidynes the voltages of the agitation voltage polarity.

To retain the position of the homing antenna (matching of selsyns КС-1 of the homing antenna and КС-2 of the control panel) when changing from circular scanning and automatic tracking to manual control or sector scanning use is made of the follow-up system.

This system consists of a follow-up amplifier and a follow-up motor.

During circular scanning or automatic tracking, the error voltage from the rotor of azimuth selsyn КС-2 of unit Д-1 is applied to the follow-up amplifier unit Д-10 which produces control voltage for the follow-up motor located in unit Д-11М.

The shaft of the follow-up motor is coupled mechanically to that of the azimuth selsyn rotor through a reduction gear and differential.

(5) Follow-Up Sighting System

The reply signals from the guided missile come to the sighting antenna, then are converted by the crystal mixer into signals of intermediate frequency (f_2), amplified by the six-stage IF amplifier, converted by the second detector into video signals and after being amplified by the video amplifier are passed to the cathode follower.

From the cathode follower the video signals are communicated to the mixer of unit Д-4М and after passing the succeeding stages are displayed on the screen of the sighting indicator.

Besides, from the output of the cathode follower the video signals are furnished to the mixer in unit Д-4 and then to the video amplifier of unit Д-5 and to the Y-plates of the cathode-ray tube.

The sighting antenna is trained on the guided missile by means of a circuit which is identical with the gyrostabilization circuits incorporated in unit Д-9.

This circuit is fed with an error signal from the rotor of selsyn A-3 located in unit Д-15М (sighting antenna).

The output voltage of the above-mentioned circuit is applied to the sighting antenna azimuth motor (ДК-11) coupled mechanically to the rotation axle of the sighting antenna.

(6) Control of Radar Equipment

The main controls for tuning and checking the performance of the station are carried on the control panel. Part of controls is located on connection boxes Д-12M and Д-13M, and on the front panels of the units.

III. DESCRIPTION OF UNITS

1. Homing Antenna Д-1

The homing antenna is designed:

(a) to illuminate ground and sea surface with pulses of radio-frequency energy and pick up the reflected signals;

(b) to form a narrow symmetrical beam of the radiation pattern and the equisignal zone;

(c) to make the beam move in azimuth, elevation and bank.

The antenna assembly consists of the radio-frequency portion and electromechanical control elements.

The radio-frequency portion of the antenna assembly consists of the following main components:

1. Parabolic reflector.
2. Double-slot radiator.
3. Radiator rotating joint.
4. Elevation rotating joint.
5. Bank rotating joint.
6. Azimuth rotating joint.
7. Connecting waveguide.

The diagram of the antenna radio-frequency channel is shown in Fig.11.

Radio-frequency elements

The reflector of the homing antenna is essentially a rotary paraboloid with a focal distance of 270 mm and flare diameter of 750 mm fed by a double-slot radiator.

The reflector forms a radiation pattern, 3.4° wide in plane H and 2.5° in plane E, by half power points.

A number of metal plates mounted on the radiator feeding waveguide widens the radiation pattern in plane H and ensures that the amount of power within the bearing angles (± 5°) is not less than 0.5 per cent of P_{max}.

Radio-frequency energy is conveyed to the radiator through the feeding waveguide.

The tapered part of the waveguide turns into a resonance cavity provided with two slots through which the radio-frequency energy is fed to the reflector.

The double-slot head of the radiator is displaced relative to the reflector axis so that the axis of the antenna directional radiation does not coincide with that of the paraboloid, and departs from it through an angle of 1°30'.

By aid of a motor the radiator may rotate about the optical axis of the paraboloid at 1800 r.p.m. (30 r.p.s.) causing the antenna electromagnetic beam to rotate so that its maximum describes a cone in space.

Fig.12 shows the antenna radiation pattern and the cone formed by the maximum of radiation of the antenna during antenna rotation.

From the illustration it is seen that with a target directly on the reflector axis, the receiver will take 50 - 60 per cent of the maximum energy whatever may be the position of the radiation pattern.

If the target is off the paraboloid axis by 1°30', the intensity of the reflected signal will vary with the radiator rotating within 100 to approximately 20 per cent.

Absence of intensity-modulated echo signals (presence of signals themselves) indicates that the target is in the equisignal zone on the reflector axis.

Waveguide rotating joints

All the rotating joints are coupled with one another by means of connecting waveguides.

The waveguide run of the unit is sealed hermetically.

Rubber gaskets are provided between the flanges of the connecting waveguides to ensure air-tightness.

All the rotating joints are made air-tight by means of a tight-fitting collar of frost-resistant and wear-resistant plastic.

The radiating slots of the radiator are sealed with mica plates in metal holders.

The antenna radiation pattern in planes H and E is presented in Fig.13.

Electromechanical elements

The electric drive portion of the antenna consists of the following main components:

1. Reduction unit for rotating the antenna in azimuth (main actuating motor Д-75, auxiliary actuating motor ДК-11, azimuth sweep selsyn СГС-1, follow-up selsyn КС-1, sighting selsyn А-3, flat course transmitting selsyn ПСД).

2. Reduction unit for turning the antenna in elevation (main actuating motor Д-75, auxiliary actuating motor ДК-11, elevation selsyn КС-1, elevation indicator selsyn СГСМ-1, pitch check potentiometer).

3. Reduction unit for turning the antenna in bank (bank actuating motor ДК-11, bank check potentiometer).

4. Radiator reduction unit (turning motor 2Д-60, reference voltage generator).

5. Reduction unit of vertical gyro АП-5.

The vertical gyro rotates in azimuth in synchronism and in phase with the antenna mirror with gear radio 1:1.

The vertical gyro potentiometers are connected with the bank and pitch potentiometers of the antenna and perform the

function of signal transmitters for the bank and pitch stabilization systems.

The homing antenna is rotated in azimuth by two D.C. motors M1-4, type Д-75, and M1-3, type ДК-11, which are actuating motors of the tracking (control) channels and course stabilization follow-up systems respectively.

Tilting the antenna up and down in elevation is also performed by D.C. motor M1-11, type Д-75, and M1-10, type ДК-11 (See Fig.16), which are actuating motors of the follow-up systems of the tracking (control) channels and pitch stabilization respectively.

Bank turning of the homing antenna (with respect to the elevation shaft journals) is effected by D.C. motor M1-7, type ДК-11, which is the actuating motor of the bank stabilization follow-up system.

The field windings of all the actuating motors are fed from the 27-V D.C. aircraft network.

Control voltages are applied from amplidyne unit Д-14 to the armatures of actuating motors M1-4 through the azimuth channel and M1-11 through the elevation channel.

Control voltages to the armatures of actuating motors M1-7, and M1-10, and M1-3 are supplied from the relay amplifiers of stabilization units Д-9 and Д-19 (See Figs 14, 15, 16, 17) through bank, elevation and azimuth channels (course stabilization) respectively.

The azimuth channel follow-up system controlling the antenna rotation in azimuth consists of azimuth selsyn M1-6, type KC-1, coupled through a reduction unit with the antenna, and a selsyn-transformer, type KC-2, in the control panel. The stator windings of azimuth selsyn M1-6 are connected to the respective windings of the selsyn-transformer in the control panel.

The rotor winding of the azimuth selsyn is supplied with 115 V , 400 c.p.s. from the automatic control box.

By rotating handwheel AZIMUTH (АЗИМУТ) on the control panel the operator turns the rotor of the azimuth selsyn-transformer, type КС-2, thereby introducing an error signal in the azimuth channel of tracking unit Д-10.

The error signal is being converted and amplified by the D.C. amplifier (unit Д-10) whose load is the control winding of the amplifying dynamotor (unit Д-14).

The amplidyne output voltage is being applied to the armature of the azimuth actuating motor M1-4, which turns the antenna towards decrease of the angular difference between the rotors of the selsyn pair.

The elevation channel follow-up system controlling the antenna motion in elevation operates in essentially the same manner.

In this case, the stator windings of elevation selsyn M1-12 are connected to the respective windings of the selsyn-transformer in the control panel.

The rotor winding of selsyn M1-12 is fed with 115 V, 400 c.p.s. from the automatic control box.

Synchronous rotation of the circular sweep in indicator unit Д-5 and plan position indicator Д-6 with the homing antenna is ensured by a follow-up system consisting of antenna-mounted transmitting selsyn M1-1, type СГС-1, and two receiving selsyns, type СМС-1, located in units Д-5 and Д-6М respectively.

The stator windings of the azimuth sweep selsyn (M1-1) are connected to the corresponding receiving selsyn in units Д-6М and Д-5.

The rotor windings of these selsyns are fed with 115 V, 400 c.p.s.

The above selsyns turn in synchronism.

For synchronous and inphase rotation of the sighting and homing antennas in azimuth, as well as for the receiver course indication, use is made of a sighting follow-up system consisting of sighting selsyn M1-2, type A-3, whose stator

windings are connected to the corresponding windings of the selsyn-transformer, type A-3, located in course indication unit Д-15 and the corresponding windings of selsyn-transformer, type A-3, in course indication unit Д-21 (in COMBAT COURSE mode) or in sighting station Д-29M (in BEAM CAPTURE mode).

The rotor winding of selsyn M1-2 is fed with 40 V, 400 c.p.s. from the transformer in unit Д-13M.

For remote transmission of the elevation angle of the homing antenna to the pointer instrument in the control panel, a follow-up system is used consisting of transmitting selsyn M1-9, type СГСМ-1 and a receiving selsyn СМСМ-1 in unit Д-11. The stator windings of elevation selsyn indicator (M1-9) are connected to the corresponding windings of receiving selsyn СМСМ-1 whose rotor, as well as the rotor of selsyn M1-9, is fed with 115 V, 400 c.p.s.

Both selsyns turn in synchronism.

The antenna radiator and reference voltage generator M1-13 are driven round by a D.C. motor, type 2Д60 (M1-8).

The reference voltage generator produces two sinusoidal voltages shifted in phase by 90°.

These voltages are supplied as reference voltages to unit Д-10 to shape the antenna control signals during automatic tracking (See Fig.22).

Bank and pitch potentiometers R1-3 and R1-6 together with the transmitting potentiometers of the vertical gyro constitute bridges (or measuring transmitters) of the bank and pitch stabilization follow-up systems.

The measuring-transmitting bridge of the bank stabilization system is fed with 40 V, 400 c.p.s. from transformer Tp1-1 mounted on the homing antenna.

The slider of the bank-transmitting potentiometer located in vertical gyro АП-5 is earthed, and the voltage from the slider of bank potentiometer R1-3 is being applied to bank gyrostabilization unit Д-9.

Here this voltage is converted into D.C. control voltage which forces motor M1-7 to rotate in bank (the latter is coupled through a reduction gear with potentiometer R1-3) until the potential difference across the potentiometer sliders is zero.

Bank check potentiometer R1-3 mounts limit switches B1-5 and B1-6 which open the circuit of actuating motor M1-7 on the boundaries of the bank working area.

The pitch stabilization follow-up system operates in the same manner (See Fig.21).

The bridge circuit arrangement of pitch potentiometers R1-6 and R1-2 of the vertical gyro is supplied with 40 V, 400 c.p.s. from the transformer in the control panel.

Signal from the slider of potentiometer R1-2 (vertical gyro) is passed to the pitch gyrostabilization unit.

The output voltage of unit Д-19 is fed to the armature of elevation actuating motor M1-10 and rotates the antenna and the slider of pitch check potentiometer R1-6 until the pitch voltage is zero.

The antenna tilt in elevation equals 7° up and 42° down.

To limit the antenna tilting within the specified range there are two limit switches B1-3 and B1-4.

Upon operation, these switches energize relays P13-3 and P13-4 in the control panel by means of which the control windings of the elevation amplidyne are shunted by selenium rectifiers (located in the control panel).

This results in closing the amplidyne field windings and, consequently, in the elevation actuating motor (M-11) coming to a standstill.

Relays P13-3 and P13-4 break the armature circuit of elevation stabilization actuating motor M1-10 at the edge of the elevation working area.

Course stabilization follow-up system

The homing antenna is course-stabilized by means of flat transmitting selsyn M1-5 , type ПСД, coupled with the gyro compass selsyn.

The rotor of selsyn M1-5 is supplied with 40 V, 400 c.p.s. from unit Д-13; the stator windings of this selsyn are connected to the corresponding stator windings of the selsyn-transformer, type ПСД in the gyro compass.

The voltage from the selsyn-transformer rotor proportional to the angular difference between the rotors of these selsyns is fed to the input of unit Д-10 (course stabilization channel), where it is amplified and converted into D.C. voltage being supplied to actuating motor M1-3, type ДК-11 (Fig.17).

Motor M1-3 rotates the homing antenna through a reduction gear toward reduction of the course error.

Course indicator B1-1 operates to send a pulse to unit Д-5 when the antenna azimuth coincides with the lubber line of the aircraft.

Contact B1-2, through which the relay is energized to phase the circular scan selsyns of units Д-5 and Д-6, is closed by a cam whose arc is 46° (± 23° with reference to the azimuth axis of the reflector).

Elevation limiting of homing antenna Д-1

The homing antenna may tilt up and down in elevation within angle 8° up and 42° down.

Special elevation stowing switches prevent further motion of the antenna and appearance of substantial efforts during stops.

The connection diagram of the elevation stowing switches is shown in Fig.18.

Selenium rectifiers Д13-1 and Д13-2 incorporated in the automatic control box (unit Д-13M) pass currents only in one direction.

With open contacts of relays P13-4 and P13-3, the selenium rectifiers are disconnected and the antenna may be tilted up and down (depending upon the current intensity in the amplidyne control windings).

Suppose the current of valve Л10-11 prevails over that of valve Л10-12.

The antenna in this case moves upwards.

When the antenna reaches the extreme upper position the upper limit switch closes to feed + 27 V to relay P13-3.

The relay operates, which results in the amplidyne field winding being shunted by selenium rectifier Д13-1, magnetic field disappearing and antenna stopping.

When rotating the elevation handwheel on the control panel in the opposite direction, the anode currents of valves Л10-11 and Л10-12 of IF amplifier in unit Д-10 are so changed that the current in the amplidyne control windings changes its sign and the current of valve Л10-12 will prevail.

As selenium rectifier Д13-1 passes current only in one direction, it fails to shunt the field winding of the amplidyne; the antenna will start tilting down; as a result, relay P13-3 will be deenergized and break the field winding circuit shunted by selenium rectifier Д13-1.

With the antenna in the extreme lower position relay P13-4 operates to connect selenium rectifier Д13-2 which will function as mentioned above.

During simultaneous closure of the upper and lower limit switches, relays P13-3 and P13-4 break the ± 27-V circuit of the elevation relay amplifier incorporated in unit Д-19.

Remote transmission of antenna azimuth angle

The remote angle transmission system for synchronizing the azimuth rotation of the homing antenna and sweep on plan position indicators Д-5 and Д-6M comprises a transmitting selsyn, type СГС-1, located in the ~~antenna~~ azimuth reduction unit and two receiving selsyns, type СМС-1, installed in units Д-5 and Д-6M respectively.

The receiving selsyn turns the deflection coil of the indicator in step with the azimuth rotation of the antenna.

The rotating antenna is coupled with the rotor of selsyn СГС-1 through a step-up gear ratio of 1:10, and the rotor of selsyn СМС-1 is coupled to the deflection coil of the indicator through a step-down gear ratio of 10:1.

Thus, the antenna will rotate in step with the deflection coil, and the angle the coil lags behind the antenna is 10 times as small as the angle the receiving selsyn lags behind the transmitting selsyn.

To eliminate the angular difference (which may occur with the equipment deenergized due to jolting or for a variety of other reasons) phasing cam switches are mounted on the antenna and in the indicator.

The action of cam switches is illustrated in Fig.19.

The contact units of the indicator cam switches are so assembled that, with the deflection coils positioned in a right way with respect to the antenna, the phases of selsyns СГС-1 and СМС-1 remain connected through the contact units or contacts of phasing relay P12-1 housed in the connection box of unit Д12М.

Before operation of the cam switches located on the antenna and in indicators Д-5 and Д-6М the phases of both selsyns stators are connected through the contact unit of the indicator cam switch.

When the antenna occupies the position within 157° and 203° (180° ± 23°) the phasing cam of the antenna - protrusion of the main gear rim - makes the contact unit of the antenna cam close and relay P12-1 is earthed.

The relay contacts operate to connect the phases of the stators of selsyns СГС-1 and СМС-1.

The indicator phasing cam - a protrusion on the deflection coil body - breaks its own contact unit while passing position 180° ± 18° during the coil rotation.

But the stator phases of the selsyns remain connected, since relay P12-1 contacts continue connecting the stator phases.

The operating angle of the antenna cam equals 46°, while that of indicator cam equals 36°.

If the antenna and deflection coils are phased correctly, the circuits of the selsyns will not be open during the antenna rotation since closing of the antenna-mounted switch overlaps opening in the indicator by ± 5°.

In case of dephasing the circuits will be broken and the deflection coil rotation will be retarded until the coils are in the position corresponding to that of the antenna.

2. Transmitter-Receiver Д-2М

The transmitter-receiver consists of:

1. Submodulator designed to shape the pulses triggering the modulator.

2. Modulator designed to shape powerful pulses modulating the magnetron oscillator.

3. Magnetron oscillator designed to generate powerful radio-frequency pulses.

4. Waveguide system with T-R switch, mixer and klystron oscillator.

The system is designed to receive and convert the target echoes into intermediate-frequency pulses.

5. IF preamplifier.

6. Klystron automatic frequency control circuit designed to keep intermediate frequency constant.

7. Power pack consisting of filament transformers, high-voltage rectifier feeding the modulator valve anodes, the rectifier feeding the anodes of the submodulator valves and screen grids of the modulator valves, bias rectifier for the modulator and submodulator control grids.

Schematic diagram of transmitter-receiver

Fig. 20 shows the schematic diagram of the transmitter-receiver.

The transmitter-receiver is started by positive pulses of 10-V amplitude from the autoselector lock unit.

The repetition frequency during search operation is n_1 c.p.s. during tracking is n_2 c.p.s. wobbulated at frequency 10 c.p.s.

The trigger pulse is passed through autotransformer Tp2-1 to the grid of the trigger amplifier connected as a blocking oscillator with cathode follower using 6H8C (Л2-1).

The pulse amplified to 160 V is communicated from the cathode follower to the control grids of valve ГИ-30 (Л2-2) through duration switching relay P2-1.

The submodulator uses dual beam tetrode ГИ-30 in a biased blocking oscillator circuit.

During the pulse intervals the blocking oscillator is biased negatively with - 120 ± 10 V.

The bias voltage is impressed on the control grids of valve Л2-2 through grid leak R2-12 and grid suppressors R2-10 and R2-11.

The bias voltage is taken off from resistor R2-31, bias rectifier divider.

The trigger pulses drive the blocking oscillator valve Л2-2 into conduction.

The blocking oscillator produces short pulses whose length is determined by the characteristics of the valve grid circuit.

The repetition frequency of the blocking oscillator pulses corresponds to that of the pulses arriving at its grid. The valve anodes are coupled through resistors R2-8 and R2-9 to the anode winding of pulse transformer Tp2-3.

Resistors R2-4, R2-76, R2-6 and R2-5 connected in parallel with the input (grid) and output windings of the pulse transformer, serve to quench parasitic oscillations occuring on the pulse trailing edge due to parasitic parameters of the pulse transformer.

From the divider composed of R2-5 and R2-6 is taken a voltage for monitoring the submodulator pulse on the oscillograph when aligning the transmitter-receiver. The additional fifth winding produces a trigger pulse of at least 100-V amplitude.

The transmitter-receiver unit provides for operation with two pulse durations.

The durations are changed over by means of relay P2-1.

When the blocking oscillator operates with a circuit composed of L2-2 and C2-5, the pulse duration equals 0.5±0.05 µsec.

When the blocking oscillator operates with a circuit composed of L2-1 and C2-6, the pulse duration equals 1±0.1 µsec.

The duration switching relay is controlled from unit Д11М.

The shaped pulse is fed from the output winding of transformer Tp2-3 to the control grids of the modulator valves. The amplitude of the submodulator pulse is 1000 ± 100 V.

The transmitter modulator uses valves Л2-3, Л2-4, pulse tetrodes ГМИ-83, parallel-connected in a circuit with partial discharge of the storage capacitor.

During the pulse intervals the modulator valves are cut off on the control grids by negative bias voltage of -900 ± 50 V.

This voltage is applied to the control grids of the modulator valves by the bias rectifier from the anode of valve Л2-6 through the winding of pulse transformer Tp2-3 and resistors R2-13, R2-18.

The screen grids of the modulator valves are fed with a positive voltage of 1200 ± 50 V.

This voltage is applied to the screen grids of valves Л2-3, Л2-4 through limiting resistors R2-14, R2-15, and R2-16 from the rectifier feeding the anodes of submodulator Л2-5.

The screen grids are blocked by capacitor C2-10; in parallel with the capacitor is placed discharger РИ2-1 protecting the valve grids from maximum voltages occurring in case of intervalve breakdowns.

The modulator valves are triggered by positive pulses of 1000 ± 100 V amplitude being fed to the control grids of the modulator valves from the output winding of pulse transformer Tp2-3.

In this case, the control grids are found to be kept at a positive potential of the order of 100 to 250 V.

The valves are sharply made conductive, their internal resistance drops to some 100 ohms and the reservoir capacitor discharges partially through the valves to the magnetrons. As a result, the magnetron cathode appears to be at high negative potential.

Reservoir capacitor C2-13 has rather a large capacitance valve (0.05 microfarad), therefore during the pulse time there is no noticeable voltage drop across it, and the top of the pulses coming to the magnetron remains almost flat. Upon cessation of the positive pulse on the grids of valves Л2-3 and Л2-4 the valves are driven to cut-off, reservoir capacitor C2-13 is charged by the high-voltage rectifier (valves Л2-7 and Л2-8) through R2-17, R2-19, R2-20 almost as high as the voltage of the power source (14.5 kV). Resistor R2-20 and capacitor C2-14 constitute a measuring circuit meant for measuring the magnetron current D.C. component by a meter located on the control panel.

The magnetron is used as a high radio-frequency oscillator in the transmitter unit.

The radio-frequency energy of the magnetron is passed through the waveguide run to the antenna. The antenna is changed over to the reception or transmission channel by means of the antenna switch formed by anti-transmit-receive tube (PP6; Л2-11) and transmit-receive tube (PP-49, Л2-12) connected to the narrow side of the waveguide.

Used as a local oscillator in the unit is a klystron oscillator (Л2-10) producing continuous radio-frequency oscillations with a frequency 30 Mc/s higher than the magnetron frequency.

During reception the target echoes are communicated through the antenna to the main waveguide onto crystal mixer Д2-1. This mixer is also fed with the local oscillator output.

From the crystal mixer the IF pulses are supplied to the input of the IF preamplifier.

The IF preamplifier is a 3-stage amplifier using valves 6Ж1П (Л2-13, Л2-14, Л2-15).

When powerful magnetron pulses are transmitted into the antenna, part of the magnetron energy breaks through the attenuator with 70-db attenuation and is supplied to crystal mixer Д2-2 of the AFC channel. The same crystal mixer is supplied with radio-frequency energy from the klystron (Л2-10).

The crystal mixer output converted in the AFC mixer cell is fed in the form of IF pulses to the two-stage IF amplifier using valves 6Ж1П (Л2-16 and Л2-17), amplified by it and is coupled to discriminator, 6х6 (Л2-18). The detected pulses (video pulses) are furnished from the discriminator load to the double-stage video amplifier. From the video amplifier positive-going pulses are passed to the grid detector (half-of valve Л2-20-6Н9С).

The negative voltage regulating the klystron frequency variations is picked off the anode load of the grid detector and is coupled to the klystron repeller.

The potentiometer for manual control of the klystron repeller voltage is carried on the control panel.

The power pack of the transmitter-receiver consists of valve filament transformers and three rectifiers.

The rectifier supplying the submodulator anodes and modulator screen grids utilizes valve B1-0.02/20 (Л2-5).

The bias rectifier utilizes valve B1-0.02/20 (Л2-6).

The high-voltage rectifier utilizes valves B1-0.02/20 (Л2-7, Л2-8).

The filaments of valves Л2-1, Л2-2, Л2-3, Л2-4, Л2-5 and Л2-6 are supplied from transformer Тр2-6.

Valves Л2-7 and Л2-8 are supplied from filament transformer Тр2-7.

Magnetron oscillator Л2-9 is supplied from transformer Тр2-4.

The valve filaments of the receiving portion of the unit (valves Л2-10, Л2-13, Л2-14, Л2-15, Л2-16, Л2-17, Л2-18, Л2-19, Л-20) are supplied from filament transformer Тр2-9.

The bias and submodulator supply rectifiers (Л2-5, Л2-6) use a half-wave circuit and operate from common transformer Тр2-5.

The bias rectifier is loaded by capacitor С2-22 and a voltage divider composed of series-connected resistors R2-29, R2-81, R2-30, R2-31, R2-32. The load of the submodulator anode supply rectifier and the screen grids of the modulator are capacitor С2-19 and a voltage divider composed of resistors R2-22, R2-23, R2-24, R2-25, R2-26, R2-27.

The submodulator and modulator bias rectifier produces a voltage of - 900 $\pm$ 50 V. The voltage produced by the rectifier supplying the submodulator anodes and the screen grids of valves Л2-3, Л2-4 equals 1400 $\pm$ 50 V.

The high-voltage rectifier supplying the anodes of the modulator valves employs valves Л2-7, Л2-8 in a voltage doubler circuit.

The rectifier load is the capacitive filter formed by capacitors С2-24, С2-25, the anode circuit of the modulator valves and reservoir capacitor С2-13.

The rectified voltage at the rectifier output can be regulated by changing over the taps of transformer Тр2-8. Stepless control can be effected in box Д12М by means of a variable resistor connected in series with the primary winding of transformer Тр2-8.

Cooling is provided by fans М2-2 (Д-7) and М2-1 (Д-7) installed in the unit.

The motors are supplied from 27-V ship's mains.

When high voltage is switched on the magnetron filament voltage is automatically reduced from 6.3 V to zero.

To ensure safe attendance of the unit, the latter incorporates electrical (ВК2-1) and mechanical (ВК2-2) interlocks.

To protect the receiver against pulse interference via the supply circuits the modulator is provided with a number of decoupling filters.

Pressure inside the unit is checked by rheostat pressure transmitter ДД2-1 measuring the air pressure within 0.8 to 1.5 atm.

When the pressure inside the unit drops below the rated value (0.8 atm.) the pressure transmitter automatically opens the primary supply circuit of the high-voltage rectifier. Air into the unit and waveguide system is pumped with the help of a hose and a valve connected to the air main of the aircraft.

Transmitter-receiver characteristics

The frequency generated by the magnetron is f_1 Mc/s.

The average power generated by the magnetron is at least 57 W.

Pulse duration:

(a) 0.5 ± 0.05 microsec;

(b) 1.0 ± 0.1 microsec.

The pulses repetition frequency:

(a) n_2 c.p.s., wobbulated;

(b) n_1 c.p.s.

The intermediate frequency of the IF preamplifier is 30 ± 0.3 Mc/s.

The amplification factor of the IF preamplifier (K) is at least 10.

The passband of the IF preamplifier (at 0.7 voltage level) is at least 6 Mc/s.

The sensitivity of the receiver channel is at least 94 db.

The amplitude of the starting pulse is at least 100 V.

The AFC passband of the system amplifier (at 0.7 voltage level) must be at least $\pm$ 2.5 Mc/s when readings are taken with respect to 30 Mc/s.

The voltage of the klystron frequency control is 160 ± 30 V.

The unit operates on:

(a) A.C. voltage of 115 V $\pm$ 3 per cent, 400^{+40}_{-20} c.p.s.

(b) D.C. voltage of + 27 ± 2 V.

(c) D.C. regulated voltage of 300 V ± 1 V.

(d) D.C. voltage of + 140 V ± 5 per cent.

(e) D.C. regulated voltage of –225 V ± 5 per cent.

The unit consumption:

(a) Not more than 6 A in 115 V, 400 c.p.s. circuit.

(b) Not more than 3 A in 27 V circuit.

(c) Not more than 35 mA in +300 V stabilized voltage circuit.

(d) Not more than 4 mA in –255 V circuit.

Trigger stage

This is designed for amplifying the trigger pulse.

The trigger pulse is shown in Fig.21.

The trigger stage employs dual triode 6H8C. The input of the first valve includes pulse autotransformer Tp2-1 whose purpose is to amplify the trigger pulse.

The left portion of the valve functions as a blocking oscillator, the right portion, as a cathode follower.

The constants of the trigger stage are summarized in Table 1.

Table 1

U_{heater}, V	I_{heater}, A	U_{anode_1}, V	U_{anode_2}, V	E_{g1}, V	E_{g2}, V	U_{out}, V
6.3±0.2		400±50	400±50	–33±5	0	160

Submodulator

The submodulator employs valve ГИ-30 (Л2-2) in a separately excited blocking oscillator circuit. The submodulator circuit is shown in Fig.22. The blocking oscillator constants are given in Table 2.

Table 2

U_{heater}, V	I_{heater}, A	E_{g1}, V	E_{g2}, V	U_{anode}, V
6.3±0.2	2.5	-120±10	600	1400±50

The blocking oscillator is triggered by positive pulses of 160-V amplitude being passed from the trigger stage.

The trigger pulse will open the valve right after the line has been discharged. The line discharge takes place rather slowly and is entirely determined by grid leaks R2-12, R2-32. The amplitude of the pulses at the submodulator output U_{out} = 1000 V ± 100 V.

Modulator

Fig.23 shows the modulator circuit. The modulator employs two pulse tetrodes, type ГМИ-83, operating in parallel. The modulator operation is based on partial discharge of a reservoir capacitor to the magnetron through the modulator valves.

The constants of the modulator valves are tabulated below.

Table 3

U_{heater}, V	I_{heater}, A	U_{anode}, kV	I_{anode}, mA	E_{g2}, V	E_{g1}, V	U_{g1}, V
27	2.15	at least 13	12	1200±50	-900±50	1000±100

In intervals between the pulses the control grids of the modulator valves are supplied with large negative voltage E_{g1} = -900 V from the bias rectifier. The pulses of 1000 ± 100 V amplitude produced by the blocking oscillator are fed to the

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control grids of the modulator valves to the output winding of the blocking transformer. In this case, the valve control grids are found kept at high positive potential of + 100 to 250 V.

Voltage $E_{g2} = 1200 \pm 50$ V is applied to the screen grids of the valves from the submodulator supply rectifier through resistor R2-16, R2-15, R2-14. Resistors R2-13, R2-18 contained in the control grid circuits of the modulator valves are intended to cancel spurious oscillations in the modulator.

Reservoir capacitor C2-13 of the modulator has a 0.05-microfarad value and is rated for an operating voltage 16,000 V. During the pulse intervals it is being charged by the high-voltage rectifier through charging resistors R2-17 and R2-19. The capacitance value of the reservoir capacitor, 0.05 microfarad, ensures that the voltage pulse tilt does not exceed 5 per cent of the amplitude value.

Rectifiers

(a) Bias rectifier

The bias rectifier serves to supply negative bias voltage to the control grids of valves ГМИ-83, ГИ-30 and 6H8C. It employs a kenotron, type B1-0.02/20.

The bias rectifier is loaded by filter capacitor C2-22 and resistors R2-29, R2-81, R2-30, R2-31, R2-32 connected in parallel with it. The rectified voltage equals -900 ± 100 V.

(b) Submodulator supply rectifier

The submodulator rectifier feeds positive voltage to the anode of valve ГИ-30 (+1400 ± 100 V) and to the screen grids of valves ГМИ-83 and ГИ-30.

Fig. 24 shows the schematic diagram of the submodulator supply rectifier and bias rectifier. Both rectifiers employ one transformer Tp2-5.

The submodulator supply rectifier uses kenotron B1-0.02/20.

(c) High-voltage rectifier supplying anodes of valves ГМИ-83

The high-voltage rectifier uses kenotrons, type B1-0.02/20, in a voltage-doubling circuit.

Fig. 25 shows the rectifier circuit. The rectifier employs two transformer Tp2-7 and Tp2-8.

Tp2-7 is a filament transformer designed to feed the filaments of the rectifier valves.

Tp2-8 is an anode transformer designed to feed the anodes of the high-voltage rectifier valves. The primary winding of transformer Tp2-8 is provided with three taps whose switching enables control of the magnitude of the high-voltage; the winding is fed with 115 V, 400 c.p.s.

Waveguide system with transmit-receive switch (radio-frequency head)

The waveguide system of the transmitter-receiver unit consists of the main waveguide connecting the magnetron oscillator with the antenna waveguide system, and a T-R switch with klystron and mixer chambers.

Radio-frequency head Д-2M meets the following requirements:

1. Minimum losses in the transmitter pulse channel are 0.2 db.

2. Minimum losses in the receiving signal channel are 2 db.

3. Possibility of frequency retuning over the frequency range $f_1 \pm 30$ Mc/s is 0.5 per cent.

The T-R switch employs an ordinary branch circuit.

Description of schematic diagram of radio-frequency head

The schematic diagram is shown in Fig.26.

Simultaneously with the picked-up signal the radio-frequency energy radiated by klystron 3 is applied to the

detector through adjustable slot 1. Detector Д2-2 of the AFC channel is fed with radio-frequency energy from klystron 3 through adjustable slot 2.

Adjustable screws 1 and 2 make it possible to set the required values of the crystal currents.

Inserted in the klystron cell, across the waveguide, is absorbing plate 4 made of graphite-coated pertinax. The plate when set in a right way with reference to the short-circuited walls (5 mm) is the matched load (SWR - 1.3).

Signal detector Д2-1 is thus made to act as a converter producing IF signals which are then furnished to the input of the IF preamplifier.

The radio-frequency head and the IF preamplifier are directly coupled to each other. When the magnetron generates a strong pulse an extremely weakened magnetron signal (60-70 db attenuation) is passed to AFC crystal detector Д2-2 through round hole 5 in the main waveguide and cylindrical waveguide 6 which is, in effect, a cut-off attenuator.

From the output of the AFC crystal detector the IF signal is communicated to the input of the IF preamplifier of the AFC channel and is thus used for the control of the AFC circuit.

A sufficiently large iterative attenuation between the signal detector and AFC detector considerably improves the frequency spectrum of the pulse controlling the AFC circuit, which enhances operational reliability of the AFC system.

Capacitors C1 and C2 (6 to 8 picofarads) are constructional parameters of the crystal holders.

The keep-alive electrode of the TR cell is fed with negative voltage through anti-relaxation resistor R2-37.

The radio-frequency head has the following controls: adjusting screw of TR cell resonance frequency РЗП (8) and crystal current adjusting screws 1 and 2, adjusting screw of ATR cell resonance frequency РБП (7).

IF preamplifier

The schematic diagram of the IF preamplifier is shown in Fig.27.

The purpose of the IF preamplifier is to amplify the IF signal provided the best signal-to-noise ratio is preserved.

The IF preamplifier consists of three stages: the first two stages are a combination of an earthed-cathode triode and grid-earthed triode; the third stage is a normal pentode amplifier operating into a 90-ohm radio-frequency cable.

The IF preamplifier possesses the following advantages:

(1) low noise factor determined by the low noise factor of the first triode;

(2) stable operation, since the first stage has a gain of about unity.

The IF signal is passed from crystal mixer ДГ-С4 (Д2-1) to the grid of valve 6Ж1П (Л2-13) through inductively coupled windings of circuit L2-10 and L2-11.

Coils L2-12 , L2-13, L2-14 and capacitors C2-44, C2-45, C2-46, C2-47 constitute filters in the crystal current circuit.

Capacitors C2-44, C2-45, C2-46, C2-47 decouple this circuit from the high-frequency currents.

The signal amplified substantially in power by valve 6Ж1П (Л2-13) is fed through isolating capacitor C2-50 to the cathode of grid-earthed triode 6Ж1П (Л2-14). The coil in the anode of valve Л2-13 serves as a load.

The first valve Л2-13 is neutralized by inductance coil Л2-16 tuned together with grid-anode capacitance. The use of neutralizing improves the noise factor by 0.25 db. The cathode-anode capacitance of grid-earthed triode Л2-14 is sufficiently large (3.1 picofarads) and is neutralized by inductance coil L2-32 resonating with this capacitance. The signal from the anode of valve Л2-14 is coupled through isolating capacitor C2-55 to the control grid of valve 6Ж1П (Л2-15) of the normal pentode amplifier operating into a

90-ohm radio-frequency cable. The circuit of the valve grid is tuned to a frequency of 33 Mc/s, while the anode circuit of this valve (L2-21), to a frequency of 27 Mc/s.

Resistor R2-43 is the anode load of valve Л2-15. Connected into the anode circuit of valve Л2-15 is coil L2-21 which together with isolating capacitor C2-56 and resistor R2-44 with radio-frequency cable serve to match the IF preamplifier with the main IF amplifier.

The bias voltage on the control grids of valves Л2-13 and Л2-15 is automatic on account of the voltage drop across resistors R2-38 and R2-46. Capacitors C2-48 and C2-58 block these resistors.

The anode circuits of valves Л2-13 and Л2-15 contain filters formed by resistors R2-39, R2-40, R2-42 and blocking capacitors C2-52, C2-53, C2-54. The valve anodes are supplied with 140 V from supply unit Д-8.

The filaments are connected in parallel and fed with 6.3 V A.C. being applied from filament transformer Tp2-9. Chokes L2-17, L2-18, L2-20 with capacitors C2-49, C2-90, C2-57 perform the function of high-frequency decoupling filters in the filament circuits of the valves.

The IF preamplifier has the following characteristics:

1. Amplification factor of IF preamplifier K ≥ 10.
2. Passband at 0.7 level $2\Delta f \geq 6$ Mc/s.
3. Mid-frequency of IF preamplifier $f = 30 \pm 0.5$ Mc/s.

Klystron AFC system

The AFC system of the klystron is designed for automatic control of the klystron frequency so that the intermediate frequency may remain equal to $f_{int} = 30$ Mc/s upon variation of the magnetron frequency.

The AFC and search system consists of an IF amplifier, frequency detector (discriminator), video amplifier, blocking oscillator and control valve (See Fig.28).

The output of the AFC system mixer is applied to the two-stage IF amplifier using valves 6Ж1П (Л2-16, Л2-17).

The amplifier input uses an autotransformer circuit. Coil L2-22 and capacitor C2-64 constitute an input circuit. Resistor R2-47 serves to widen the passband of the input circuit. Coil L2-26 and input capacitance of valve Л2-17,as well as the spurious capacitances, constitute an input filter of the second stage.

Connected into the anode circuit of valve Л2-17 is the primary winding of the frequency detector transformer (L2-27). Coil L2-28 is coupled with coil L2-27 by weak indicative coupling and,capacitively,through C2-71 (See Fig.29). Choke L2-29 serves to pass D.C. of dual diode, 6x6 (Л2-18), and to block A.C. Capacitors C2-75 and C2-76 are blocking capacitors. Their capacitive resistance to IF currents is very small; therefore there is no A.C. component of the potential to chassis on the dual diode cathodes. The output voltage of the frequency detector is taken from the common resistance jack Г2-3 (cathode 8 of Л2-18) consisting of two resistors P2-55 and R2-56 carrying D.C. currents of both halves of dual diode Л2-18 in opposite directions.

From Fig.29 it is seen that each anode of dual diode Л2-18 is fed with alternating voltage consisting of the sum of two voltages; the first voltage is applied from the primary circuit of the transformer through capacitor C2-71, and the second is equal to half, the voltage being developed in the secondary circuit. Since the quality factor of the primary circuit is comparatively small (due to shunting action of resistor R2-52), then near the intermediate frequency the magnitude of the primary circuit voltage being applied to the anodes of valves Л2-18through capacitor C2-71 will not change practically.

The magnitude and phase of the secondary circuit voltage on each of the anodes of diode Л2-18 depend upon the frequency of the input signal.

From Fig.29 it is evident that the primary circuit voltage is supplied in phase through capacitor C2-71 to the anodes of the diode, while the secondary voltage, in anti-phase.

It is well known that at frequency (30 + 0.3) Mc/s near to the resonance one, the resistance of the secondary circuit is actually an active resistance; in this case, the voltage across the secondary winding is shifted in phase by 90° with respect to the primary winding voltage. Based on this the vector diagram of Fig.30 is considered true. The designations on the diagram mean the following:

U_1 - voltage vector of primary circuit;

$\frac{1}{2}U_2'$ - half the vector of secondary circuit voltage applied to anode 3 of diode, 6Х6 (Л2-18);

$\frac{1}{2}U_2''$ - half the vector of secondary circuit voltage applied to anode 5 of diode, 6Х6 (Л2-18);

U_3 - vector of voltage applied to anode 3 of diode;

U_5 - vector of voltage applied to anode 5 of diode.

From the vector diagram it is seen that the voltages applied to anodes 3 and 5 of diode 6Х6 are equal; as a result, the currents of both portions of the dual diode are equal, and as they oppose each other, the output voltage of the frequency detector is zero.

With a frequency exceeding (30 + 0.3) Mc/s the resistance of the secondary circuit is of the inductive nature and the vector diagram will have the form shown in the figure.

From the diagram it is seen that voltages U_3 and U_5 applied to anodes 3 and 5 of diode 6Х6 are unequal and the output voltage of the frequency detector is other than zero.

With a frequency below (30 + 0.3) Mc/s, the resistance of the secondary circuit is of the capacitive nature and the vector diagram will have the form shown in Fig.30,c.

Voltages U_3 and U_5 applied to anodes 3 and 5 of diode 6Х6 are unequal and the output voltage of the frequency detector is other than zero.

From the vector diagrams considered it is seen, that the phase relationships between the voltages of the primary and secondary circuits are such that at frequency (30 + 0.3) Mc/s the resulting voltages on the anodes of the frequency detector are equal, while at a frequency exceeding (30 + 0.3) Mc/s the resulting voltage applied to anode 3 of the left diode increases, and at a frequency below (30 + 0.3) Mc/s the resulting voltage applied to anode 5 of the right diode increases, as well.

Thus, a pulse appears across frequency detector load resistors R2-55, R2-56 the magnitude and sign of which depend upon departure of the intermediate frequency from (30 + 0.3) Mc/s. Since the given AFC circuit is operated by the positive pulse, the characteristic of the frequency detector (discriminator curve, See Fig.31) is asymmetrical. This is achieved by inserting resistor R2-80 in the anode of the left portion of valve Л2-18.

The point where the discriminator curve passes zero differs by 0.3 Mc/s from intermediate frequency 30 Mc/s so that the pulse may be communicated to the control valve (right portion of valve Л2-20 when the intermediate frequency is precisely 30 Mc/s.

The pulses from the frequency detector output are supplied to a two-stage video amplifier - valve Л2-19 (6H8C). Variable resistor R2-57 changing the video amplifier gain serves for setting the required level of the output voltage of the AFC system. Positive pulses from the anode of the right portion of valve Л2-19 are coupled to the grid of the control valve - the right portion of valve Л2-20 (6H8C) operating as a grid detector. These pulses decrease the anode current through the valve, thereby increasing the negative voltage being applied to the klystron repeller. The anode current of the control valve is the smaller, the larger is the amplitude of the positive pulses coming from the anode of the right portion of valve Л2-19, i.e. the larger is the IF drift from the

rated value. In this case, as a result of the negative voltage rise on the repeller, the klystron changes the frequency being generated, owing to which the rated intermediate frequency is obtained. This is the way how the intermediate frequency is maintained constant within the working section of the discriminator curve. If the intermediate frequency changes substantially (beyond the range of the working section of the discriminator curve), positive pulses will not arrive at the control stage.

For this reason the anode current will grow through the right portion of valve Л2-20; this will result in a decrease of the negative voltage on the cathode of the control valve, and on the grid of the cut-off blocking oscillator (left portion of valve Л2-20) coupled to it through chain R2-66, C2-81. The blocking generator starts producing pulses which are taken from cathode 3 of valve Л2-20 and after being differentiated by chain C2-80, R2-64, R2-62 and R2-63 are fed to the grid of the second stage of the video amplifier (right portion of valve Л2-19) replacing the pulses from the frequency detector. Positive pulses are clipped by the grid currents, while negative after amplification are passed from anode 5 of valve Л2-20 in the form of positive pulses to the grid of the control valve. The negative voltage at the AFC output begins increasing changing correspondingly the grid voltage of the blocking oscillator. As the time constant of chain R2-66, C2-81 is large enough, the voltage on grid 1 Л2-20 of the blocking oscillator varies slowly and the voltage at the AFC output has the time to vary substantially (by some dozens of volts). This gives simultaneous rise to the negative potential on the grid of the blocking oscillator and the latter stops oscillating. After the blocking oscillator oscillations are stopped, its pulses are not furnished to the grid of the control valve and the negative voltage at the AFC output starts decreasing until the blocking oscillator is made conductive again, and so on. At the moment the intermediate frequency coincides with its rated value, the AFC circuit is

returned automatically to control operation, since the blocking oscillator is cut off at this moment.

Variable resistors R2-67 and R2-68 determine the operating conditions of valve Л2-20. They can be used for varying the frequency, amplitude and range of the search voltage at the AFC output.

3. Receiver Д-3

Purpose

Unit Д-3 is a component part of the main receiver and sighting receiver of station K-IIM. It amplifies the IF signals produced by the receivers and converts these signals in target and sighting video pulses passing them to the indicators and automatic tracking system of the station. Besides, the unit ensures automatic level control of the picked-up target signals and blocks the receivers at the start of each operation cycle of the station, thereby eliminating the harmful effect produced by the leaking of the main pulse.

Unit composition

According to the functions performed the unit components may be considered under the following headings:

(a) IF amplifier of main channel amplifies the IF signals (30 Mc/s).

(b) Second detector of receiver main channel converts the IF signals into video signals.

(c) Video amplifier of main channel amplifies the target video amplifier to the level required for normal operation of the tracking system and plan position indicators of the station.

(d) Video amplifier of main channel indicators ensures transmission of the target signal to the plan position

indicators of the station and separation of the tracking and plan position indicator video circuits with a view to removing coupling between these circuits.

(e) AGC detector is part of the AGC circuit of the main channel receiver. The circuit produces D.C. voltage proportional to the level of the selected signal.

(f) AGC amplifier ensures obtainment of the required level of the D.C. control voltage in the automatic gain control circuit.

(g) IF amplifier of sighting channel amplifies the IF (40 Mc/s) signals produced by the mixer of the sighting receiver.

(h) Second detector of sighting channel converts the IF signals into sighting video signals.

(i) Video amplifier of sighting channel amplifies the signals to the level required for operation of the sighting indicator of the station.

(j) Disabling pulse multivibrator produces pulses disabling the main channel and sighting receivers when the transmitter fires the main pulse.

Description of schematic diagram

The schematic diagram of the unit is shown in Fig.32. The upper part of the diagram is occupied by the IF amplifier, second detector and video amplifier of the main channel. Shown in the middle are the disabling pulse multivibrator, detector and AGC amplifier. In the lower part of the diagram are shown the IF amplifier, second detector and video amplifier of the sighting channel. From the IF preamplifier of unit Д-2M the IF signal is communicated to the valve grid of the first IF amplifier. Resistor R3-1 is connected for matching the cable and equals its characteristic impedance (91 ohms). All the six stages of the IF amplifier of the main channel connected as an oscillatory circuit into the grid circuit of the next

valve. In this case anode resistors R3-4, R3-7, etc., shunt the circuits ensuring obtainment of the required quality factor. The circuit capacitors of the stages consist of the input and output capacitances, valve capacitance, wiring capacitance, capacitance of the valve sockets and interturn capacitance of coils L3-2, L3-4, etc.

The circuits of the first, third and fifth stages are tuned to frequency (30 + 3) Mc/s. The circuits of the second, fourth and sixth stages are tuned to frequency (30 - 3) Mc/s.

By detuning the tuned circuits with respect to IF it is possible to obtain a 6 Mc/s passband of the entire amplifier.

The negative-going disabling pulse is furnished to the grid of Л3-1 through the high-frequency filter and voltage divider R3-3, R3-2. Capacitor C3-1 prevents short-circuiting of the disabling pulse across the low-resistance circuit of the amplifier input, capacitor C3-2 improving the pulse shape.

Resistor R3-5 serves to obtain automatic bias on the grid of valve Л3-1. Capacitor C3-4 blocks this resistor for high frequency. Filtering circuit R3-6, C3-5 cancels parasitic feedback through the anode circuits of the first and second stages of IF amplifier (valves Л3-1, Л3-2). Capacitor C3-6 protects the control grid of valve Л3-2 against high voltage on the anode of valve Л3-1.

The purpose of similar components of other IF amplifier stages is analogous. Each stage raises the IF signal level approximately six times. The gain of the entire amplifier makes up 35,000.

The gain of the main channel receiver is controlled by feeding the grids of the second (Л3-2) and third (Л3-3) stages with negative voltage (relative to the valve cathodes) from the AGC and MGC circuits. Filters R3-9, C3-7 and R3-13, C3-12 cancel spurious feedback through the gain control circuit.

The amplified signal is detected by the diode detector (Л3-7) whose circuit includes: oscillatory circuit of the sixth IF amplifier stage L3-12, valve 6Ж1П connected as a diode, and load resistor R3-24.

Connection of coil L3-12 into the cathode of valve Л3-7 causes negative-going video pulses to appear across the detector load. Capacitor C3-30 slightly increases the detector conversion factor equal to 0.52. Choke L3-13 prevents penetration of radio-frequency energy obtained during detection to the video amplifiers. Jack 2 is used only when tuning the unit for measuring the amplification factor and taking frequency characteristics of the amplifier.

The video signals produced by the detector are furnished through isolating capacitor C3-31 to the grid of the first video amplifier stage (Л3-8), amplified and passed further to the output cathode follower (Л3-9) employing valve 6Н1П. The anode of valve Л3-8 contains inductance coil L3-14 to improve the video amplifier frequency characteristic in the region of hight frequencies.

Valve Л3-8 operates with a slight bias on the control grid, the bias being built up by automatic voltage drop across resistor R3-25. This provides the required amplification of the negative-going pulse.

Double capacitor C3-32 blocks the anode and cathode circuits of valve Л3-8.

Provision of a cathode follower (Л3-9) in the video amplifier is necessitated by further conveying signals over the low-resistance cable to unit Д-7. The cathode follower enables the amplifier to be matched with the cable due to comparatively low output resistance (approximately 100 ohms). R3-30 is the gridleak of valve Л3-9 and R3-32 is the cathode follower self-bias resistor. Filter R3-31, C3-35 cancels spurious feedback through a +300-V rectifier supplying the amplifier. Test jack 3 is used for passing pulses to the amplifier input when adjusting the unit.

The main channel video amplifier transfers the signal onto the selector stage of unit Д-7 to the input of the video amplifier of the indicators. The video amplifier, which uses valve 6Н1П (Л3-12) in a cathode follower circuit, ensures transmission of the signal over the coaxial cables to units Д-4М, Д-5 and separates video channels of the tracking system and indicators with a view to removing coupling between these channels.

In the diagram C3-47 is the isolating input capacitor; R3-49, the gridleak of valve Л3-12; C3-48, R3-50, the decoupling anode filter; R3-51, the self-bias resistor and C3-46, the isolating output capacitor.

The automatic gain control AGC circuit of the main channel receiver consists of an AGC detector (left portion of valve Л3-13) and AGC amplifier (right portion of valve Л3-13). The anode circuits of the AGC detector and AGC amplifier are supplied by the rectifier developing a negative voltage of -255 V relative to the unit chassis. This provides obtainment of the AGC negative voltage controlling the IF amplifier gain. The left portion of valve Л3-13 operates as an anode detector with load R3-54 connected to the valve cathode. Negative bias to the detector grid is supplied from voltage divider R3-53, R3-55, R3-59 for obtaining the AGC delay.

The AGC delay is controlled by potentiometer R3-53. Owing to the delay the AGC circuit starts functioning at a certain level of the received signal and is disconnected automatically in case of weak signals. The target signal of positive polarity is coupled to the input of the AGC detector through capacitor C3-54, from the selector of unit Д-7. Across R3-54, C3-50 the D.C. component of the detected signal is separated and through resistor R3-58 is passed to the control grid of the AGC amplifier. The amplified D.C. voltage is separated across resistor R3-56, filtered by R3-57, C3-51 and is applied to the valve grids of the second and third stages of the main channel IF amplifier. To this circuit may

be connected a voltage divider located on the control panel of the station when manual gain control is resorted to. Connection of the MGC paralyses the AGC action.

This is because the low-resistance MGC potentiometer shunts high resistance AGC circuits (R3-57). The time constant of the AGC circuits determines the receiver ability to keep constant the level of video pulses at the output at various rates of changing the picked-up signals. The time constant in the circuit under consideration is mainly determined by the values of R3-54, C3-50 and is taken equal to 0.25 sec.

With these characteristics the receiver AGC does not demodulate the signal whose level changes due to scanning of the antenna dipole at frequency Ð c.p.s. and slowly varies the signal power conditioned by interference. The AGC range is 60 db at the 22-V signal level at the receiver output.

The IF amplifier of the sighting channel differs from that of the main channel only in electrical characteristics. It is also composed of six single IF amplification stages employing miniature pentodes 6Ж1П (Л3-14, Л3-15, Л3-16, Л3-17, Л3-18, Л3-19) with tuned circuits connected into the valve grids of the subsequent valves. The circuits of the first and fourth stages are tuned to frequency (40 + 5) Mc/s; the circuits of the second and fifth stages are tuned to frequency (40 - 5) Mc/s, the circuits of the third and sixth stages are tuned to 40 Mc/s. The adopted system of tuning the circuits to three different frequencies ensures a passband of the entire amplifier equal to 10 Mc/s and amplification factor to 7000.

The IF amplifier of the sighting channel is fed with signals from the IF preamplifier of unit Д-16 through a coaxial cable. The sighting video signals are discriminated by the diode detector Л3-20, which passes them to the cathode follower (Л3-21 and Л3-22).

The detectors and video amplifier of the sighting channel are similar to those discussed above. The conversion

factor of the detector is 0.7 and amplification factor of the video amplifier is 15. The start-stop multivibrator (Л3-10) is triggered by positive pulses leading the main pulses by 1 microsec. This improves suppression of noise from the main pulse on account of radar time displacements of the main and suppressor pulses.

Before arrival of the trigger pulse the right triode Л3-10 is made conducting by +300 V being supplied to the valve grid through resistor R3-43. The anode current of this triode sets up a voltage drop across R3-41 driving the left triode to cut-off. When a positive pulse arrives at the grid of the left portion of valve Л3-10, a negative pulse cutting off the right triode through C3-42 appears on its anode. In this case the voltage drop across R3-41 becomes equal to zero and the left triode begins to conduct.

The right triode will be cut off until capacitor C3-42 discharges through resistor R3-43 to a potential approximately equal to the anode of the open left portion of valve Л3-10. After capacitor C3-42 has discharged the circuit comes back to the initial state, i.e., the right triode becomes conducting and the left triode is driven to cut-off.

The voltages in the circuit drop in an avalanche-like manner because of positive feedback between the stages. Owing to this condition the circuit produces disabling square pulses. The time constant of R3-43, C3-42 is so chosen that the length of the disabling pulse is approximately 0.5 microsec., which ensures overall covering of the noise pulse.

Potentiometer R3-38 makes it possible to change the trigger level of the left triode and thereby vary the duration of the disabling pulse when aligning the radar. From the anode of valve Л3-10 the disabling pulse is furnished through isolating capacitor C3-43 to the grid of valve Л3-11(6Ж1П).

This valve improves the shape of the disabling pulse by clipping the positive peak, limits the amplitude of the disabling pulse and reverses its phase. Valve Л3-11 operates

with a large negative bias on the control grid produced by the voltage divider R3-48, R3-47, R3-46 - the anode load resistance of valve Л3-11, R3-45, C3-44 - the decoupling anode filter.

From the anode of valve Л3-11 the disabling pulse is passed to the grids of the first IF stages of the main and sighting channels through a RF filter.

The unit supply: the filament circuits of the valves are fed by transformer Tp3-1 incorporated in the unit; in this case, valve Л3-13 is connected to an individual unearthed winding.

The anode circuits of both IF amplifiers are fed with unstabilized voltage of +140 V; the output cathode followers and disabling pulse multivibrator are fed by the +300-V rectifier.

The unit consumption: 0.45 A from 117 V, 400 c.p.s. supply source
-30 mA from +300-V rectifier
-150 mA from +140-V rectifier
-3 mA from -225-V rectifier.

4. Sweep Unit Л-4М

Purpose

The sweep unit is designed to:

(a) Shape the saw-toothed current feeding the rotating coils of the search indicator.

(b) Square the search sweep brightening pulse.

(c) Shape the trigger pulse of the search sweep generator, when the unit is triggered by external pulses.

(d) Obtain calibration range markers used for determining the target range.

(e) Mix signals: target, range mark, heading marker, calibration range markers, and present these signals for intensity modulation of the search indicators when sweep is on its forward stroke.

(f) Mix signals: reply signals, target selection pulse and calibration markers for amplitude modulation of A-display of the sighting indicator.

(g) Shape the saw-toothed sweep voltage of the target tracking indicator.

(h) Form positive square-wave voltage of sweep brightening on the screen of the target tracking indicator.

The unit consists of five component parts designed as three individual subpanels.

Oscillator and search sweep amplifier comprise the 1st subpanel (left); marker oscillator and search channel mixer form the 2nd subpanel (middle).

Sighting indicator mixer, saw-toothed voltage forming channel of the tracking indicator and shaping stage for triggering search sweep generator from external sources make up the 3rd subpanel (right).

Description of functional and schematic diagrams of sweep unit

The diagram includes twenty-five elements of which:

1. Forming stage, 2. Buffer amplifier, 3. Sweep limiter, 4. Multivibrator , 5. Saw-toothed wave generator, 6. Saw-toothed voltage amplifier of operator's indicator, 7. Saw-toothed voltage amplifier constitute the search sweep channel.

8. Buffer amplifier, 9. Forming stage, 10. Oscillator, 11. Blocking oscillator, frequency divider of calibration range markers constitute the calibration marker oscillator.

12. Calibration marker cathode follower, 13. Range marker cathode follower, 14. Video amplifier, 15. Output stage constitute the mixer.

16. Calibration marker amplifier, 17. Calibration marker cathode follower, 18. Target selection pulse cathode follower, 19. Reply signal cathode follower constitute the mixer of the sighting indicator.

20. Trigger pulse cathode follower, 21. Multivibrator, 22. Saw-toothed wave generator, 23. Cathode follower, 24. Brightening pulse cathode follower of the tracking indicator constitute the saw-toothed voltage channel of the tracking indicator.

The schematic diagram of the unit is shown in Fig.34.

The sweep unit consists of five component parts:

1. Oscillator and search sweep amplifier.
2. Calibration marker oscillator.
3. Mixer.
4. Mixer of sighting indicator.
5. Saw-toothed wave generator of tracking indicator.

Oscillator and search sweep amplifier

The unit is fed from unit Д-7 (resistor R7-166 of valve Л7-9) with positive-going, short-time pulses of 40 - 45 V amplitude through cable 28. These pulses trigger the search sweep circuit.

The search sweep circuit consists of a buffer amplifier, sweep trigger multivibrator, sweep limiter or quenching valve, sweep generator , sweep amplifier and diode restorer. The block diagram of the oscillator and search sweep amplifier is shown in Fig.35. An additional element of the search sweep channel is the forming stage to trigger the sweep from external sources, which is switched on when changing for checking the station operation.

Sweep trigger forming stage

The forming stage serves to trigger the search sweep channel by positive and negative pulses of some 15 V from external sources. In this case, switch B4-2 must be set at CHECK.

In view of the majority of the pulses used for external triggering having large parasitic peaks, the right half of

valve Л4-15 is driven to "minimum" limiting, for which purpose its grid is biased negatively. This clips the peaks by an amplitude of 10 - 12 V.

If triggering is effected by a positive pulse, the pulse is furnished from jack $Г_4$-1 to control grid 1 through capacitor C_4-41 and is picked off negative from anode 2 through capacitor C_4-44 to grid 4.

If the trigger pulse is negative, it is supplied from jack $Г_4$-6 to cathode 3.

The other half of valve Л4-15 (grid 4, anode 5, cathode 6) amplifies the trigger pulse to 40 V and turns it over. The pulse is fed to trigger the sweep of the search channel.

Buffer amplifier

The first portion of valve Л4-1 (6Н9С) is the buffer amplifier whose grid is fed with positive trigger pulses: the amplifier cathode is kept at some +12 V. This voltage is taken from a divider formed by R4-2 and R4-3. With such a bias the valve is driven to cut-off. The positive trigger pulse being applied to the grid is greater in amplitude than the bias, and the valve is driven into conduction. The anode potential drops sharply. As a result, a negative pulse is obtained at the stage output. The shape and polarity of the input and output pulses is shown in the block diagram of the oscillator and sweep amplifier (Fig.35).

Sweep trigger multivibrator

The negative pulses from the buffer amplifier start the sweep trigger multivibrator. The simplified diagram of this multivibrator is shown in Fig.36.

The multivibrator employs two triodes of valve Л4-2 (6Н8С). Before arrival of the trigger pulse the second triode of the valve (grid 4, anode 5, cathode 6) is conducting, since

grid 4 is connected to +300-V supply through resistor R4-11, which causes the appearance of the grid current. The cathode resistor carries a current large enough to raise the cathode potential with respect to the grid potential. That is why the triode operates near the zero bias.

The first triode of valve Л4-2 (grid 1, anode 2, cathode 3) is cut off. Its grid is fed with a positive bias of about +14 V (the voltage drop caused by +300-V supply across resistors R4-67 and R4-4); at the same time a large negative bias is fed from resistors R4-97, R4-9 and R4-10 through which the anode current of the second triode flows.

Upon arrival of the negative pulse from the buffer amplifier, the anode current of the second triode stops flowing through resistors R4-97, R4-9 and R4-10, and the second triode is cut off. As a result, the negative bias is taken from grid 1, the first triode begins conducting. The anode potential of the first triode drops and the resulting negative pulse on grid 4 keeps the second triode cut-off. The second triode might be open upon discharge of capacitor C4-3. The capacitor discharge depends on the time constant of RC (C4-3 and R4-11), but the triode is driven into conduction before capacitor C4-3 manages to discharge. This is because grid 1 is fed with a strong "finalling" negative pulse from the sweep limiter (auto-quenching valve).

The anode potential of the first triode rises, grid 4 receives sufficient positive bias and the second triode starts conducting.

Thus, the trigger multivibrator shapes a negative square pulse (on the anode of the first triode), whose beginning is time-coincident with that of the trigger pulse, and the end is determined by the pulse from the sweep limiter. The waveform and polarity of the pulses are shown in Figs 36 and 37.

Sweep limiter (auto-quenching valve)

The simplified circuit diagram of the sweep limiter is presented in Fig.37.

Valve Л4-1 (grid 4, anode 5, cathode 6) of the sweep limiter is started by a positive saw-toothed pulse, taken from feedback resistors R4-24 and R4-25 of the sweep amplifier.

Before arrival of this pulse the triode is cut off by the bias voltage (on the triode cathode) taken from resistor R4-9. As the positive saw-toothed pulse arrives from the feedback resistors of the sweep amplifier, the potential of grid 4 rises. When it exceeds the positive bias on the cathode, the triode becomes conductive.

The trigger potential is set by means of rheostat R4-9. When the triode is triggered, its potential on anode 5 drops and cuts off (stops) the first triode (grid 1, anode 2, cathode 3) of the sweep trigger multivibrator. As a consequence, the triode (sweep limiter valve) is cut off momentarily, for the trigger multivibrator will stop the saw-toothed wave generator and this will restore the initial negative bias on grid 4 of the triode with respect to cathode 6.

Sweep generator

The sweep generator valve Л4-3 (6Н9) has no cathode load. Its grid is coupled to the unit chassis through resistor R4-12. This valve is a well-conducting valve. At the moment its grid is fed with a negative pulse, the valve gets cut off, the anode potential begins rising at the rate depending upon the rate of charge of capacitor C4-5 through one of the sweep rate-determining resistors R11-37, R11-58, R11-39, R11-57 located in the control panel. By operating the range selector on the control panel resistance is selected (for each range) of such a value that the RC time constant may produce the wanted rate of sweep. Upon cessation of the negative square

pulse valve Л4-3 becomes triggered again and its anode potential falls sharply. As a result, a saw-toothed voltage is obtained, which is fed to the grids of the sweep amplifiers of the operator's and remote indicators.

The output voltage on the screen grid of the sweep generator has the form of positive square pulses. They are obtained, in this case, due to absence of the earth-shunting capacitor. The pulses are used for brightening the search sweep trace.

The sweep generator, the waveform and polarity of its pulses are shown in Fig.38.

Sweep amplifier

The saw-toothed voltage from the sweep generator is fed to two similar three-stage amplifiers located one in the operator's indicator, the other - in the remote indicator. The simplified circuit diagram of the sweep amplifier of the operator's indicator is presented in Fig.39 (this amplifier is under consideration).

The first stage employs the first triode of valve Л4-4 (grid 1, anode 2, cathode 3) and is actually a normal voltage amplifier. The triode grid is directly coupled to the anode of the sweep generator. This results in appearance of positive bias on the grid, but the anode potential of valve Л4-3 is so low between the sweep pulses that the bias from resistor R4-23 turns to be sufficient for keeping this grid at a low negative potential.

When the sweep is on its forward stroke, the potential of cathode 3 of valve Л4-4 rises maintaining a negative bias on grid 1.

The sweep amplitude is varied by the rheostat (R4-25) in the cathode circuit entitled AMPLITUDE OF OPERATOR'S INDICATOR SWEEP (АМПЛИТУДА РАЗВЕРТКИ ИНДИКАТОРА ОПЕРАТОРА).

The first stage (valve Л4-4 of the sweep amplifier operates as a variable-gain amplifier.

Setting of rheostat R4-25 has no effect on the amplitude of the voltage being applied to the sweep limiter.

If, for instance, the value of R4-25 is decreased, the gain of the sweep amplifier increases due to a decrease of negative feedback. Resistor R4-25 handles greater current and the voltage drop across the resistor remains invariable. This means that the steepness of the sweeping voltage and, consequently, the amplitude can be changed without changing the duration of the saw-toothed voltage.

The second stage of the sweep amplifier employs the second triode of valve Л4-4 (grid 4, anode 5, cathode 6). It has no resistor in the cathode circuit and operates as a normal amplifier with the anode load and gridleak for partial D.C. restoration. Capacitor C4-9 blocks the D.C. component of the saw-toothed voltage.

The remaining A.C. component is applied to the grid and produces equal areas above and below the mean value of the zero potential line. During the resting time the grid starts acquiring a negative potential, but the grid current that has appeared passes through resistor R4-19 and feeds negative bias to the grid.

During the resting time the capacitor discharges slightly, which is conditioned by the time constant of resistor R4-19 and capacitor C4-9. This provides for operation of the second triode of valve Л4-4 irrespective of the amplitude of the sweep pulses or duration of intervals between them.

The third (output) stage of the sweep amplifier employs valve Л4-8.

Its cathode is earthed through deflecting coil of the indicator and resistor in the cathode circuit of the first stage. The anode current of the last stage builds up a voltage in the cathode circuit of the first stage of the polarity, which causes increase of bias in the first stage. The injected

negative bias stabilizes the gain and linearity of the sweep.

Before the sweep starts valve Л4-8 is cut off, since it has a bias 27 V less than the cut-off voltage. The amplifier (valve Л4-4) operates without negative feedback and gives a high gain. Owing to this the saw-toothed voltage becomes steeper, the sweep speeds up considerably and the loss of time required for raising the potential from the value at the cut-off valve to the value at the open valve is only 0.5 microsec. After the valve is open the negative feedback reduces the overall gain. The sweep rate decreases the value required for the distance range used.

Action of the negative feedback is shown in Fig.40.

Section AB is below the cut-off bias of valve Л4-8 and is amplified greatly by the two first stages.

Section BC is the operating section of the sweep at which a current flows through the deflecting coils. This develops a negative feedback decreasing the gain. This section is less steep than section AB.

After every pulse, the current through the sweep coils must be zero. This is necessary to allow the spot to return to the screen centre before commencement of the next sweep pulse. If there is current during the resting time a circle is obtained in place of the bright spot in the screen centre, i.e., the start of the sweep is off-centred.

To prevent direct current between the sweep pulses a constant bias of - 52 V on valve Л4-8 is sufficient.

To eliminate jumping of range marks on the remote indicator, potentiometer R4-20 is introduced into the second stage of the search sweep amplifier.

Diode restorer

Placed between the second and the final stages of the sweep amplifier is diode Л4-6. Its function is to restore the D.C. component on the grid of the output stage. This is

necessary to make the instantaneous value of the grid potential of valve Л4-3 dependent only upon the impressed saw-toothed voltage and independent of the waveform of this voltage.

If this is not so the amplitude of the sweep on the indicators would vary with the range scale used. The functioning of the diode restorer is shown in Fig.41.

The left part of the illustration shows the case when there is no diode restorer before the output stage. Two cases are referred to when the grid of the output stage is fed with saw-toothed voltage with equal amplitude of 40 V, but with different lengths of the saw-toothed wave.

Grid 5 of valve Л4-7 and Л4-8 is fed with bias voltage of 72 V from the -225 V supply via voltage divider R4-39 and R4-38.

When the saw-toothed voltage passes through the coupling circuit (C4-12, R4-34, R4-35, R4-38) not containing diode Л4-6 to the grid of valve Л4-8, it will be displaced with respect to the fixed bias level of 72 V so that the saw-toothed areas above and below the bias level line are equal. However, in consequence of different lengths, the saw-toothed wave amplitude will vary, in one case, from -82 V to -42 V, and, in the other case, from - 76 V to -36 V, i.e., in one case, the saw-toothed wave amplitude with respect to the 72-V level is 30 V, and in the other case is 36 V. This means that the operating point of valve Л4-8 will be displaced depending on the duration of the saw-toothed voltage and the sweep on the PPI screen will not start from the same point at various range scales, while the sweep amplitude will be the larger the shorter the sweep duration and vice versa.

To prevent displacement of the operating points of valves Л4-7 and Л4-8 at various range scales, use is made of the diodes of valve Л4-6 designed for D.C. restoration on the grids of valves Л4-7 and Л4-8.

The operating principle of the diode consists in the following: passage of the saw-toothed voltage causes capacitor

C4-12 to change to a potential determined by the duration of the saw-toothed wave and time constant of the RC circuit formed by C4-12, R4-35, R4-36, R4-38, R4-27. In this case, the time constant of the RC circuit is large.

Upon cessation of the saw-toothed voltage, a negative voltage corresponding to the charge of capacitor C4-12 will be applied to the diode cathode. This results from the potential difference between the capacitor plates not being in position to change instantly. Diode 6X6C will be driven into conduction and capacitor C4-12 will discharge through the diode over the circuit with a smaller time constant. Thus, the initial level of bias voltage equal to -72 V will be set on the grid of the output valve by the beginning of the next saw-toothed wave.

So, with any duty cycle of the saw-toothed wave, the bias level of output valve Л4-8 is maintained constant.

Calibration marker generator

The marker generator produces markers appearing on the indicator screen 10, 20 or 40 km. apart depending upon the position of knob RANGE, KM. (ДАЛЬНОСТЬ, КМ) on the front of the control panel.

The first marker appears at the moment when the trigger pulse arrives, the second, third, fourth, fifth and sixth markers (the fifth interval between the markers) will settle in line with the duration of the negative square pulse at the input of the calibration marker generator channel.

Owing to the sweep the range markers on the indicator screens look like bright circles. The latter are used as distance scales when range is to be read off.

The calibration marker generator circuit begins at divider R4-6 and R4-7 and ends at grid 1 of mixer valve Л4-11.

The calibration marker generator consists of a buffer amplifier, forming stage, sine-wave oscillator and blocking

oscillator with frequency division circuits. Fig.42 shows a simplified circuit of the calibration marker generator.

The negative square trigger pulse is communicated through capacitor C4-15 to control grid 1 of valve Л4-9.

Before arrival of the trigger pulse the first triode of valve Л4-9 (grid 1, anode 2, cathode 3) is conducting, since there is no bias on its grid. Therefore, the potential on anode 5 of valve Л4-9 and grid 1 of valve Л4-10 is low due to a voltage drop when the anode current passes through resistor R4-43. In this case, grid 1 of valve Л4-10 is fed with a voltage of about +90 V, and cathode 3 with some +160 V from a voltage divider formed by resistors R4-49, R4-51, R4-52. Therefore, grid 1 of valve Л4-10 is biased to about -50 V with respect to cathode - the triode is cut off. Grid 4 of valve Л4-9 is zero-biased with respect to cathode, since it is fed with +29 V from the +300-V supply via divider R4-42 and R4-44. +29 V is taken via divider R4-49, R4-51 and R4-52 from the 300-V supply. Thus, the triode of valve Л4-9 (grid 4, anode 5, cathode 6) is open.

As the negative trigger pulse arrives, the triode of valve Л4-9 (grid 1, anode 2, cathode 3) is cut off. The potential of anode 5 of valve Л4-9 and that of grid 1 of valve Л4-10 increases, the first triode of valve Л4-10 (grid 1, anode 2, cathode 3) begins to conduct, and the circuit generates oscillations. The oscillation frequency in the circuit is determined by the parameters of the resonance circuit connected between the cathodes of the second triode of valve Л4-9 and first triode of valve Л4-10.

The marker frequency produced by the oscillatory circuit is 14.984 Mc/s, which corresponds to a 10-km. range. The frequencies of 7.492 Mc/s - 20 km. and 3.746 Mc/s - 40 km. are obtained on account of the frequency of the 10-km. markers divided by the blocking oscillator - the second triode of valve Л4-10 (grid 4, anode 5, cathode 6).

The oscillations amplified by the first portion of valve

Л4-10 are taken from the secondary winding of the pulse transformer to the grid of the blocking oscillator - the second portion of valve Л4-10.

The frequency of the 10-km. markers are divided due to the blocking process in valve Л4-10. In this case on 10- and 50-km. sweep scales the blocking oscillator is operated by every positive pulse on its grid.

On the 100-km. sweep scale, relay $P_{\text{Л}}$4-1 is energized, which switches over the blocking oscillator discharge circuit C4-22, R4-45 to C4-22, R4-46, R4-70. The frequency is halved, i.e., valve Л4-10 (second triode) opens only when acted on by every second pulse from the oscillatory circuit.

On the 200-km. sweep scale relays $P_{\text{Л}}$4-1 and $P_{\text{Л}}$4-2 are energized. These relays connect discharge circuit C4-22, R4-125, R4-71 to the blocking oscillator. In this case, the frequency of the oscillatory circuit is divided by four, i.e., the valve is driven to cut-off only when acted on by every fourth pulse of the oscillatory circuit. Resistors R4-70 and R4-71 are designed for adjusting the discharge circuits on the 100- and 200-km. sweep scales. Through the second group of relay $P_{\text{Л}}$4-1 on the 10- and 50-km. sweep scales a cut-off voltage of -225 V is supplied to the control grid of valve Л5-12 of unit Д-5.

On the 100- and 200-km. scales the circuit between the control grid of valve Л5-12 and the -255-V supply is broken and the cut-off voltage is not supplied, as a result.

Mixer

The mixer is designed for mixing video signals, calibration range markers, heading markers, range markers and presenting these signals over the video channel of units Д-5 and Д-6M when the sweep is on its forward stroke.

The mixer consists of the following components:

1. Video limiter - the second triode of valve Л4-12 (grid 4, anode [illegible], cathode 6).

2. Cathode follower (calibration range marker channel) - the first triode of valve Л4-11 (grid 1, anode 2, cathode 3).

3. Cathode follower (range marker channel) - the second triode of valve Л4-11.

4. Cathode output stage - valve Л4-13).

Video limiter

The video limiter (the second triode of valve Л4-12) starts functioning when the video pulse builds up bias on cathode 6 equal to the valve cut-off bias.

From anode 5 video signals are passed to the grid of valve Л4-13 through capacitor C4-33.

Cathode follower (calibration range marker channel)

Calibration range marker signals are applied to the first triode of valve Л4-11, followed in cathode 6, picked off resistor R4-60 common for the three stages of the mixer, to grid 4 of valve Л4-13. Resistor R4-54 serves to control the amplitude of the calibration markers by varying the negative bias on the cathode-follower grid.

Cathode follower (range marker channel)

The range marker signal is applied to grid 4 of the second triode of valve Л4-11 through capacitor C4-27. Then it is followed in cathode 6, taken from resistor R4-60 common for the three mixer stages and is communicated to grid 4 of the cathode follower (valve Л4-13).

The range marker brightness is controlled by means of potentiometer R4-126.

Cathode follower (Л4-13)

All the signals mixed are applied to the grid of valve Л4-13.

The valve cathode is fed with a heading marker. The circuit handling the heading marker is shown in Fig.44.

The low-resistance output of this stage provides a low shunting effect of distributing capacitance of the cables running to the indicators (Д-5 and Д-6М).

The simplified circuit diagram of the mixer with the output stage is presented in Fig.43.

Sighting indicator mixer

The mixer is designed to mix calibration range markers, missile reply signals, selected target pulses and to transmit these signals to unit Д-5 when the sweep of the sighting indicator is on its forward stroke. The mixer of the sighting indicator consists of the following components:

1. Calibration marker amplifier - the first triode of valve Л4-14 (grid 1, anode 2, cathode 3).
2. Calibration marker cathode follower - the second triode of valve Л4-14 (grid 4, anode 5, cathode 6).
3. Selected target pulse cathode follower - the first triode of valve Л4-16 (grid 1, anode 2, cathode 3).
4. Missile reply signal cathode follower - the second triode of valve Л4-16 (grid 4, anode 5, cathode 6).

The simplified circuit diagram of the sighting indicator mixer is shown in Fig.45.

Calibration range marker amplifier

Before arrival of positive calibration range marker pulses at grid 1 of valve Л4-14 the amplifier control grid is biased to about 12 V due to the voltage drop across resistor R4-74 in the cathode of the triode resulting from the passage of the anode current through the valve.

As the calibration marker pulses arrive at the grid, the first triode of valve Л4-14 opens sharply and the voltage on its anode 2 drops abruptly. Negative calibration marker pulses

are passed through blocking capacitor C4-38 to grid 4 of valve Л4-14 (calibration marker cathode follower).

Upon cessation of the calibration marker, the first triode of valve Л4-14 comes back to its initial state.

The process is repeated with the arrival of the next calibration marker pulse.

Calibration range marker cathode follower

The calibration range marker cathode follower serves for transmission of signal and separation of the calibration range marker amplifier circuits and video channel of the sighting indicator.

The negative calibration range marker pulses come to grid 4 of valve Л4-14, followed in cathode 6 across resistor R4-78 and are passed to unit Д-5 over coaxial cable 12.

Selected target pulse cathode follower

This cathode follower serves for transmission of signals and separation of the circuits of unit Д-7 and unit Д-5.

The positive selected target pulse of about 25-V amplitude is applied to grid 1 of valve Л4-16, followed in cathode 6 across resistor R4-78 and is passed to unit Д-5 over coaxial cable 12.

Missile reply signal cathode follower

This cathode follower serves for transmission of signals and separation of the circuits of unit Д-3 and unit Д-5.

The positive missile reply signal of about 30-V amplitude is coupled to grid 4 of valve Л4-16, followed in cathode 6 across resistor R4-78 and is then furnished to unit Д-5 over coaxial cable 12.

Saw-toothed wave generator of tracking indicator

The saw-toothed wave generator is intended for producing the sweep on the tracking indicator of unit Д-5.

The tracking sweep generator consists of the following components:

1. Trigger pulse buffer stage (cathode follower – the second triode of valve Л4-17).
2. Driven multivibrator (Л4-18).
3. Saw-toothed wave generator (the first triode of valve Л4-19) and output signal cathode follower (the second triode of valve Л4-19).
4. Square brightening pulse cathode follower (the first triode of valve Л4-17).

The simplified circuit diagram of the saw-toothed wave generator of the tracking indicator is shown in Fig.46.

Buffer stage

The trigger pulse buffer stage employs the second triode of valve Л4-17 (grid 4, anode 5, cathode 6).

This stage behaves as a cathode follower. It separates the trigger circuit of unit Д-7 from the sweep circuits of the tracking indicator of unit Д-4M.

Driven multivibrator

The driven multivibrator employs valve 6H8C. It functions in the same way as the multivibrator in the search sweep channel (valve Л4-2), the only difference being that the duration of the square pulse triggering the sweep generator is determined by the parameters of the multivibrator, by the discharge time constant of capacitor C4-56 in particular, rather than by an external turn-over pulse.

Saw-toothed wave generator and output signal cathode follower

The saw-toothed wave generator circuit is based on stabilization of the charging current by injecting the compensating EMF. Valve Л4-19, the first triode (grid 1, anode 2, cathode 3), performs the function of the generator proper, the second triode - cathode follower, conveying the output saw-toothed voltage to the second end of the discharge resistor (R4-109), thereby making the charging current constant.

This results in a linear saw-toothed voltage of a large amplitude at the generator output.

Resistor R4-110 forms a pedestal of saw-toothed voltage which compensates for the effect produced by the cable capacitance (about 300 to 850 pF), thereby providing linearity of the saw-toothed voltage at the beginning of the sweep.

Brightening pulse cathode follower

The brightening pulse cathode follower employs the first triode of valve Л4-17. The positive pulse from anode 5 of valve Л4-18 is applied to the cathode follower(half of valve Л4-17).

From the output of the cathode follower the positive brightening pulse (blanking signal ' ') is coupled through capacitor C4-53 to the tracking indicator of unit Д-5.

Current consumption

The current drawn by the unit from the power supplies must be:

(a) not more than 0.9 A from 115 V, 400 c.p.s. supply;

(b) not more than 100 mA from +350 V unstabilized voltage supply;

(c) not more than 110 mA from +300 V unstabilized D.C. voltage supply;

(d) not more than 35 mA from +300 V stabilized voltage supply.

5. Indicator Unit И-5

The indicator unit used in radar K-IIM is intended for viewing the position of targets in space scanned by the radar antenna. With regard to the purpose and operating modes of the radar, the indicator unit is comprised of three individual indicators. These are as follows:

(a) plan position indicator;

(b) tracking indicator;

(c) sighting indicator.

The plan position indicator is intended:

(a) to determine the range of a target or a group of targets;

(b) to determine the azimuth of a target or a group of targets;

(c) to determine the aircraft heading.

This indicator displays a circular sweep. The echo signal voltage is applied to the control grid of the cathode-ray tube. This increases the electron beam intensity during the pulse time. The result is that a bright spot indicating the azimuth of and distance to the target appears on the indicator screen.

The tracking indicator in radar K-IIM is intended for accurate target selection and viewing of automatic tracking of the selected target and targets within the field of vision of the antenna.

The tracking indicator has the L-display. This display is a modification of the A-display where two pulses reflected from the target are presented on the screen as peaks looking in opposite directions.

Schematic diagram of indicator unit Д-5

The schematic diagram of the indicator unit presents the channels of three indicators (Fig.47); therefore, the diagram will be analysed separately, channel by channel.

Plan position indicator channel

This consists of cathode-ray tube Л5-2, type 13ЛМ31, brightening pulse shaper and video amplifier - valve Л5-1, type 6Н8С.

The equipment of the cathode-ray tube consists of:

(a) Rotary deflection coil L5-2.

(b) Selsyn motor M5-1, type СМС-1, rotating deflecting coil L5-2.

(c) Focusing permanent magnet with adjustable shunt.

(d) Centring system for vertical or horizontal displacement of the electron beam on the screen of the cathode-ray tube.

(e) Phasing contacts for synchronizing the deflection coil rotation with the antenna.

For protection of the cathode-ray tube against the effects produced by the external magnetic fields, its envelope is enclosed in a special screen.

The deflecting saw-toothed current is applied to deflection coil L5-2 from sweep unit Д4М.

The coil is coupled through selsyns to the gear rotating the deflection coil of the plan position indicator. The radial sweep trace rotates in synchronism with the antenna, and its direction at any time corresponds to the antenna direction.

High voltage to feed the cathode-ray tube is produced by the high-voltage rectifier located in unit Д25.

The tube brightness is controlled by varying the positive voltage on its cathode taken from potentiometer R5-9.

Resistors R5-106 and R5-10 determine the maximum (R5-10) and minimum (R5-106) brilliance of the tube.

The video signal which is actually a mixed signal composed of calibration markers, range markers, heading markers and echoes, is applied from the sweep unit to the cathode of the second triode of the search video amplifier (half of valve Л5-1), type 6H8C (See Fig.48).

RF correction is effected by coil L5-1, and AF correction, by resistor R5-7 and capacitor C5-3. The width of the amplifier passband is about 0.1 to 1.8 Mc/s. From the amplifier output (contact 5) the positive-going video signal is communicated to the grid of the cathode-ray tube. The cathode of the first triode of valve Л5-1 is fed from the sweep unit with the sweep trace brightening pulse which from the anode of this triode is passed to the first anode of the cathode-ray tube (contact 3), and from the divider formed by resistor R5-77, to the grid of cathode-ray tube Л5-14 (contact 3).

When the sweep length is varied from 335, 670 and 1340 microsec. the beam brightness of tube Л5-2 is almost invariable. When changing over to a sweep of 67 microsec. duration the beam brightness decreases more noticeably on account of the screen illumination time being reduced.

Tracking indicator channel

This consists of:

(a) Cathode-ray tube Л5-9, type 8Л029.

(b) Saw-toothed voltage buffer amplifier - valve Л5-8, type 6H8C.

(c) Tracking sweep brightening pulse buffer amplifier - half of valve Л5-3, type 6H9C.

(d) Switching video amplifier - valves Л5-4, Л5-6, type 6Ж4.

(e) Switching stage - valve Л5-5, type 6H9C.

(f) Brightening pulse shaper - valve Л5-10, half of valve Л5-3, type 6H8C, and half of valve 6H9C.

(g) Mixer - valve Л5-11, type 6H8C.

The cathode-ray tube (Л5-9) presents the L-display to find deviation of the target being tracked from the axis of the equisignal zone (See Fig.51). The duration of the tracking indicator sweep is 10 km. The start of the sweep may be displaced throughout the entire range of the radar. The sweep generator located in the sweep unit feeds unit Д-5 with positive saw-toothed voltage pulses (13th contact of connector ШВ5-1) and sweep trace brightening pulses (12th contact of connector ШВ5-1) at frequency n_2.

The saw-toothed voltage pulse is applied to plate Д-4 of tube Л5-9. Part of this voltage is fed to the grid of the buffer phase inverter amplifier with voltage negative feedback (capacitor C5-12) - valve Л5-8 (See Fig.49).

This amplifier employs two paralleled triodes, type 6Н8С; in addition to the divider (resistors R5-103, R5-46) the input contains correcting circuit C5-11, R5-103 which improves the linearity of the saw-toothed voltage. The negative saw-toothed voltage pulse having approximately the same amplitude as the positive pulse (200 V ± 10 per cent) is passed from the output of the buffer amplifier (resistor R5-44) to deflection plate Д-3 of tube Л5-9.

Thus, symmetrical deflection in range is effected.

The sweep trace brightening pulse coming from the sweep unit is applied to the cathode of the amplifier - half of valve Л5-3 (6Н9С) (See Fig.50). After being amplified the pulse is applied as positive to the grid of cathode-ray tube Л5-9 (contact 3).

Plates Д-1 and Д-2 are alternately supplied with video signals (connector Ф5-2) arriving from unit Д-3 (cable 45). The switchable amplifier (See Fig.52) serves for alternate supply of the video signals to plates Д3 and Д4.

The video signals are coupled to the control grids of two video amplifiers Л5-4 and Л5-6 (6Ж4). Each video amplifier is loaded by its own deflection plate (tube Л5-9).

The video amplifiers employ identical circuits and are actually normal wide-band amplifiers with RF correction (inductance coils L5-3 and L5-4).

For alternate connection of the video amplifiers use is made of voltage Ю c.p.s. synphased with the antenna beam rotation during tracking.

This voltage is applied from the reference voltage generator unit Д-1, to the primary winding of balancing transformer Тр5-2 through connector ШВ5-1, contact 9. From the secondary winding the anti-phase voltages (about 130 V) are impressed on the grids of the switching stage (valve Л5-5, type 6Н9С). The switching stage is a two-way clipping amplifier, whose cathode is fed with -255 V. On the anodes of clipping amplifier Л5-5 anti-phase half-cycle pulses are squared (See Fig.52). Since the loads of the clipping amplifier are connected between the anodes of valve Л5-5 and "earth", the upper level of the square pulses is clamped at all times at the "earth" potential when the triode is cut off, and the lower level of the pulses is -160 V when the triode is conducting (in the selected operating condition). The anodes of triodes Л5-5 are coupled to the suppressor grids of video amplifiers Л5-4 and Л5-6. With the suppressor grid at -160 V in the selected operating condition, valve 6Ж4 is cut off reliably. Consequently, during operation of the switching stage the video amplifiers are driven into conduction and are cut off alternately every half-period of the Ю-cycle voltage developed by the reference voltage generator. Therefore, the video signals are passed to plates $П_1$ and $П_2$ of tube Л5-9 alternately, too. The simplified circuit of the switchable video amplifier is shown in Fig.52.

To obtain a clear picture of the target when taking its bearings, it is necessary that the number of target echoes passed to the plates of the tracking indicator should be equal to 6 - 8, which corresponds to a 40° turn of the radiator during conical scanning at the Ю-cycle frequency.

The video pulses are furnished to plates $П_1$ and $П_2$ of the tracking indicator tube alternately during every half-cycle (180°) of the antenna beam rotation. But since only 40°

of the antenna beam rotation arc should be used for obt[illegible] a clear picture and accurate bearings of a target, use is made of 40° brightening. When the target is swept by the equisignal zone, the video pulses appear on the indicator screen only during the period the antenna beam covers about 40° in the left and right azimuth positions of the antenna beam in case of conical scanning. The 40° brightening is obtained by means of the brightening channel in the tracking video amplifier. In this channel are squared the pulses brightening the pattern on the tracking indicator tube at the moments when the antenna beam passes an angle of 40° in the left and right azimuth position of the beam (Fig. [illegible]).

The first shaping stage - Valve Л5-10, type [illegible] The valve grids are fed with anti-phase sinusoidal voltages from the secondary winding of transformer Tp5-2. As the time constant of transmission circuit C5-27, R5-30, R5-101 and C5-28, R5-[illegible] R5-102 is rather small (1.0 µF, 20 megohms), and the voltage value is large (about 130 V [illegible]), the sinusoidal voltages of [illegible] c.p.s. are limited unilaterally in either triode. [illegible] parameters of this circuit ensure that the anode current of the valve [illegible] for about 4 microsec. during the [illegible] half-wave of the [illegible], since [illegible] c.p.s. correspond to approximately a period of 34 microsec. or to the turn of the antenna beam through 360°. 40° of the antenna beam rotation corresponds to about 4 microsec.

The [illegible] double frequency [illegible] c.p.s.) [illegible] from load [illegible]-29 of valve Л5-10 and [illegible] to the second shaping [illegible] (half of valve Л5-8, type 6Н8С). In this [illegible] the rounded pulse peaks are clipped by the [illegible], which results in positive square pulses [illegible] on the anode of half of the valve. The voltage waveforms are shown in Fig.53.

Further the positive-going 40° brightening pulse is supplied to mixer Л5-[illegible] (valve 6Н8С) where it is mixed with the range mark. [illegible]

1.5-microsec. duration, which is produced in range unit Л-7. This pulse is closely timed with the pulse triggering the range sweep of the tracking indicator, i.e. it always follows the sweep trigger pulse after each 34 microsec. Therefore, the range mark must always be in the middle of the sweep trace.

In order to obtain a sharp, focused bright range mark, the range mark pulses are applied to the cathode-ray tube in intervals between 40° brightening pulses. This happens as follows: both triodes of valve Л5-11 where the signals are mixed, have common cathode resistor R5-39 shunted by capacitor C5-32. The capacitor resistance is high for the 4-microsec. pulse and low for the 1.5-microsec. pulse. When the positive 40° pulse arrives at the grid of the left triode of the mixer, the other mixer grid proves to be cut off due to the 40° pulse being repeated on cathode resistor R5-39 in magnitude sufficient for cutting off the adjacent triode. In the absence of the 40° pulse the right triode operates as an amplifier. From the common anode load the mixed pulses are communicated to the cathode of the cathode-ray tube and modulate the latter in intensity.

The mixer circuit and voltage waveforms are shown in Fig.54.

With the [illegible] switch on the control panel in position SEARCH (ПОИСК) and MANUAL I (РУЧН.I), relay P5-1 is dead, and only one video amplifier stage (Л5-4) is functioning. In this case the [illegible] screen presents the pulses at the right side.

When the [illegible] on the control panel is set at MANUAL II and [illegible] ([illegible]), the relay is fed with +27 V. The second video amplifier starts functioning, and the screen presents [illegible] pulses moving both sides. The switching stage begins to [illegible] simultaneously.

To [illegible] the [illegible] supply circuit and reduce the number of [illegible] blocking capacitors, the minus of the high voltage is applied to the cathode of tube Л5-9, while the

plus is earthed. The high-voltage from the -1500-V rectifier is impressed to the high-voltage divider which feeds all the electrodes of cathode-ray tube Л5-9. The brightness is controlled by potentiometer R5-57. The horizontal and vertical displacement is carried out by the symmetrical voltage taken from ganged potentiometers R5-48, R5-49, R5-54, R5-35, connected between the +300-V and -255-V stabilized supplies.

Sighting indicator channel

The sighting indicator channel comprises:

(a) cathode-ray tube Л5-14, type 8Л029;

(b) clipping amplifier and saw-toothed voltage phase-inverter amplifier(valve Л5-12);

(c) video amplifier valve Л5-13, type 6Ж4.

The cathode-ray tube presents A-display (See Fig.55).

The sweep voltage is the voltage applied from the sweep unit to the deflection coil of the search indicator. The saw-toothed voltage is clipped and amplified by valve Л5-12 (6H8C). The operating condition of this valve ensures rather a good clipping of the saw-toothed wave pedestal and amplification of the saw-toothed voltage at the output. This results in appearance on the sighting indicator of sweep durations corresponding to the search indicator sweeps 100 and 200. Fig.55 shows that the saw-toothed voltage amplitude produces a sweep larger in size than the working part of the screen. The working part of the screen is 55 to 60 mm and in case of range 100 it should contain a working range of about 80 ± 4 km. with 15 per cent linearity.

The grid of the clipping amplifier (valve Л5-12,type 6H8C) is fed with saw-toothed voltage (with pedestal) from the search channel (contact 14 of connector ШВ5-8). A saw-toothed voltage pulse at the output of valve Л5-12 (contact 2) is 170 V. This pulse is applied to one of the horizontal plates (contact 7 of the cathode-ray tube); simultaneously

part of this voltage taken from the divider (variable resistor R5-85) is applied to valve Л5-12, where it is turned over and amplified. The turned-over saw-toothed voltage equal to 170 V is applied to the horizontal plate (contact 8) of the tube from the output of the phase-inverter amplifier.

The required linearity is ensured by optimal current negative feedback (resistors R5-84 and R5-86) in both valves.

The sighting sweep trace is brightened by the brightening pulse of the search indicator. This square-wave voltage is taken from part of the anode load of valve Л5-1 (resistor R5-17) and applied to the grid of cathode-ray tube Л5-14 (contact 3).

The signals coming from the sweep unit mixer are amplified by valve Л5-13 (6Ж4); the linear characteristic is obtained by introduction of current negative feedback (R5-84). The sighting indicator can also be used for viewing the processes occurring in various circuits of the radar. For this purpose, the sweep can be started by an external pulse (See sweep unit Д-4М) and the indicator functions as a synchroscope.

This is achieved by setting switch OPERATION-CHECK (РАБОТА-КОНТРОЛЬ) to position CHECK and feed an external pulse to start the sweep to test jack 3 (Г4-3) located on the front panel of the sweep unit.

Tube Л5-14 is supplied in essentially the same way as tube Л5-9 from a common rectifier (-1500 V).

Brightness is controlled by potentiometer R9-63 and focus, by potentiometer R5-66. The horizontal and vertical centering is symmetrical and effected by gang potentiometers R5-71, R5-72, R5-94, R5-95, connected between the stabilized power supplies (+300 V and -255 V).

Unit supply

The filaments of all the electronic devices are supplied by filament transformer Tp5-1 connected to 115 V, 400 c.p.s. mains. The secondary windings are distributed as follows:

(a) winding II feeds the filaments of valves Л5-1, Л5-3, Л5-4, Л5-5, Л5-6, Л5-8, Л5-10, Л5-11, Л5-12 (6.3 V, 6 A);

(b) winding III feeds the filament of tube Л5-2 (6.3 V, 0.6 A);

(c) winding IV feeds the filaments of tubes Л5-9 and Л5-14 (6.3 V, 0.5 A);

(d) winding V feeds lighting lamps CM-36 - Л5-16, Л5-19, Л5-20, Л5-21 (3 V, 0.8 A).

The anode circuits of the tracking indicator valves are fed with unstabilized voltage of +300 V (about 25 mA), and anode circuit of the search and sighting indicators, brightness control circuit of the search indicator and beam-shift control circuits of the tracking and sighting indicators are supplied with the regulated voltage of +300 V (about 30 mA) applied to unit Д-5 from supply unit Д-8. When supplied with -255 V reg., unit Д-5 consumes a current of some 14 mA.

The 1500-V rectifier is supplied from 115 V, 400 c.p.s. mains. The total consumption by unit Д-5 when fed with 115 V, 400 c.p.s. is not more than 1.4 A.

6. Remote Indicator Д-6М

Purpose

The remote indicator (PPR) in radar K-IIM is intended for:

(a) determining the range of a target or a group of targets;

(b) determining the azimuth of a target or a group of targets;

(c) determining the aircraft heading.

The indicator presents a plan of the positions of all targets within a circular area. The echo pulse voltage is applied to the control grid of the cathode-ray tube, which causes increase in intensity of the electron beam during the working time of the pulse.

Basic circuit of unit П-6М

The basic circuit of the PPR (Fig.81) consists of a cathode-ray tube, type 13ЛМ31, with its equipment, brightening pulse shaping stage and video amplifier Л6-1, type 6Н1П.

The cathode-ray tube equipment comprises:

(a) rotary deflection coil L6-2;

(b) selsyn-motor M6-1, type CMC-1, rotating deflection coil L6-2;

(c) focusing permanent magnet with adjustable shunt;

(d) centering system for shifting the electron beam on the screen of cathode-ray tube 13ЛМ31 in horizontal and vertical directions.

(e) phasing contacts for synchronizing the rotation of the deflection coil with that of the radar antenna.

The deflecting saw-toothed current is delivered to deflection coil L6-2 from the sweep unit. The deflection coil is rotated in step with the antenna around the neck of the cathode-ray tube. The deflection coil is rotated through gearing 10:1 by receiving selsyn M6-1 (type CMC-1).

The phasing switch is intended for connecting the receiving selsyn to the transmitting selsyn (antenna-mounted unit И1) in one of its ten positions. The switch contact may be shorted by means of relay P12M-1.

Fig.57 shows the schematic diagram of the receiving selsyn, type CMC-1.

The signal of the video amplifier of the sweep unit is communicated to the plan position repeater through coaxial cable 46 and box Ш22. This signal is passed over cable 35 to the cathode of the second triode of valve Л6-1 (6Н1П). This is a compensated video amplifier of unipolar signals. The width of the amplifier passband is about 0.1 to 1.8 Mc/s. From the output of the video amplifier a positive signal is furnished to the grid of cathode-ray tube Л6-2 (13ЛМ31). The cathode of the first triode of valve Л6-1 is fed with a sweep trace

brightening pulse from the sweep unit (over a multi-core screened cable).

The brightening pulse is passed to the first anode of tube Л6-2 from the anode of first triode of valve Л6-1. The video amplifier is necessary because of substantial losses in the long video cable and for ensuring the frequency passband.

The indicator beam brightness is controlled by varying the positive potential at the cathode with potentiometer R6-8 BRIGHTNESS (ЯРКОСТЬ).

Focusing and centering are effected by means of a permanent ring-shaped magnet.

The ring-shaped magnet is assembled in a magnetic system with controls brought out.

The beam centering is achieved by moving the magnetic shunt while rotating knobs VERTICAL CENTERING (ВЕРТИКАЛЬНАЯ ЦЕНТРОВКА) and HORIZONTAL CENTERING (ГОРИЗОНТАЛЬНАЯ ЦЕНТРОВКА).

Focusing is controlled by knob FOCUSING (ФОКУСИРОВКА) coupled with the magnetic shunt moving on the helical rails of the magnetic assembly along the magnet axis, thus changing the field.

+4900 V D.C. is supplied to the second anode of the tube from high-voltage rectifier Д-25.

A D.C. regulated voltage of +300 V, feeding valve Л6-1, the first anode and brightness control divider of tube Л6-2, is delivered from regulated rectifier Д-8.

The filament voltage to valve Л6-1 (6Н1П), tube Л6-2 (13ЛМ31) equal to 6.3 V and to dial lamps ЛН6-3, ЛН6-4, ЛН6-5 equal to 3 V is applied from the step-down transformer of unit Д-22.

The rotor of selsyn-motor M6-1 operates on 115 V, 400 c.p.s. from the inverter.

The brilliance of the dial lamps is controlled by means of rheostat R6-11.

Unit supply

The unit is supplied from the following sources:

(a) 115 V ± 3 V, 400 c.p.s.+40,-20 c.p.s.;

(b) regulated voltage +300 V ± 3 V;

(c) D.C. voltage +4900 V ± 300 V.

7. Autoselector Д-7

Purpose

The purpose of autoselector unit Д-7 is to synchronize the radar operation, follow the target automatically in range and select the target video pulses for the elevation tracking unit and AGC channel.

Composition of unit

Autoselector Д-7 consists of two main parts which are a selector and a lock unit.

The lock unit is mounted on the first subpanel and consists of the following assemblies:

(a) Crystal oscillator designed for stabilizing the mid repetition frequency of the pulses generated by the unit.

(b) Voltage dividers converting high frequency of the crystal (60 Kc/s) to a series of short pulses with repetition frequency n_1 or n_2 c.p.s.

(c) Frequency modulation phantastron modulating the repetition frequency of the synchronizing pulses.

(d) Output cathode followers allowing the output synchronizing pulses of the unit to be passed through long screened conductors.

(e) 90-microsec. delay multivibrator designed for displacing the transmitter trigger pulse for a time equal to the initial delay of the range mark in the selector circuits (fixed delay of 90 microsec. is connected when the radar is operated on CHECK).

(f) Delay line producing a small additional delay (about 1 msec.) of the transmitter trigger pulses.

The selector portion of the unit is mounted on the second and third subpanels and consists of the following assemblies:

(a) Comparator comparing the time delay of the tracked target signal with the delay of the gate pulses produced by the unit. If the delays are not coincident, the comparator produces the respective error voltage.

(b) Double integrator converting the pulse error voltage to constant control voltage for the range phantastron. It also ensures the velocity memory of the system.

(c) Variable delay (range) phantastron delaying the gate pulses. Together with the integrator and comparator it forms a servo system for automatic target tracking.

(d) Selector proper designed for separation of the target tracking pulse from all the targets within the swept area.

Description of functional diagram

Fig.58 is a representation of the autoselector.

The crystal oscillator produces a stable frequency of 60 Kc/s.

The frequency dividers reduce the crystal frequency in three stages (3:1, 4:1 and 4:1).

The third frequency divider generates pulses at frequency n_2 c.p.s. and starts the modulation phantastron. The trailing edge of the phantastron pulse controls the moment all synchronizing pulses of the unit are generated.

The control circuit of the phantastron is fed with sinusoidal voltage from the reference voltage generator.

The phantastron pulse length being changed, all the synchronizing pulses produced by the unit are proved to be frequency-modulated.

The check pulse of circuit K-I is the first in time to go (time moment t_1, Fig.9, Part I).

The suppressor and sweep trigger pulses are delayed by 90 microsec. with respect to the above-mentioned pulse. The

transmitter trigger pulse is delayed in addition by 1 microsec. with respect to the suppressor and sweep trigger pulses.

The synchronizing pulses are communicated to the external circuits through the cathode followers.

The variable delay phantastron circuit is started at time moment t_1.

The trailing edge of the phantastron pulse controls the moment the tracking indicator trigger pulse is generated, and the moment of operation of the fixed delay multivibrator (34 microsec.).

Position of the multivibrator pulse trailing edge assesses the moment of operation of the blocking oscillator which, in turn, triggers the 1st and 2nd gate pulse oscillator, the 2nd pulse being delayed by the delay line by 0.7 microsec. with relation to the 1st pulse. The range mark formed in the circuit is delayed by 0.35 microsec. with respect to the 1st gate pulse.

The tracking indicator trigger pulses, gate pulses and range mark can be simultaneously displaced with respect to moment t_1, provided mutual time delays between them are retained.

Since generation of the enumerated pulses is associated with functioning of the variable delay phantastron, variation of the phantastron pulse duration allows the pulses to be displaced with the entire distance range, and gate pulses can be matched with it upon arrival of the target pulse.

Through the comparator and integrator circuits the target pulse controls the duration of the variable delay phantastron pulse keeping the gate pulses automatically in the stage of balance with regard to itself (the so-called target tracking).

The pulse of the tracked target and the selecting pulse are applied to the coincidence stage.

The coincidence stage allows the tracked target pulse to pass through the selector channel, preventing the passage of all pulses from the other targets. The pulse selected is amplified and through the cathode followers is furnished to the external circuits (to units Д-3, Д-4М and Д-10).

50X1-HUM

Description of schematic diagram of unit

(Fig.59)

(a) Lock unit

Crystal oscillator and pulse generator

The crystal oscillator (Fig.60) employs valve 6H8C (Л7-1); the crystal is connected between the grid and cathode of the valve.

To obtain generation the circuit in the anode is tuned to a frequency higher than that of the crystal plate (f crystal-60 Kc/s).

During functioning of the crystal oscillator, the grid of valve Л7-1 is kept at a great negative bias due to which the anode current flows not continuously, but pulsewise.

The anode current pulses are passed through transformer Tp7-1 winding and develop a voltage across it (See Fig.61) synchronizing the blocking oscillator (utilizes the right portion of valve Л7-1).

Functioning of the blocking oscillator is illustrated in Fig.62. When the anode current passes through the transformer, a positive voltage is induced on the grid causing the anode current to rise.

The grid current flowing through resistor R creates a voltage of the opposite polarity and charges capacitor C.

When the anode current stops rising, the grid voltage is not induced. The valve is driven sharply to cut-off by the negative voltage across the capacitor, and the latter starts discharging through resistor R. Damped oscillations are excited in the anode, and pointed peaky pulses are produced at the output (point A).

The pulse generator of unit Д-7 functions in essentially the same way.

With the positive voltage at the grid of the right portion of valve Л7-1, capacitor C7-104 acquires a charge. The capacitor discharges through resistor R7-106.

The output negative pulses having a frequency of 60 c.p.s. are taken from resistor R7-104.

Frequency dividers

The synchronizing pulses taken from resistor R7-104 are furnished to the cathode of the left portion of valve Л7-2.

This stage (Fig.63), which is actually a blocking oscillator, is intended for dividing frequency (1st frequency divider).

The functioning of the blocking oscillator is analogous to that of the pulse generator, the difference being that the time constant of the circuit (C7-108, R7-107, R7-108) is approximately three times as large as the time constant of the pulse generator circuit.

Owing to this the blocking oscillator is triggered only by the fourth pulse furnished by the pulse generator, since certain synchronizing pulses (2nd and 3rd in Fig.64) fail to open the valve during the charge reduction time of the capacitor.

Thus, the circuit acts as a frequency divider (3:1). Potentiometer R7-107 is used for varying the circuit time constant, which is necessary for obtaining stable division.

The output negative pulses are taken from resistor R7-110 and furnished to cathode 2 of the frequency divider (right portion of valve Л7-2) whose division ratio is 4:1.

Potentiometer R7-115 is used for varying the time constant of the circuit (C7-114, R7-116, R7-115).

Taken from resistor R7-113 are negative pulses going to cathode 3 of the frequency divider (left portion of valve Л7-3) whose division ratio is 4:1.

The time constant of the circuit (C7-116, R7-118, R7-117) is adjusted by potentiometer R7-117. The output pulses of frequency n_2 pulses per second are taken from resistor R7-120 (negative) and from divider R7-123, R7-124 (positive).

The stability of the obtained pulse sequency is rather high, for the frequency is crystal-controlled.

Modulation phantastron

The negative pulses from the 3rd frequency divider are passed to the phantastron circuit.

The phantastron is a modification of the cathode-coupled multivibrator which employs a multigrid valve - pentagrid 6A7 instead of two valves.

The simplified circuit of the phantastron is shown in Fig.65, and characteristic curves in Fig.66.

Valve 6A7 can be considered as a pentode whose screen grid is substituted by the whole system formed by three grids G_2, G_3, G_4.

The second and fourth grids are coupled together; they are fed with a positive voltage and function as a screen grid. The voltage on grid G_3 may be higher or lower than that on the cathode; this grid functions as an additional control grid.

Due to provision of the screen grid the variation of the voltage being applied to grid G_3 has no noticeable effect on the total amount of the cathode current of the valve which is essentially the sum of the anode current and current of the screen grid; however, the magnitude of this voltage governs the current distribution between the anode and the screen grid.

If the voltage at grid G_3 has a large negative magnitude with respect to cathode, the electrons leaving the cathode are not able to pass the grid; for this reason all the electrons are directed to the screen, and the anode current is blocked. The voltage rise on grid G_3 causes an increase in the anode current and a corresponding decrease in the screen grid current.

In grid G_3 itself the current appears when its voltage reaches the cathode voltage. Like the control grid of a pentode, grid G_1 controls the total of the anode current and the screen grid current. Grid G_5 functions normally as a suppressor grid.

Voltage to screen grid G_2-G_4 is picked off voltage divider R7-132, R7-133 (134).

The current in the cathode circuit of valve Л7-4 is to a great extent determined by the screen current.

A voltage drop of about +40 V occurs across resistor R7-130. A voltage divider formed by resistors R7-128, R7-129 feeds grid G_3 with a bias of about +25 V with respect to earth. Thus the bias set up on grid G_3 is -15 V with respect to cathode and its magnitude is large enough to drive the valve to cut-off.

This steady initial state is illustrated in Fig.66 as stage VI.

The stage is characterized by that the anode current is blocked with the potential at grid G_3, and the screen grid current is controlled with the potential at grid G_1. Due to a large value of resistor R7-131 the anode current is maintained at a level pre-set by potentiometer R7-126, through a diode (right portion of valve Л7-3).

The cathode voltage of this diode is actually the control voltage of the phantastron.

Grid G_1 is connected to the +300-V supply through resistor R7-132 and it is kept at zero bias with respect to cathode. Capacitor Cg is charged to the control voltage minus the initial voltage on grid G_1 with respect to earth.

The cathode of diode Л7-3 is furnished with a negative pulse from the 3rd frequency divider. In this case, the cathode potential becomes more negative relative to anode, causing the anode current in the diode to increase. This current while flowing through resistor R7-131 develops a voltage drop across it.

As a result, the anode voltage of the phantastron decreases. The negative pulse is also communicated to grid G_1 via capacitor Cg, which involves reduction of the current flowing through cathode resistor R7-130. The positive potential on the cathode decreases, which lowers the bias on grid G_3 and gives start

to the flow of the anode current (Fig.66, stage I).

Capacitor Cg has no time to discharge materially through resistor R7-135. The cathode voltage becomes so low that grid G_3 does not prevent the passage of the anode current. The duration of the above-mentioned stage I is approximately 4 microsecs.

The next stage of phantastron operation is characterized by the voltage dropping on the anode in a linear manner. The negative feedback through capacitor Cg ensures the constant rate of voltage variations.

This gives start to discharge of capacitor Cg charged to the control voltage determined by the position of potentiometer R7-126 slider, minus the inital voltage on grid G_1.

There is no current in the circuit of grid G_1 and its potential starts rising to +300 V. The anode current and the potential of screen grid G_2-G_4 increase. The resultant anode voltage drop is supplied to grid G_1, thereby preventing the rise of its positive voltage.

That is the result of the feedback effect. The voltages on the anode and screen grid drop so that the anode current ceases to increase when the positive voltage rises on the grid.

The time of the linear voltage drop is proportional to the control voltage set by potentiometer R7-126.

In the next cycle (stage III, Fig.66) there is no feedback, the anode voltage rises slightly.

The voltage on control grid G_1 rises tending to reach the plus (+) of high voltage according to the time constant of Cg Rg, the cathode voltage and screen grid current increase, too. Due to a voltage drop on the screen grid, the anode current would have to rise. But the rise of the current flowing through resistor R7-130 causes an increase of the bias on grid G_3. This prevents increase of the anode current, and the anode voltage rises continuously.

At last the cathode voltage so rises that the anode current is clipped by the bias on grid G_3. This process lasts about 1.5 microsecs.

When the anode current is clipped and the voltage on the screen grid drops, the blocking oscillator gets excited. The blocking oscillator uses valve Л7-5 (right portion of the valve).

The voltage rise on grid G_1 is backed by the rise of the anode voltage. The screen grid voltage drops rapidly due to the increase in the grid current.

The rate at which the anode voltage rises is determined by the rate of fall of the anode current depending on the bias at G_3 and RC time constant (Fig.66).

The voltage on the screen grid drops to such a degree at which its current ceases to grow as the bias on grid G_1 decreases.

Before the phantastron comes back to the initial stage discussed above, the following phenomena take place in it.

The anode voltage rises as high as the control voltage. Capacitor Cg charges to the same voltage magnitude. The anode voltage rises with the RC time constant tending to reach the plus of high voltage. As the anode voltage rises, the voltage across capacitor Cg rises too. The anode voltage rises until it is clipped by the diode. It remains constant before arrival of the next trigger pulse.

The considered process results in the moment of operation of the blocking oscillator, utilizing the right portion of valve Л7-5, being time-delayed with respect to the trigger pulse arriving from the 3rd divider. The amount of delay depends upon the control voltage.

The moment of operation of the blocking oscillator determines the beginning of the shaping process of all synchronizing pulses produced by the unit.

With the constant magnitude of the phantastron control voltage, the synchronizing pulses are displaced by the amount of delay produced by the phantastron, but their repetition frequency remains equal to the frequency of the pulses of the 3rd divider, i.e. n_2 pulses per second.

To obtain frequency modulation (wobbulation), alternating sinusoidal voltage of 10 c.p.s. frequency taken from potentiometer R7-007 slider is superimposed on the constant control voltage (10 c.p.s. voltage is applied to the potentiometer from the reference voltage generator or from the RC oscillator of the course indication unit).

When acted on by the control voltage varying sinusoidally the delay produced by the phantastron varies sinusoidally, too, due to which the interval between the adjacent pulses increases or decreases.

This variation of the interval is tantamount to frequency modulation in the course of which frequency n_2 receives increment $\Delta n_2 \sin (2\pi 10 t)$.

The ratio of maximum frequency increment Δn_2 to initial frequency n_2 is called the relative frequency deviation (ε). Using potentiometer R7-007 located on the front panel of the unit allows any frequency deviation to be set within 0 to 1 per cent.

In the course of frequency modulation, during the time the modulating voltage changes from zero to maximum (i.e., for the half-cycle of the modulating voltage), every following pulse is displaced with respect to the preceding one. The total time displacement t_m of the pulse in relation to its position is determined, in the absence of modulation, by expression

$$t_m = \frac{\varepsilon}{2\pi f_1}$$

The displacement of pulses is the result of variation of the delay produced by the phantastron, owing to which the linear section of the phantastron (unit Д7) exceeds two t_m microsec., and the initial control voltage of the phantastron provides for operation on the linear portion of the characteristic curve (Fig.67).

Frequency divider 2:1

In the search mode relay P7-1 is caused to operate (See the schematic diagram).

The relay operates when the radar is on and switch RANGE, KM. (ДАЛЬНОСТЬ В КМ) is set at 200 km. In this case, the phantastron and difference amplifier utilizing the left portion of valve Л7-5 are disconnected.

The synchronizing pulses from the 3rd divider picked off resistor R7-123 are furnished through capacitor C7-118 and the relay contacts to the control grid of the blocking oscillator; the time constant of the blocking oscillator time-setting circuit increases, for resistor R7-137 (750 kilohms) is connected in place of gridleak R7-136 (30 kilohms).

In this condition the blocking oscillator functions as a frequency divider 2:1. Hence the frequency of all the synchronizing pulses produced by the unit equals n_1 pulses per second.

Output cathode follower of

test circuit K-1

The positive pulse is fed from the 3rd winding of the pulse transformer to the grid of the cathode follower (left portion of valve Л7-6, type 6H8C) through coupling capacitor C7-129 (See Fig.68).

From resistor R7-146, connected into the cathode of valve Л7-6 is taken K-1 test pulse (moment t_1 of time-relationship diagram, Fig.9).

The pulse is supplied to the external circuit via prong 1 of the connector.

To reduce the quiescent current the valve cathode contains a self-bias circuit consisting of resistor R7-147 shunted by capacitor C7-131 (the circuit contains several cathode followers connected similarly).

90-microsec fixed delay multivibrator

To compensate for initial delays in the selector which total to about 90 microsecs, some lock unit pulses (the suppressor and sweep trigger pulse, transmitter trigger pulse) are delayed by 90 microsecs, too.

The delay is effected by the multivibrator (Fig.69).

The multivibrator employs valve Л7-7 and is actually a normal cathode-coupled multivibrator.

The left portion of the valve is normally cut off by a large negative bias formed by the voltage drop across common cathode resistor R7-152 when it carries the current of the right portion of valve Л7-7.

The multivibrator is triggered through a buffer amplifier (right portion of valve Л7-6). The amplifier grid is fed with a trigger pulse (moment t_1, Fig.69). Across the anode resistor common for the left portion of valve Л7-7 and the right portion of valve Л7-6 is developed a negative pulse, that passes to the grid of the right portion of valve Л7-7 through capacitor C7-138. The current stops flowing through the right portion of the multivibrator, and now the left portion is under current (turn-over process).

Such a condition lasts until capacitor C7-138 is charged through resistors R7-156, 157 to a magnitude sufficient for driving the right portion of the multivibrator valve into conduction.

The circuit returns to the initial state.

Across the anode load of the multivibrator are squared the pulses whose duration may be adjusted by means of resistor R7-156.

The positive square pulse of 90 microsecs is furnished from the anode of the right portion of the multivibrator valve to winding I of pulse transformer Тр7-6, connected into the anode of the biased blocking oscillator using the left portion of valve Л7-8.

As a result of the existent differentiation, two pulses are formed across the pulse transformer winding: positive, corresponding to the leading edge of the pulse, and negative, corresponding to its trailing edge (Fig.70). The pulses are of the reversed polarity across the winding connected into the grid circuit.

The first, i.e. negative pulse has no effect on the cut off valve, the second - positive pulse makes it conducting thereby

causing operation of the blocking oscillator.

The moment of operation of the blocking oscillator appears to be delayed by 90 microsecs relative to the trigger of the multivibrator.

When relay P7-3 is deenergized, the multivibrator is isolated from the circuit and the blocking oscillator is caused to operate by the multivibrator trigger pulse.

The positive pulse from the 3rd winding of pulse transformer Tp7-6 via capacitor C7-149 is applied to the grid of the cathode follower (the left portion of valve Л7-9). The suppressor and sweep positive trigger pulse are taken from the cathode load. The pulse is furnished to the external circuits through prong 3 in the connector.

Besides, the suppressor and sweep trigger pulse is coupled to delay line Z7-1.

To eliminate reflection of the pulse from the end of the delay line, matching resistor R7-169 is connected to the exit of the line. The delayed pulse is passed to the grid of the cathode follower (right portion of valve Л7-9).

The trigger pulse of the transmitter-receiver unit is taken from cathode load R7-168.

The pulse is passed to the external circuit through prong 8 of the connector.

Due to provision of the delay line the trigger pulse of the transmitter-receiver unit is 1 microsec. late relative to the suppressor and sweep trigger pulse.

Delay line

Delay line Z7-1 consists of three sections. Each section is actually a single-layer coil wound on a pertinax bar. The coil inductance is 220 μH. The coil is enclosed in an earthed screened cylinder. The coil capacity with respect to the screen (C=550 pF) is distributed evenly over the entire length of the coil.

One section of the line delays the signal by the time equal to $\tau_3 = \sqrt{Z \cdot C} = 0.35$ microsec.

The delay line consists of three sections, consequently, the total delay of the pulse is 1.05 microsecs. The delay line loaded by resistance R equal to its characteristic impedance, i.e., $R = \rho = \sqrt{\frac{L}{C}} = 620$ ohms.

Note: Delay line Z7-3 used in the unit is composed of two sections $\tau = 0.7$ microsec.), and Z7-2 of one section (τ=0.35 microsec.).

(b) Selector

At time moment t_1 (See Time Relationships, Fig.9) the variable-delay(range phantastron is triggered.

The duration of the phantastron pulse can be varied by means of external controls or, in case of automatic tracking, by the target pulse.

Position of the trailing edge of the phantastron pulse determines the delay of the selector pulses. These pulses may be displaced over the entire range band, their mutual delays being invariable.

Range phantastron.

The negative trigger pulse is taken from resistor R7-142 and furnished to the cathode circuit of the phantastron diode (right portion of valve Л7-24, type 6H9C).

The control voltage is applied from the circuits of the 2nd integrator (See below).

The variable-delay phantastron (Fig.70) utilizes pentagrid 6A7 (Л7-25). The phantastron circuit contains two additional valves.

One of them (left portion of valve Л7-24) is a cathode follower which serves to reduce the restoration time of the phantastron anode voltage. This is necessary because the linear portion of the anode voltage drop must make up about 90 per cent of the interval between the trigger pulses, whereas in the circuit devoid of the additional valve the linear portion makes up at best 60 per cent of the interval (See Fig.71).

The other additional valve of the phantastron (left portion of valve Л7-23) clamps the initial voltage on the 1st grid of the phantastron, owing to which stabilization of the phantastron pulse duration is achieved at fluctuations of the feeding voltages.

The pulse from the screen grid of pentagrid 6A7 is differentiated by circuit C7-314 and R7-323 and amplified by the right portion of valve Л7-23 (6H9C). The amplified positive pulse corresponding to the trailing edge of the phantastron pulse (See Fig.72) triggers the blocking oscillator utilizing the left portion of valve Л7-22 (6H8C).

Blocking_oscillator

The right portion of valve Л7-22 (6H8C) is a trigger amplifier (See the schematic diagram). The blocking oscillator is triggered on account of the anode load common for the amplifier and blocking oscillator valve which is the winding of pulse transformer Tp7-8. From the third winding of blocking oscillator pulse transformer Tp7-8 the positive pulse is passed to the grid of the cathode follower employing the left portion of valve Л7-20 (6H8C). The positive trigger pulse of the tracking indicator is taken from resistor R7-302 of the cathode follower.

This pulse is furnished to the external circuit through prong 12 of the connector.

Multivibrator

The blocking oscillator pulse taken from resistor R7-316 connected into the cathode of the blocking oscillator starts the multivibrator (See the schematic diagram).

The duration of the multivibrator pulse is set by potentiometer R7-309 and equals 34 microsecs.

Blocking_Oscillator

The blocking oscillator employing the right portion of valve Л7-13 (See the schematic diagram) is triggered by the trailing edge of the multivibrator pulse.

The moment the blocking oscillator is caused to operate is delayed by 34 microsecs with respect to the trigger pulse of the multivibrator.

From winding III of transformer Tp7-10 the pulse of 1.2-microsec. duration is passed to the strobe pulse shaping circuits (See below 'Selection of Target Pulse').

Gate pulse generator

The positive pulse from resistor R7-273 is coupled to winding I of pulse transformer Tp7-7 (See Fig.73) through capacitor C7-304.

This pulse starts the biased blocking oscillator using the right portion of valve Л7-20.

Connected into the blocking oscillator cathode are delay lines Z7-2 and Z7-3 loaded by matching resistors R7-272, R7-223. From the input and output of the delay line (Z7-3) are taken 1st and 2nd gate pulses respective and communicated to the comparator circuit.

From the output of delay line Z7-2 the pulse is furnished to the grid of the first portion of valve Л7-12 to shape the range marker.

Comparator

The comparator circuit comprises two coincidence valves (Fig.74).

The coincidence valves are represented by pentagrids 6A7 (valves Л7-14 and Л7-15).

The control grid of valve Л7-15 is fed with the 1st gate pulse, and the control grid of valve Л7-14, with the 2nd gate pulse, the latter being delayed by 0.7 microsec. with respect to the 1st pulse.

The heterodyne grids of these valves coupled together are supplied with video pulses of the circuits from the receiver output.

The comparator circuit contains a diode (left portion of

valve Л7-16) designed for D.C. restoration of the video pulse and noise after coupling capacitor C7-216.

Valves Л7-14 and Л7-15 are cut-off on both grids and are triggered only when the pulses applied to the control and heterodyne grids coincide.

The amplitude and duration of the coincidence pulses developed on the anode loads of the pentagrids grow as the degree of coincidence of the pulses arriving at the control and heterodyne grids of the valves increases.

If the 1st gate pulse coincides with the target echo, the current in the circuit of valve Л7-15 is prevalent, and if the 2nd gate pulse, the current in the circuit of valve Л7-14 (See Fig.75). When the gate pulses are in symmetry with the circuit pulse, the current in both valve circuits are equal.

In order to balance the comparator circuit variable resistor R7-001 is connected into the cathode of one of the valves. This resistor is located on the front panel of the unit.

Coincidence pulse amplifier

The coincidence pulses (See Fig.76) are furnished to the single-stage amplifiers (valve Л7-17, 6H9C). The positive pulses from the anode load resistors R7-242 and R7-245 are passed to the 1st integrator circuit.

1st integrator (charge-discharge valve)

The 1st integrator employs two portions of valve Л7-18 (6H9C) each of which is cut off on the grid and opens for a time depending upon the duration of the pulse arriving from the coincidence pulse amplifiers.

The load of valve Л7-18 is a filter formed by C7-224 and R7-251.

As the diagram (Fig.76) shows, opening of the right triode of valve Л7-18 is accompanied by the charge of capacitor C7-224, and opening of the left triode, by the capacitor discharge.

If the grid of the triode is supplied with pulses of equal amplitude and duration, the charge and discharge currents are equal too, and the voltage across the capacitor is kept invariable. This corresponds to the case when the balanced comparator is fed with the receiver noise voltage or a pulse returned from the target whose relative speed is zero.

If the target speed is other than zero, a state of unbalance occurs in the comparator, and depending upon the direction of the target flight the charge or discharge current prevails in the integrator circuit. In this case, a negative or positive voltage proportional to the target speed settles across capacitor C7-224.

By means of the relay contacts additional capacitor C7-225, whose function will be explained below, may be connected to the filter via resistor R7-252.

Cathode follower

The voltage from the detector filter is applied to the grid of the cathode follower employing the left portion of valve Л7-19 (6Н8С).

The cathode load of the valve is formed by three voltage dividers: R7-254, 255, R7-256, 257 and R7-258, 002.

From divider R7-254 and R7-255 connected into the cathode circuit of valve Л7-19 a negative bias is taken to the grid of the right portion of charge-discharge valve Л7-18.

Owing to this connection, the grid potential of valve Л7-18 remains approximately constant with respect to its cathode despite the cathode voltage varying in magnitude and polarity.

The arms of divider R7-256, R7-257 are so chosen that the voltage at the point where the above resistors are connected equals that on the grid of the cathode follower (See "Target search" below).

The third voltage divider is composed of potentiometer R7-002 INTEGRATOR BALANCE (БАЛАНС ИНТЕГРАТОРА)on the front panel of the unit) and resistor R7-258. From the potentiometer slider the voltage repeating the voltage on the filter is applied to the second integrator.

2nd integrator

The 2nd integrator (See Fig.77) employing valve Л7-28 (6Ж8) is actually a voltage negative feedback amplifier.

The feedback voltage is applied from the anode of valve Л7-28 (6Ж8) to its control grid through integrating capacitor C7-317.

When the D.C. control voltage is applied to the integrator input (i.e. to the control grid of valve Л7-28 through R7-347) the voltage on the anode of valve Л7-28 varies linearly.

The steepness of the voltage variation on the anode is proportional to the magnitude of the input voltage. Depending upon the magnitude and polarity of the input voltage the anode voltage may vary toward increase or decrease.

The right portion of valve Л7-27 (6H8C) serves to establish feedback that increases the integrator linearity.

The feedback voltage is applied to the screen grid of the integrator valve (See schematic diagram).

The integrator circuit provides for maximum and minimum limiting of the range voltage by the diodes - the right portion of valve Л7-16 (6X6) for maximum limiting of the anode voltage, the right portion of valve Л7-19 (6H9C) for minimum limiting of the anode voltage, and both portions of valve Л7-26 (6X6) for grid limiting in maximum and minimum (See Fig.78).

Changing the radar from the tracking to search condition causes the relay (P7-1) in unit Д-7 to operate. This changes over resistance of divider R7-237, R7-238, R7-126 and R7-239 setting the cathode voltage of the maximum limiter (the right portion of valve Л7-16).

This causes variation of the upper limit of the distance range in the tracking and search modes. Potentiometer R7-126 (MAXIMUM RANGE/ МАКСИМАЛЬНАЯ ДАЛЬНОСТЬ) is used to continuously control the upper limit of the distance range in the tracking mode.

The voltage on the anode of valve Л7-28 (RANGE VOLTAGE) is repeated by the cathode follower employing the left portion of valve Л7-27 (6H8C) and via resistor R7-325 it is fed to the cathode of diode Л7-24 through which the phantastron is started, controlling the duration of the phantastron pulse.

As a result, a follow up system is formed.

Automatic target tracking

The follow up system is in the state of equilibrium when the video pulse from the target is in symmetry with the gate pulses. In this case the coincidence pulses in both arms of the coincidence circuit are identical, the charge and discharge currents of the filter are equal; the control voltage being impressed on the 2nd integrator corresponds to zero speed and the range voltage is constant.

Variation of the video pulse delay causes a respective unbalance in the comparator. This results in the output voltage of the integrator and phantastron delay varying in such a manner that the range pulses "follow" the target pulse.

With relay P7-2 (Fig.76) energized, the system possesses "memory" in speed, for when the target signal fades out, the range pulses go on moving in the same direction and at approximately the same speed.

The speed "memory" of the system is ensured by a substantial time constant of the filter and by provision of the 2nd integrator.

When the target signal disappears the total charge and discharge current of the filter through valve Л7-18 equals zero and the voltage across the filter decreases with the time constant of the filter. In the consequence of this action the rate of change of the integrator voltage (range voltage) decreases constantly, too, but with a short-time fading of the signal from the target, the mismatch between the gate pulses and the target echo is slight and tracking is continued upon appearance of the signal returned from the target.

Target search

The target search is carried out by feeding cathode follower Л7-19 with the "search voltage" from the control panel via resistor R7-251 and contacts 1 and 2 of relay P7-2 (Fig.81).

In the search mode capacitor C7-225 is disconnected from the filter by relay P7-2.

This causes an abrupt reduction of the filter time constant, which becomes necessary for locking on the target pulse with the assigned search speed.

To make impossible a sudden change of the voltage across the filter when the relay is changed over, which may result in stopping of tracking, capacitor C7-225 is connected to the input of the control voltage cathode follower. With divider R7-256, R7-257 chosen properly, the voltage across capacitor C7-225 remains approximately equal to that across capacitor C7-224 at any search speed.

Selection of target for tracking

The target is furnished from unit Д-3 to unit Д-7 by coaxial cable 41 (See schematic diagram).

If there are several targets, the unit allows one to be selected for tracking. For this purpose the unit has provision for blocking the comparator to drop the target which is not to be tracked.

Blocking is effected by changing the parameters of the divider in the cathodes of valves Л7-14 and Л7-15 by opening the contacts through which resistor R7-234 in the tracking mode is earted. (The contacts are the component part of the combination control marked RANGE located in the control panel, Fig.81).

Disconnection of resistor R7-234 is accompanied by a sudden increase of the positive voltage on the cathodes of the comparator valves, the target signal stops controlling the unit and the gate pulses may be matched with any other target.

Selection of target pulse

To make units Д3, Д-4М and Д10 function, it is necessary

to select the signal of the tracked target from all the targets, having cancelled the remaining signals.

With this purpose in view unit Д7 is provided with a selector circuit (See Fig.79).

Video pulses from the target are passed to the grid of coincidence valve Л7-12 (½ 6H8C) through divider R7-210 and R7-221.

This valve is driven to cut-off by the positive voltage on account of the current of the open valve Л7-11 passing through common cathode resistor R7-207.

During tracking of the target the cathode of valve Л7-12 is furnished with a strobe pulse opening the valve. This pulse is shaped as follows:

A pulse from the 3rd winding of pulse transformer Tp7-10 of the blocking oscillator employing the right portion of valve Л7-13 (6H8C) is applied to the input of the amplifier (the left triode of valve Л7-13), the pulse being time-coincident with the 1st gate pulse.

From anode load resistor R7-219, a negative pulse is taken to the grid of valve Л7-11.

This pulse is much in excess of the cut off voltage of the valve which results in cutting off the anode current of valve Л7-11;

A square strobe pulse whose duration is approximately 1.2 microsecs is shaped on the valve Л7-12 cathode.

Due to matching of the pulse from the tracked target and strobe pulse valve Л7-12 opens and the selected pulse passes through the selector channel.

The remaining pulses from the targets are not passed by the valve.

After being amplified in the left triode of valve Л7-11 (½6H8C) the selected pulse is passed to cathode follower Л7-10. From the output of the cathode follower the pulse is communicated by coaxial cable 42 to the AGC circuit of the receiver (unit Д3) and by coaxial cable 48 to the angle tracking unit (unit Д10).

Via the cathode follower (the right portion of valve Л7-8) the selected pulse is furnished to prong 4 of the connector and is applied to unit Д-4М via the valve and multicore cable.

The initial operating condition of the coincidence valve is set by means of potentiometer SELECTION LEVEL (УРОВЕНЬ СЕЛЕКТ) (R7-270).

Control SELECTION AMPLIFICATION (УСИЛИТ.СЕЛЕКТ.) (R7-269) allows the amplification factor of the selector to be varied within 1.2 to 1.4.

Range marker shaping

Shaped in the unit is a range marker intended for producing a bright spot on the indicator screen (unit Д5).

The range marker is obtained by means of the 1st gate pulse delayed by 0.35 microsec. The pulse is furnished to the cathode follower (the right portion of valve Л7-12, type 6Н8С).

From the cathode follower output the pulse is taken to the external circuits (to unit Д-4М and Д5) via prong 2 of the connector.

Control of operation of unit Д-7

Operation of unit Д7 is controlled by means of a combination control situated on the control panel. This control which is actually a potentiometer structurally combined with a series of switches allows all operations on target selection to be done (Fig.81).

Shifting the potentiometer slider permits changing of the direction and search rate. The rate of search rises suddenly when pressing the potentiometer knob to the stops, for this closes contacts 7, 8 or 9, 10 shunting the resistor, and the search voltage rises abruptly.

After a target has been locked on the potentiometer is put to the mid position. In this case, contacts 4, 6 close and the "memory" relay is energized. The target is dropped by pushing the potentiometer knob and turning it clockwise or counter-clockwise. This opens contacts 2, 3 and drives the

comparator valves to cut-off; the gate pulses stop matching the target pulse; the tracking of the given target ceases.

In one of the positions of the range-scale selector located on the control panel the winding of relay P7-1 is energised, the repetition frequency of the synchronizing pulses changes from n_2 pulses per second to n_1 pulses per second and wobbulation ceases.

Supply of Unit Д7

The filament circuits of the unit are supplied by 115 V, 400 c.p.s. via step-down transformer Tp7-9.

The anode and bias circuits are fed by the rectifiers housed in unit Д8.

The current drawn from the power supplies makes up:

for voltage of 115 V .... 0.8 to 0.95 A
for voltage of +300 V reg. .... 60 to 80 mA
for voltage of +300 V .... 60 to 70 mA
for voltage of -255 V .... 6 to 8 mA
for voltage of +27 V .... 300 mA

8. Regulated Rectifier Д8

Purpose

The regulated rectifier is designed for feeding the anode-screen circuits and the bias circuits of the radar with rectified voltages of the following ratings:

(a) regulated voltage of +300 V at a load current of 180 mA;
(b) voltage of +350 V at a load current of 130 mA;
(c) voltage of +140 V at a load current of 240 mA;
(d) voltage of +300 V at a load current of 450 mA;
(e) regulated voltage of -225 V at a load current of 50 mA.

The schematic diagram of the unit is shown in Fig.83.

Unit design

There are three transformer Tp8-1, Tp8-2, Tp8-3 to feed the anode and filament circuits of the unit, transformer Tp8-1 feeds the anode circuits of rectifiers: +300 V reg, +350 V and 140 V unreg.

Transformer Tp8-2 feeds the anode circuit of the +300-V unregulated rectifier.

Transformer Tp8-3 feeds the anode circuit of the -225-V regulated rectifier, as well as the filament circuits of all the unit valves.

All the transformers are rated for operation on 115 V, 400 c.p.s.

+300-V regulated rectifier

From the secondary winding of transformer Tp8-1 the alternating voltage is supplied to kenotron Л8-1 (5Ц3С). The rectified voltage is supplied to the output via the filter (C8-1, C8-2, Дp8-1) and electronic regulator (valves Л8-2, Л8-3, Л8-4). The electronic regulator (Fig.82) consists of valve Л8-2 (6Н5С) - regulating, valve Л8-4 (6Ж4) - control, and voltage regulator Л8-3 (СГ3С), which serves as a source of reference voltage for the cathode of valve Л8-4 and for voltage divider R8-10, R8-11, R8-12 (Fig.82).

Resistors R8-7, R8-8 serve to limit the current through the stabilivolt, as well as to feed the screen grid of valve Л8-4 (R8-7).

Regulator operation. Suppose the regulator output voltage increases due to a voltage rise at the input of the regulator (or decrease of the load current).
Divider R8-10, R8-11, R8-12 is so selected that the voltage applied from it to the control grid of valve Л8-4 is with the rated voltage at the regulator output, somewhat lower than the reference voltage on cathode of Л8-4 taken from voltage regulator Л8-3. This ensures constant negative bias on valve Л8-4. Therefore the grid potential increases relative to the cathode as the output voltage increases. So does the current flowing through valve Л8-4 and resistor R8-6. This results in an increase of the voltage drop across the resistor and, consequently, of the negative bias on the grid of valve Л8-2; the internal resistance of this valve increases. The voltage drop across valve Л8-2 increases in proportion to the output voltage. As a result, the output voltage remains invariable.

If the input voltage of the regulator decreases (or the load current increases), the control grid potential of valve Л8-4 decreases which causes the voltage drop across resistor R8-6 to decrease and control grid potential of Л8-2 to increase. The internal resistance of valve Л8-2 decreases. The voltage drop across the latter decreases and the output voltage rises to the initial valve.

The magnitude of the rectified regulated voltage may be controlled by potentiometer R8-11 (brought out to the front of the unit panel) marked +300 V reg. (регул.+ 300 V) from the slider of which voltage is taken to the control grid of valve Л8-4.

Capacitor C8-4 passes the ripples directly from the regulator output to the control grid of valve Л8-4.

The output of the +300-V regulated rectifier contains capacitor C8-5 designed to ensure reliable operation of the rectifier under pulse load conditions.

-255-V regulated rectifier

The rectifier valve is actually kenotron Л8-11, type 5Ц4М. The A.C. voltage to the kenotron is furnished from the secondary winding of transformer Тр8-3. The rectified voltage is passed through filter C8-12, C8-13, Др8-5 to the regulator input.

The electronic regulator consists of the following valves: Л8-12(6П3С) - regulating, Л8-13(6Ж4) - control, Л8-14(СГ3С) - voltage regulator in the cathode circuit and circuit of divider R8-24, R8-25, R8-26.

The operating principle of the -255-V rectifier is similar to that of the +300-V rectifier. Resistors R8-31, R8-32, R8-33 connected in parallel with valve Л8-12 serve to alleviate the operating conditions of the valve by carrying part of the load current.

Potentiometer R8-25 located on the front of the unit panel and marked -255 V reg. (-255B ст.) is used to control the rectified regulated voltage.

Connected at the output of the -255-V rectifier is relay P8-1. The relay contacts supply 115 V to the primary windings of anode transformers Tp8-1 and Tp8-2, as well as to the primary winding of the high-voltage transformer in unit Д25. Functioning of the relay ensures delivery of all the rectified positive voltages after a negative voltage of -255 V has been applied to the radar. Upon failure of the -255-V channel, all the voltage delivered to the radar from unit Д8 are removed through the action of relay P8-1.

+350-V rectifier

The rectifier employs kenotron 5Ц4М (valve Л8-5). The A.C. voltage is applied to the valve from transformer Tp8-1. The rectified voltage is impressed on the output via filter C8-6, C8-7, Др8-2.

Potentiometer R8-35 is used for adjusting the magnitude of the +350-V output.

+140-V rectifier

The rectifier employs two kenotrons 5Ц3С (valves Л8-6, Л8-7) connected in parallel. Voltage to the anodes of the kenotrons is applied from transformer Tp8-1. The rectified voltage is impressed on the output via filter C8-8, C8-9, Др8-3.

Potentiometer R8-36 serves for adjusting the magnitude of the +140-V output.

+300-V rectifier

The rectifier employs two kenotrons 5Ц3С (valves Л8-9, Л8-10) connected in parallel. Voltage to the anodes of the kenotrons is applied from transformer Tp8-2. The rectified voltage is applied to the output via filter C8-10, C8-11, Др8-4.

Resistor R8-34 serves for more accurate adjustment of the magnitude of the output voltage.

Protective, signalling and check circuits

The unit provides for fuse protection of all the rectified voltage channels.

Channel +300 V reg. Пр8-1, fuse ПК-0.5 A
+300 V Пр8-2 fuse ПК-0.25 A
+140 V Пр8-3 fuse ПК-0.5 A
+300 V Пр8-4 fuse ПК-1 A
-255 V reg. Пр8-5 fuse ПК-0.25 A

The check circuits in the supply voltage circuits (115 V, 400 c.p.s.) and in the rectified voltage channels are provided with neon lamps, type МН-5.

115 V, 400 c.p.s. - lamp НЛ8-1
+300 V reg. lamp НЛ8-2
+350 V lamp НЛ8-3
+140 V lamp НЛ8-4
+300 V lamp НЛ8-5
-255 V lamp НЛ8-6

The unit allows all the voltages to be checked using test jacks Г located on the front panel of the unit.

Voltage 115 V, 400 c.p.s. is measured across jacks Г8-2, Г8-3.

The same voltage being applied to unit Д-25 is measured across jacks Г8-9, Г8-3.

The voltages below are measured:

+300 V reg., across jacks Г8-5 - Г8-1
+350 V reg., across jacks Г8-8 - Г8-1
+140 V reg., across jacks Г8-7 - Г8-1
+300 V reg., across jacks Г8-4 - Г8-1
-255 V reg., across jacks Г8-10 - Г8-1

Jack 8-1 is connected to the unit chassis.

9. Bank and Sight Stabilization Unit (Д-9)

Purpose

The function of unit Д9 is two-fold:

1. Bank stabilization of the homing antenna (unit Д1).

2. Control of the sighting antenna (unit Д-15М).

The operation of the unit is illustrated in the functional diagram (See Fig.84).

Bank stabilization of homing antenna

During straight and level flight, the error signal being applied to the input of the bank stabilization channel of unit Д-9 is zero.

The error signal is applied from the bridge diagonal (potentiometer sliders) formed by the bank potentiometer of the vertical gyro and check bank potentiometer (unit Д-1). The bridge operates on 40 V, 400 c.p.s.

When the aircraft is banking the body of the bank potentiometer in the vertical gyro is displaced relative to the potentiometer slider in proportion to the bank angle, which results in unbalance of the bridge.

The A.C. unbalance voltage of 400 c.p.s. (error signal) has an amplitude proportional to the angle of bank and a phase varying with the direction of the aircraft bank (when the bank direction changes, the phase is reversed). The unbalance voltage is applied to the input of the bank stabilization channel (unit Д-9). After passing the error signal amplifier with a differentiating circuit, phase detector, D.C. amplifier, it is amplified and converted to D.C. voltage, which is applied to the unit output.

The D.C. output is impressed on the armature of the bank actuating motor (unit Д1). The motor field winding is connected across 27 V. At small values of the error signal, the motor armature is furnished with D.C. voltage pulses, the direct component of the voltage being proportional to the magnitude of the error signal. Thus, the motor speed is proportional to the magnitude of the error signal; at larger values of the error signal 27 V D.C. is applied to the motor armature.

The bank motor, type ДК-11, drives round the antenna tilt journal axle so that the axle may occupy the horizontal position by moving simultaneously the slider of the check bank potentiometer toward decrease of the bridge unbalance voltage. With the journal axle in the horizontal position, the unput of unit Д9 will not be furnished with an error signal. As a result, voltage is not applied to the bank motor armature from the unit output and the motor comes to a standstill.

Synchronous and inphase rotation of sighting (unit Д-15M) and homing (unit Д1) antennas

The input of the sighting channel of unit Д9 is connected to the rotor of a selsyn transformer, type A-3, (unit Д-15M) which is coupled mechanically to the sighting antenna through gear ratio 1:1. The stator of the selsyn transformer is coupled to that of a transmitting selsyn, type A-3, located in the homing antenna.

The rotor of the transmitting selsyn is coupled through gear ratio 1:1 to the azimuth axle of the homing antenna. The rotor is fed with 40 V, 400 c.p.s.

When there is an angular difference between the rotors of the transmitting selsyn and selsyn transformer, a voltage is taken from the selsyn transformer rotor, proportional to the error angle between the selsyn rotors and reversing the phase with the sign of this angle. This voltage, being an error signal, is passed to the input of the sighting channel of unit Д9 in the way similar to the error signal coming to the input of the bank stabilization channel.

From the output of the signal channel the D.C. voltage is applied to the armature of the actuating motor coupled with the sighting antenna through a reduction gear.

The actuating motor turns the sighting antenna, and with it the rotor of the selsyn transformer toward decrease of the error angle thereby matching the position of the homing and the sighting antennas.

Description of basic circuit (Fig.85)

There are two similar channels - sighting and bank stabilization channels - having absolutely identical basic circuits (a slight difference mentioned below).

The assemblies common for both channels are:

Anode-filament transformer Tp9-1, selector switch B9-1, filters composed of induction coils L9-3, L9-4, L9-11 and respective capacitors.

Transformer Tp9-1 is rated for operation from the 115 V, 400 c.p.s. mains. It has four windings: one primary, and three secondary. Two secondary windings produce 350 V to supply the anodes of the phase detectors, i.e. to obtain reference voltage, one secondary winding feeds the filament circuits of all the valve and is used to produce an artificial error signal for tuning and testing circuits of the unit.

The similar component parts of both channels are:

1. Error signal amplifier - the left triode of valve Л9-1 (Л9-3), type 6H9C, with differentiating circuit formed by resistor R9-7 (R9-57) and capacitor C9-4 (C9-54).

2. Phase detector - the right triode of valve Л9-1 (Л9-3), type 6H9C.

3. D.C. amplifier - valves Л9-2 (Л9-4), type 6H8C.

4. Filters - induction coils L9-1, L9-2, L9-5, L9-6, L9-7, L9-8, L9-9, L9-10, L9-12, L9-13 and respective capacitors.

5. Relay amplifier - polarized relay P9-1 (P9-2), power relay P9-3 (P9-5) and P9-4 (P9-6).

6. Pilot lamps ЛН-1, ЛН-2 (ЛН-3, ЛН-4).

The unbracketed names apply to the sighting channel, and the bracketed ones, to the bank stabilization channel.

Unit supply

The unit operates on 115 V, 400 c.p.s.; 27 V D.C. and +300 V D.C.

Operation of sighting channel

Error signal amplifier

The error voltage is applied to the input of the unit - the 10th prong of connector ШВ9-1. Through switch B9-1 set at OPERATION (PAБОТА) and resistor R9-75 the error signal is passed to gain potentiometer R9-81. (If undamped oscillations occur in the closed-circuit system at the zero position, i.e. the system starts swinging, these oscillations can be stopped by cutting out gain potentiometer R9-81). From the potentiometer slider the error signal is fed to the input of the error signal amplifier (grid 1, valve Л9-1).

The error signal amplifier employs the left triode of valve 6H9C. The anode is supplied with +300 V D.C. The anode load is resistor R9-1, the decoupling filter is formed by capacitor C9-1 and resistor R9-2, the automatic bias resistor is cathode resistor R9-3.

The amplified error signal voltage is applied from anode 2 of valve Л9-1 to grid 4 of the same valve (the input of the phase detector) through a divider formed by capacitor C9-2 and resistor R9-6.

Fig.83 shows the voltage at the output of the error signal amplifier (grid 4 of valve Л9-1 plotted against the error signal applied to the input of the amplifier (grid 1 of valve Л9-1).

The curve tilt (Fig.87) determines the gain factor of the error signal amplifier.

Phase detector

The phase detector employs the right triode of valve Л9-1.

The anode of the phase detector is fed with 300 V, 400 c.p.s. from transformer Tp9-1. The anode load is resistor R9-5, and cathode resistor is resistor R9-4.

A pulsating voltage is obtained across load resistor R9-5 whose magnitude always remains constant in the absence of the error signal. The A.C. component is taken by shunting resistor R9-5 with capacitor C9-8. The D.C. component of the voltage is

applied through differentiating circuit R9-7, C9-4 to the input of the D.C. amplifier (grid 1 of valve Л9-2).

At a considerable lag in the regulating device of the follow-up system, part of which is one of the channels (the sighting channel in the case concerned) of unit Д9, undamped oscillations are likely to occur in the system when the sighting antenna is controlled in proportion to the former error, i.e. there exists a lag between the error time and the time efforts are applied to cancel this error. To reduce the lag in the regulating device, a differentiating circuit (R9-7, C9-4) is provided which gives a leading phase displacement to the error voltage within the frequency band at which oscillations may occur.

The voltage taken from resistor R9-5 is negative.

An A.C. voltage of 300 V, 400 c.p.s. applied to the anode of the phase detector may be considered as a reference voltage relative to which an error voltage impressed on the input of the error signal amplifier is in phase or in antiphase. If the error voltage is in phase with the reference voltage, the voltage on grid 4 of valve Л9-1 (phase detector input) will be in antiphase with the reference voltage and magnitude of the negative voltage drop across resistor R9-5 will decrease. As the error voltage increases, the voltage drop across resistor R9-5 decreases respectively. If the error voltage is in antiphase with the reference voltage, the negative voltage drop across resistor R9-5 increases and will rise as the error voltage rises.

Apart from being fed with the negative voltage depending upon the phase and amplitude of the incoming error voltage, the input of the D.C. amplifier (grid 1 of valve Л9-2) is fed with a positive voltage constant in magnitude. This voltage via resistor R9-8 is taken from the slider of potentiometer R9-79 (BALANCE) which together with resistors R9-9 and R9-10 constitute a voltage divider for the +300-V supply. The resultant voltage is positive.

The resultant voltage at the output of the phase detector (input of the D.C. amplifier, grid 1 of valve Л9-2) and its components plotted against the error voltage applied to the input of the error signal amplifier are shown in Fig.88. Fig.89 shows the output voltage of the phase detector plotted against its input voltage. The curve tilt of Fig.89 determines the gain factor of the phase detector.

D.C. amplifier

The D.C. amplifier employs double triode 6H8C (valve Л9-2); the valve has an unbalanced voltage input and a balanced current output. Its anodes are supplied with 300 V D.C. through the control windings of polarized relay P9-1 and resistors R9-18 and R9-19. The relay control windings and the resistors are the anode loads of the D.C. amplifier.

The balanced current output is determined by the voltage at grid 4 of the right triode of the amplifier varying with the incoming signal inversely to the voltage change at grids of the left triode, i.e. a voltage increase at grid 1 corresponds to a voltage decrease at grid 4 and vice versa.

This results from grid 4 being connected via resistor R9-13 to voltage divider R9-12, R9-14 which is coupled to the anode of the left triode.

Opposite changes of voltages at grids 1 and 4 of the D.C. amplifier agree with opposite changes of the anode currents.

In the absence of an error signal the currents flowing through the left and right triodes of the amplifier and hence through the control windings of the polarized relay must be equal. This is achieved by rotating potentiometer R9-79 BALANCE.

Rotation of potentiometer R9-79 while changing the positive component of the voltage on grid 1 equalizes the voltages on grids 1 and 4 before the moment when the currents through both portions of the valve become equal. In this case, the grid potential to cathode is 8 V.

When the error signal is furnished to the channel input, the currents through the left and right triodes will be reversed.

The nature of current reversal versus the voltage at the input of the D.C. amplifier (grid 1) is shown in Fig.90.

The curve tilt (Fig.90) determines the D.C. amplifier transconductance.

Relay amplifier

The relay amplifier is composed of three relays - polarized relay P9-1 (type PП-5) and two power relays P9-3 and P9-4 (type PЛ-5M).

The polarized relay has three windings: two control windings (3, 4 and 5, 6) and one feedback winding (1, 2). The armature relay occupies the neutral position and closes the contacts in the extreme positions upon operation of the relay.

The power relay has one working winding and one switching contact unit.

In the absence of the error signal, the currents through the control windings of the polarized relay are equal (See Fig.90). The magnetic fluxes set up by these windings are equal, too, and oppose each other.

As a consequence, the resultant magnetic flux is zero and the relay armature is in the neutral position.

When the channel input is fed with an error signal small in magnitude that increases the current in winding 6-5 and decreases accordingly the current in winding 3-4, the resultant magnetic flux caused by the current difference in these windings forces the relay to shift the armature from the neutral to an extreme position and close contacts Л and Я. In this case, +27 V is applied to the relay P9-3 winding. Relay P9-4 changes over its contact unit to supply +27 V to the armature of the actuating motor in the sighting antenna. -27V received by the armature from the contacts of relay P9-3 whose armature is released. Power to the motor field winding is supplied in the sighting antenna.

From potentiometer R9-17 connected in parallel with the contact of the power relays feeding the armature of the actuating motor part of the voltage is taken to the feedback winding (1, 2) of the polarized relay.

The magnetic flux set up by the feedback winding opposes the resultant magnetic flux of the control windings (3, 4 and 6, 5) and compensates for it. The relay armature is reset to the neutral position breaking the power supply circuit of relay P9-4 winding.

Relay P9-4 drops out, the output voltage disappears, and with it the current and magnetic flux of the feedback winding. The resultant magnetic flux of the control windings acts again and the relay armature closes contacts П and Я. The armature of the actuating motor is fed with the second voltage pulse, and further the relays operate in the vibrating condition.

The D.C. component of the voltage applied to the armature of the actuating motor in unit Д15 is small, the motor rotates the sighting antenna at a low speed to reduce the magnitude of the error signal. This results in a decrease of the voltage applied from the output of the sighting and bank stabilisation unit (Д9) to the motor armature in the sighting antenna unit (Д-15М) when the homing and the sighting antennas are matched, and the motor stops. If the error signal applied to the input of unit Д9 is larger, the pulse width increases and so does the D.C. component of the voltage applied to the sighting antenna motor. The motor starts rotating the antenna at a higher speed. At a certain, still larger value of the error signal the motor rotates at full speed. This is because the magnetic flux produced by the feedback winding in the polarized relay is not capable of compensating for the magnetic flux caused by the current difference in the control windings, and the relay remains attracted all the time. As a result, relay P9-4 is permanently engaged in operation and +27 V D.C. is applied to the armature of the actuating motor.

If the homing antenna starts rotating in the opposite direction, the input of unit Д9 is furnished with an error signal in anti-phase. This increases the current in winding 3, 4 of the polarized relay and decreases accordingly the current in winding 6, 5. The polarized relay together with the power relay operate similarly and this is the condition at which contacts Я and Л of the polarized relay close, power relay P9-3 functions and the armature of the actuating motor in the sighting antenna unit is fed with the voltage of the reversed polarity.

The waveforms of the pulses furnished to the armature of the actuating motor in the vibrating condition of the unit Д -15M relay and the error pulse corresponding to them are shown in Fig.91.

The frequency of the same pulse depending upon the error signal is shown in Fig.92.

The value of the vibrating condition determines the range of proportional regulation of the system. This characteristic is dependent upon the feedback control in the relay amplifier. The feedback control is effected by potentiometer R9-17. The vibrating condition valve decreases as the voltage applied to the feedback winding of the polarized relay decreases.

The output voltage of the unit versus the current difference in the polarized relay control windings is shown in Fig.93.

The curve tilt of Fig.93 determines the transmission factor of the relay amplifier.

The output voltage of the unit versus the error signal is shown in Fig.94.

Damping is effected by capacitor C9-5 connected between the anodes of the D.C. amplifier.

In the relay amplifier pi-filters (L, C) are used on all the incoming and outgoing leads.

Contact units of relays P9-1, P9-3, P9-4 have spark-control circuits composed of capacitors C9-9, C9-10, C9-11, C9-12 and resistors R9-20, R9-21, R9-22, R9-23.

Capacitor C9-8 bridged across the output leads with capacitors C9-9 and C9-10 in the spark-control circuits reduces the noise produced by the contact units of power relays P9-3 and P9-4.

The sighting channel in unit Д9 functions only when the radar is operated in MANUAL II and AUTOTRACKING modes. This is achieved by that the input of unit Д9 is connected to the rotor of the selsyn situated in unit Д-15M only in these modes via the contacts of relay P12-5 installed in the distribution box.

Operation of bank stabilization channel

The bank stabilization channel operates in essentially the same way as the sighting channel. The only difference is that +27 V is applied to the contacts of the power relays through the pilot switches installed in the extreme positions of the bank check potentiometer in unit Д1.

Checking Unit Performance

Protective and signalling circuits

To check the performance, tuning and adjustment of the unit use is made of transformer Tp9-1 to the filament winding of which are connected potentiometers CHECK (КОНТРОЛЬ) R9-77 for the sighting channel and R9-78 for the bank stabilization channel.

From the sliders of these potentiometers an artificial error signal may be furnished to potentiometers GAIN (УСИЛЕНИЕ) of both channels with switch B9-1 in position CHECK. Rotating the sliders of potentiometers CHECK varies the artificial error signal in amplitude and phase. In the presence of the artificial error signal the unit performance is checked by pilot lamps ЛН, type CM-30.

Lamps ЛН9-1 and ЛН 9-2 are in the sighting channel and light up depending on the polarity of the voltage applied to the armature of the actuating motor and hence indicate the sense of rotation of the motor.

Lamps ЛН9-3 and ЛН9-4 serve the same purpose in the bank channel.

Lamps НЛ9-2 and НЛ9-1, type МН-5, indicate 115 V, 400 c.p.s. and 300 V D.C. The 300-V circuit is protected by a 0.25-A fuse, type ПК.

For both channels test jacks Г1 and Г2 allow monitoring of the error signal voltage at the input of the error signal amplifiers.

Jacks 1,4 allow monitoring of the input voltages of the phase detectors; jacks 2, 3, 5, 6, voltages on the control grids of the D.C. amplifiers; test jacks Г-3, Г-5 and Г-4, Г-6, the voltage between the anodes of each D.C. amplifier.; jacks 7, 8, the voltage across potentiometers CHECK (presence of an artificial error signal and filament voltage of transformer Тр9-1).

Course and pitch stabilization unit (Д-19)

The course and pitch stabilization unit is electrically and structurally identical with the bank and sighting stabilization unit. Therefore the unit functioning, i.e. conversion of the error signal voltage applied to the input of the unit into the D.C. voltage taken from the unit output is completely analogous to the functioning of unit Д-9, the functioning of the course stabilizing channel is analogous to that of the sighting channel, and functioning of the pitch stabilization channel, to that of the bank stabilization channel.

10. Tracking Unit Д-10

1. Purpose

The tracking unit is intended to control the homing antenna in all the operating modes of radar К-IIМ.

During automatic tracking error signals are separated in the unit from the selected signals coming in from the selector output of unit Д7 and converted to D.C. signals controlling the azimuth and elevation amplidynes in the automatic tracking modes.

Apart from the above, the feedback signals are amplified in the tracking unit which are then added to the main signals, and control voltage is shaped for application to the tracking motor in the autotracking and circular scanning modes. Besides, shaping of control currents is made in the unit flowing through the control winding of the azimuth amplidyne during sector scanning and manual laying.

2. Functional and basic circuits

With regard to the functions performed the unit wiring can be divided into the following circuits:

1. Circuit separating the error signal from the selected signals which includes the following elements:

(a) AGC detector employing valve Л16;

(b) resonance T-section filter RC adjusted to frequency 10 c.p.s.;

(c) error signal amplifier and phase inverter employing valve Л10-17.

2. Circuits controlling the azimuth and elevation amplidynes, each circuit including the following similar elements:

(a) phase detector with a filter in the azimuth channel employing valves Л10-1 and Л10-2, in the elevation channel, valves Л10-8 and Л10-9;

(b) shaping stage (squarer) employing valve Л10-3 in the azimuth channel and valve Л10-10 in the elevation channel;

(c) D.C. amplifier in the azimuth channel employing valves Л10-4, Л10-5, in the elevation channel, valves Л10-11 and Л10-12.

3. Circuits of the feedback amplifiers in the azimuth channel employing valve Л10-7 and in the elevation channel, valve Л10-14. Each circuit contains a damping amplifier allowing balancing of the D.C. amplifier used in the main circuit controlling the amplidynes.

4. Circuits amplifying error signals obtained from the selsyns during manual laying, each consisting of an amplifier and phase inverter:

employing valve Л10-15 in the azimuth channel and valve Л10-13 in the elevation channel.

5. Tracking circuit consists of an amplifier and a phase inverter employing valve Л10-15 (use is made of the selsyn error signal amplifier in the azimuth circuit) and a power amplifier employing valve Л10-18.

6. Sector scanning circuit which includes the following elements:

(a) phase detector employing valve Л10-21;

(b) delayed multivibrator employing valve Л10-20 and

(c) D.C. amplifier Л10-19.

The block diagram of the tracking unit is shown in Fig.95 (for convenience relays P10-1 and P10-2 in the diagram are substituted by switches). The schematic diagram of the unit is shown in Fig.108.

Separation of error signal from selected signal

During automatic tracking the circuit input is fed with selected signals from the radar channel. In the presence of an error signal these positive pulses following at frequency n_2 are modulated by frequency Ω c.p.s.

The modulation depth during automatic target tracking may vary from 0 to 10 per cent. When the target is locked on the modulation depth may reach as much as 100 per cent and even cause fading of a series of video pulses.

The input stage of the error signal circuit employing pentode 6K3 has a remote cutoff characteristic and performs detection, amplification and automatic gain control of the error signal.

Used as an error signal detector is the grid circuit (control grid - cathode) of valve Л10-16. When positive selected signals are furnished to the detector input, the grid circuit of valve Л10-16 in the presence of pulses will carry grid current and capacitor C10-13 will acquire charge through the internal resistance (grid-cathode) of the valve and load

resistor of the cathode follower at the output of the radar of the autoselector (unit Д7) (See Fig.96).

During the resting time the capacitor is going to discharge through gridleak R10-25 and the load resistor of the cathode follower. The discharge time constant is much in excess of the charge time constant which was included in the grid-cathode section, load resistor of the cathode follower and capacitor C10-13.

The action produced by the widened grid pulses on the stage due to provision of capacitor C10-11 in the anode circuit is equivalent to the D.C. component being applied to the grid. The component is modulated by the low frequency of the video pulse envelope.

Since valve 6K3 has a remote cutoff characteristic and operation is performed with a sufficiently large amplitude of the input pulses and automatic grid bias, the automatic gain control in the stage is effected by the signal. This ensures constancy of the error signal amplitude at the input of valve Л10-16 when the modulation depth of the selected signals is constant.

Automatic gain control of error signal separation stage

As seen from Fig.96 the control grid of valve Л10-16, type 6K3, is coupled to resistor R10-25, and the valve cathode is earthed. The average value of the rectified voltage of the grid detector is the negative bias on the control grid of valve 6K3 (Л10-16).

The operating point of valve 6K3 is selected so, that the fixed bias on the control grid may be of the order of 0.5 V.

Changing the intensity of the picked-up signals changes the average value of the rectified voltage across the detector load and hence the bias on the valve grid.

On reception of weaker signals the grid bias of the valve decreases so that the gain factor of the stage increases (See Fig.99). On reception of stronger signals the grid bias of the valve increases and the stage gain factor decreases.

As a result, the signal amplitude in the anode circuit appears to be independent of the intensity of the picked-up signals.

As seen from Fig.99 the A.C. component of the anode current of valve 6K3 is found independent of the input signal level.

To make stable the AGC regulating characteristic the screen grid of valve Л10-16 is fed with a stabilized voltage of +105 V from stabilivolt СГ3С (Л10-22) (See Fig.100).

The error signal from anode of valve Л10-16 is taken to the amplifier and then to the phase inverter employing the triodes of valves Л10-17.

The anode load of the amplifying portion of the valve is resistor R10-31 and T-section image filter RC tuned to error signal frequency Ю c.p.s.

Filter RC is essentially two symmetrical T-shaped meshes connected in parallel (Fig.97).

Tuning to resonance frequency is effected by variable resistors R10-28 and R10-29.

This filter is used to apply negative voltage feedback to the grid of amplifying valve Л10-17.

At the resonance frequency the filter offers a large resistance, and the voltage fed in anti-phase from the anode to the grid of valve Л10-17 is low. At all other frequencies heavy feedback sharply reduces the voltage gain factor.

The amplified error voltage is applied to the grid of the left portion of valve Л10-17 functioning as a phase inverter.

The gain factor of the anode and cathode arms of the phase inverter is near to unity. The knob of variable resistor R10-131 serves to set the required error voltage gain factor during automatic tracking.

Circuits controlling azimuth and elevation amplidynes in autotracking

In automatic tracking when relay P10-1 is caused to operate, azimuth phase detectors Л10-1, Л10-2 and elevation phase

detectors Л10-8, Л10-9 are supplied with an error voltage from the phase inverter employing the left portion of valve Л10-17.

The phase detectors shape azimuth and elevation control voltages out of the alternating error voltage and alternating reference voltages. The circuits of both channels are identical, therefore it suffices to confine ourselves to consideration of the azimuth circuit.

The phase detector uses two double triodes 6H9C. The control grids of these triodes are fed with the output voltage of the phase inverter.

The square-wave voltage is applied to the anodes of the phase detector from the anode loads of the squarer using valve Л10-3.

The reference voltage to the grids of the squarer is applied from the reference voltage generator in unit Д.

In other words, the elevation reference voltage is 90° out of phase with the azimuth reference voltage.

Fig.98 shows the simplified diagram of the phase detector and squarer.

The squarer and the phase detector function as follows:

The squarer employs valve 6H9C behaving as an overdriven amplifier.

The squarer grids are supplied with a sinusoidal voltage of the order of 80 V. In the positive alternation the sine-wave is clipped by saturation of current, and in the negative alternation by driving the valve to cut off. Thus, as a result of the squarer operation, its anodes are maintained at square voltages which after being applied to the phase detector ensure more accurate operation of the latter.

From the anodes of the squarer the square voltages are impressed on the anodes of both valves of the phase detector connected to each other. One triode is conducting, the other is cut off.

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Since the cathodes of the phase detector triodes are interconnected, then irrespective of the fact which triode is open cathode loads R10-49 and R10-50 will carry direct anode current developing a voltage drop of about +76 V. D.C. component of about +75 V is fed from the phase inverter to the grids of phase detector valves. Thus the bias on the valve grids of the phase detector will be approximately -1 V.

The phase detector circuit has a cathode output, that is why its gain factor is less than unity.

When an error voltage is applied to the phase detector, its output voltage will be the result of the comparison of the error voltage phase with the phase of the reference voltages obtained from the squarer anodes.

Consider operation of the phase detector when its grids are fed with an error signal. Fig.101 presents the curves showing the voltage wareforms in various parts of the circuit when the error signal and the reference voltage are in phase.

The error voltages on grids 1 and 4 of valve Л10-2 are in phase with the respective voltages on anodes 2 and 5. The error voltages on grids 1 and 4 of valve Л10-1 are in anti-phase with the respective anode voltages. In the first alternation the right portion of valve Л10-1 and left portion of valve Л10-2 are open. The magnitude of the anode current of the valves will be determined only by the voltages on grid 4 of valve Л10-1 and grid 1 of valve Л10-2.

Since grid 1 of valve Л10-2 is fed with the positive alternation, and grid 4 of valve Л10-1 with the negative one, the current flowing through resistor R10-50 increases and the current flowing through resistor R10-49 decreases. The cathode potential of valve Л10-2 rises and that of valve Л10-1 falls. In the next alternation the left portion of valve Л10-1 and right portion of valve Л10-2 appear to be conducting. Grid 1 of valve Л10-1 is furnished with the negative alternation, and grid 4 of valve Л10-2, with the positive one. The current passing through resistor R10-50 increases again, and the current passing through resistor R10-49 decreases accordingly.

In the second alternation the voltage across the cathode resistors will change in the same way as in the first alternation. Thus, when the error signal and the reference voltage are in phase, the D.C. component of the cathode voltage of valve Л10-2 increases, and that of valve Л10-1 decreases.

The average potential of the cathode of valve Л10-2 obtained during the cycle is, in effect, the control voltage applied to the D.C. amplifier. The reversal of the error voltage phase will result in a voltage drop on the cathode of valve Л10-2 and a voltage rise on the cathode of valve Л10-1, i.e. in the change of polarity of the D.C. amplifier control voltage.

Operation of the phase detector when the error signal is 90° out of phase with the reference voltage is shown in Figs 103 and 104.

During the first quarter of the cycle the right portion of valve Л10-1 and left portion of valve Л10-2 are conducting. The magnitude of the valve anode current will be determined only by the voltage on grid 4 of valve Л10-1 and 1 of valve Л10-2. Since grid 1 is furnished with the positive alternation, and grid 4 with the negative alternation, the current flowing through resistor R10-50 increases, and current flowing through resistor R10-49 decreases. The cathode potential of valve Л10-2 rises, and that of valve Л10-1 falls.

At the time moment corresponding to 90° phase, the left portion of valve Л10-1 and right portion of valve Л10-2 will be momentarily initiated into conduction. In the consequence of this action grid 1 of valve Л10-1 and grid 4 of valve Л10-2 will start controlling the anode current of the valves. Since the voltage on grid 1 is a maximum and that on grid 4 is a minimum, the cathode potential of valve Л10-1 will rise, and the cathode potential of valve Л10-2 will abruptly fall to minimum. The left portion of valve Л10-1 and the right portion of valve Л10-2 will be made to

conduct in the interval between the phase angles from 90° to 270° as a result of which grids 1 and 4 will control the anode current of the valve.

The cathode potentials change with the grid potentials.

At 270° the cathode potential of valve Л10-2 is a maximum and that of valve Л10-1 is a minimum. The change of the anode voltage at 270° will result in that the right portion of valve Л10-1 and left portion of valve Л10-2 are open, and the anode currents are controlled by grid 4 of valve Л10-1 and grid 1 of valve Л10-2. Since at this moment the potential of grid 4 is a maximum, and the potential of grid 1 is a minimum, the cathode potential of valve Л10-1 rises sharply and that of valve Л10-2 falls to a minimum.

From this time on to the end of the period the cathode potential of valve Л10-1 will change with the change of potential of grid 4, and the cathode potential of valve Л10-2 with the change of potential of grid 1.

The cathode voltage curve (See Fig.104) being symmetrical in relation to the initial level, the average value of both cathode potentials remains the same as it was with no error voltage applied to the grids.

Thus, when the error voltage is shifted in phase by ±90° with respect to the reference voltage, the control voltage is zero.

From the above two examples it follows that if the azimuth phase detector is furnished with an error signal caused by the target displacement in azimuth it will be in phase with the azimuth reference voltage and the azimuth control voltage is obtainable from the output. This error signal creates no control voltage in the elevation phase detector, since the elevation reference voltage is quadrature-shifted with the azimuth reference voltage. Since during automatic tracking the grids of the squarers in the azimuth and elevation channels are fed with voltages shifted in phase by 90° with each other at

quency D, the output voltages of the detectors are found proportional to cos φ for the elevation phase detector and to sin φ for the azimuth phase detector, where φ is the angle between the axis of the equisignal zone and target direction.

The phase detector output voltage is fed to the control grids of the balanced amplifier through filter R10-51, C10-24, and R10-52, C10-25.

The output control voltages of the azimuth and elevation phase detectors may be set equal by means of resistors R10-49, R10-50, R10-73 and R10-74, which allows to set the equal rate of following up in both directions.

The D.C. balanced amplifiers in the azimuth and elevation channels employ tetrodes 6П6С. The circuits of the channels are identical, therefore functioning of the azimuth D.C. amplifier is only considered.

The anode loads of the D.C. amplifier valves are the control windings of the amplidynes.

The cathodes of the D.C. amplifier valves are coupled together and a voltage drop of about +81 V is developed across resistors R10-56 and R10-57.

The magnitude of this voltage drop is independent of unbalance since the D.C. amplifier is a balanced unit (the increase of voltage on one grid is inversely proportional to the voltage drop on the other, the resultant current through the cathode load remains invariable).

In the absence of an error signal the control grids of the D.C. amplifier are supplied with approx. +76 V. Thus the initial bias of the D.C. amplifier valves will be about -5 V. The variable resistor in series with the fixed one serves for adjusting this operating voltage.

Balance of the D.C. amplifier is achieved by the adjustment of screen voltages carried out with the damping amplifier and potentiometers located in the control panel.

When the error voltage is zero or 90° out of phase with the reference voltage, the anode currents of the D.C.

amplifier are equal and oppose each other. In this instance, the output voltage of the amplidyne is zero.

The unbalance voltage which is the result of the phase detector functioning causes a proportional unbalance of currents in the control windings of the amplidyne, the signs of unbalance of these current changing with the reversal of polarity in the phase detector output voltage. This, in turn, causes appearance of the corresponding voltage turning off the homing antenna.

Antihunt circuits and balancing in azimuth and elevation channels

To ensure stability and the required dynamic characteristics of the system incorporating tracking unit Д10 and homing antenna Д1, it is provided with antihunt circuits (flexible feedback) by means of which regulated voltages are injected. The simplified feedback and balance circuit diagram is shown in Fig.105.

As the feedback circuits in both channels are identical, only the azimuth channel is under consideration.

The feedback voltage is applied to the grid of the left portion of the feedback damping amplifier valve Л10-7 from the armature terminals of the azimuth actuating motors via special circuits RC in unit Д-13М, the grid of the right portion is fed with balancing voltage of +27 V from the divider in the control panel.

Load resistors R10-58 and R10-59 in the anode circuits of the damping amplifier are at the same time balance resistors of the D.C. amplifier screen grids.

In the common cathode circuit of valve Л10-7 a voltage drop of about +32 V is developed across resistor R10-66. Thus, the grid bias on both grids will be approximately -5 V. This resistor in the cathode ensures a balanced input of the amplifier with the unbalanced input.

When the anode currents of the damping amplifier are equal, the potentials of the D.C. amplifier screen grids will be equal and will not cause unbalance of the anode currents passing the control windings of the amplidyne.

Potential to the grid of the right-hand triode of the damping amplifier is applied from variable resistor R11-51. By changing its value it is possible to set a fine balance of the D.C. amplifier anode currents, thereby compensating for the spread in characteristics of the valves and circuit elements.

When a positive signal appears on the grid of the left triode of the damping amplifier, the anode current increases, which in turn, causes a voltage drop on the screen grid of the D.C. amplifier.

But even the screen grid potential of the D.C. amplifier second valve does not remain constant.

Indeed, the bias rise on the grid of the right triode applied from the cathode circuit causes the anode current of the right triode to decrease thereby increasing the screen grid potential of the D.C. amplifier.

As a result of such variation of the screen potentials a symmetrical unbalance of the D.C. amplifier anode currents is obtained.

This unbalance is furnished reversed to the D.C. amplifier to compensate for the unbalance caused by the error voltage. This damps the diverging oscillations of the entire system.

Changing to circular scanning is effected by unbalancing. The unbalancing voltage is supplied from a voltage divider located in the control panel. The degree of unbalance determines the rate of scanning.

Variation of the degree of unbalance during circular scanning is carried out by means of potentiometer R11-50 RATE OF CIRCULAR SCANNING (СКОР.КРУГ.ПОИСКА) located in the control panel.

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TECHNICAL DESCRIPTION OF THE
RADAR K-1M
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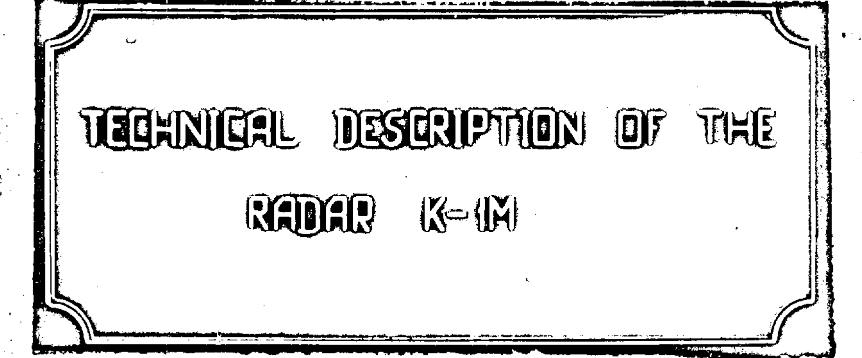
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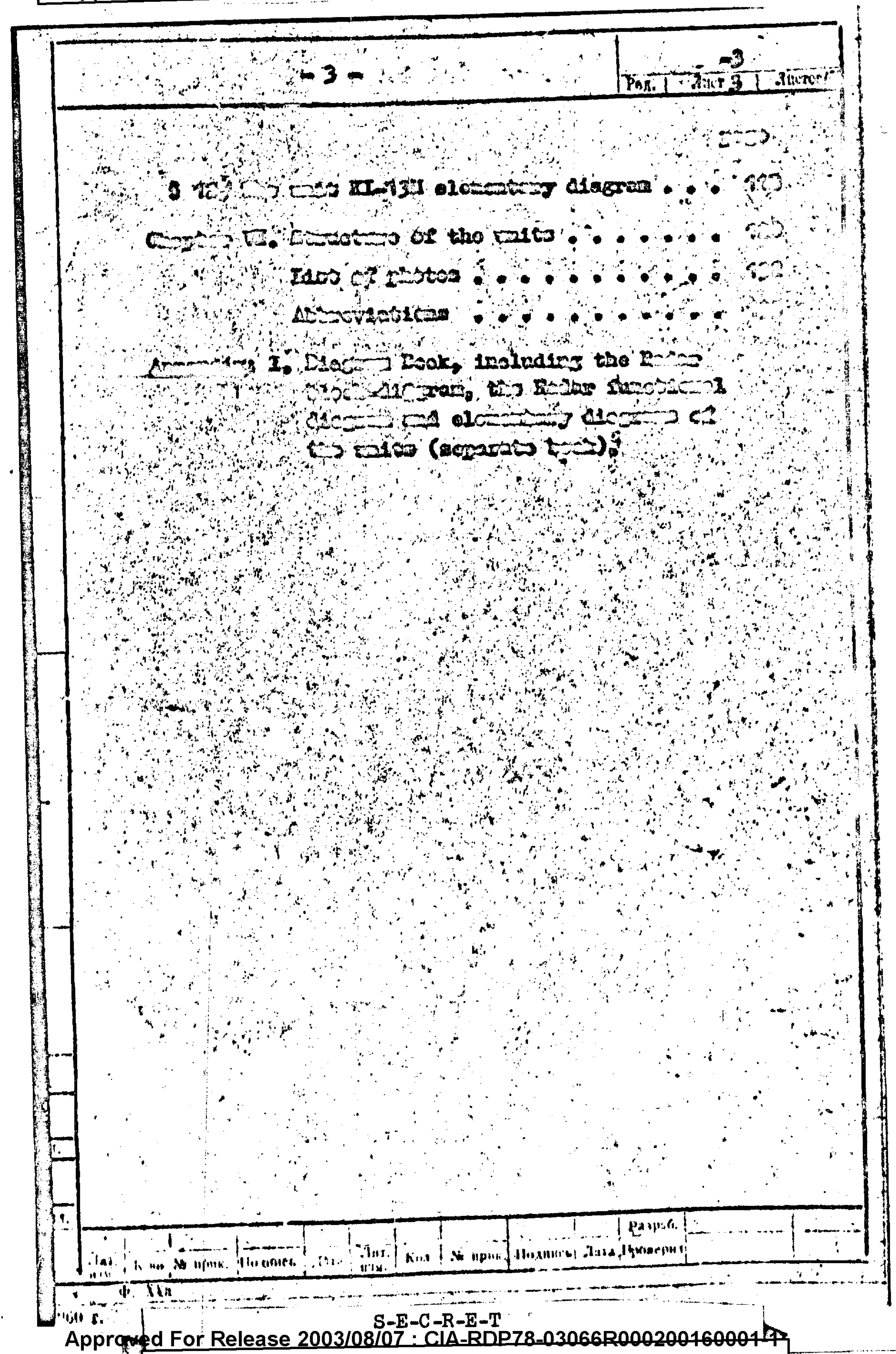
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CONTENTS



CHAPTER I

Radar K-IM purpose

The Radar K-IM forms part of the radio control system "Comet" and is located on the type "KC" missile.

In transporting the missile under fighting conditions, a specially equipped mother-ship is used; the missile should be supported from a lug under the mother-ship plane.

A special guidance Radar K-IIM is located in the mother-ship.

The Radar K-IM provides:

I. The missile guidance by controlling the autopilot in two regimes:

"A" regime - the beam-riding guidance.

"B" regime - the semi-active homing.

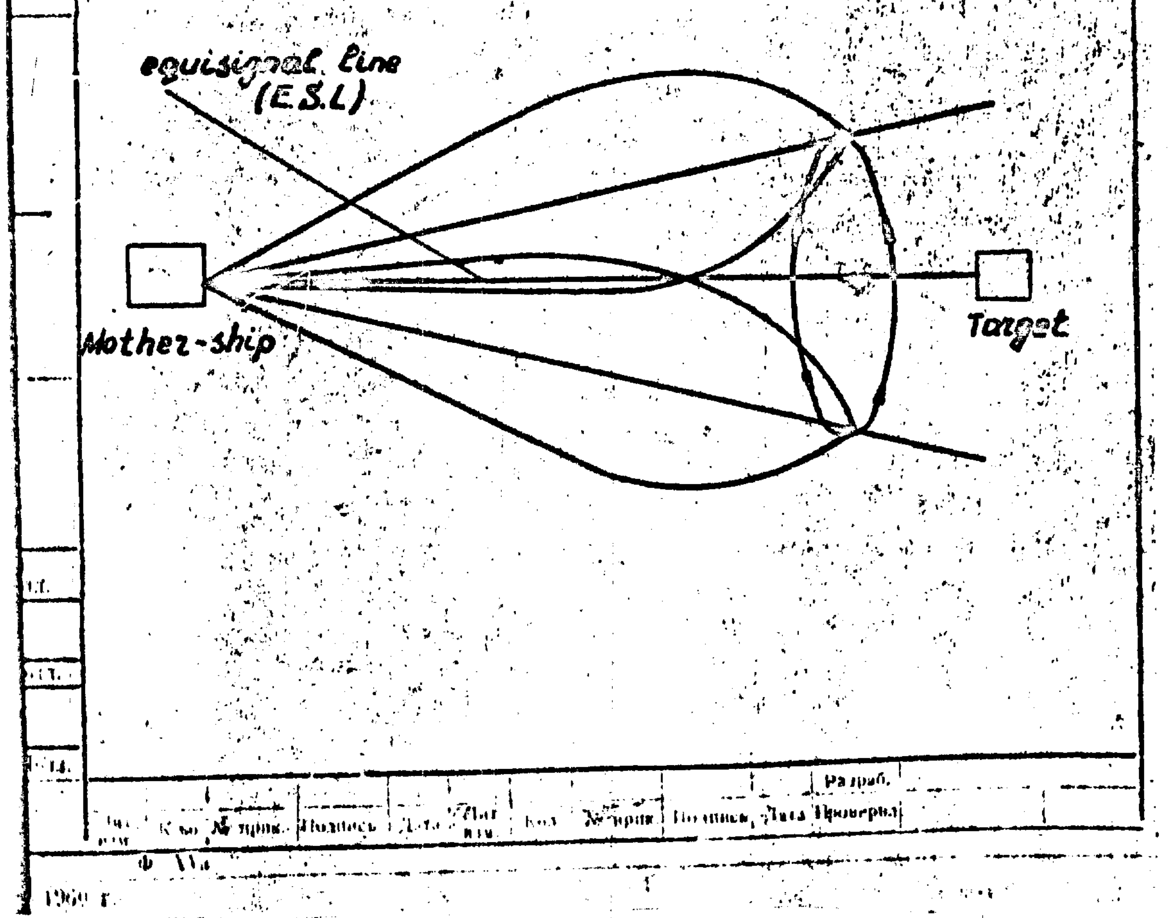
2. Tracking beacon signals, determining missile position in the beam, distance between the missile and the target and communicating command N 2 realization and target damage accuracy.

CHAPTER II

K-IM Operation Principle

When mother-ship is in flight, the Radar K-IIM carries out the search of target. After detecting and selecting the target, the Radar K-IIM starts looking on and tracking the target.

Beam of the Radar K-IIM transmitter antenna is [illegible] conically due to the antenna exiter rotating at the [illegible] cy = 10 = .



The half- power line of the radiation pattern serves as an axis of this cone. So appears a spatial line (equisignal line) which is used for missile guiding.

When the distance between the mother-ship and the target reaches a predetermined value, "KC" jet engine is fired and "KC" is dropped.

Radar K-IM operates in 3 regimes:

I. Autonomy regime (beam - entry regime);

2. "A" regime (beam-riding guidance);

3. "B" regime (semy-active homing).

I. Autonomy regime

The autonomy or beam-entry regime is lasting 39 ± 2 sec. from the moment of dropping the missile until the missile enters the beam of K-IIM Radar.

In the autonomy regime the Radar K-IM does not control the missile flight; the latter is controlled by the programme controller of the autopilot. Unit KI-6M time-motor initiates the command N I and commutates the autopilot into course and elevation radar guiding in 39 ± 2 sec. after dropping the missile.

2. "A" regime

The regime "A" starts from the moment of realising the command N I and is lasting up to "B" - regime switching on. The missile is radio controlled by the course and elevation channels.

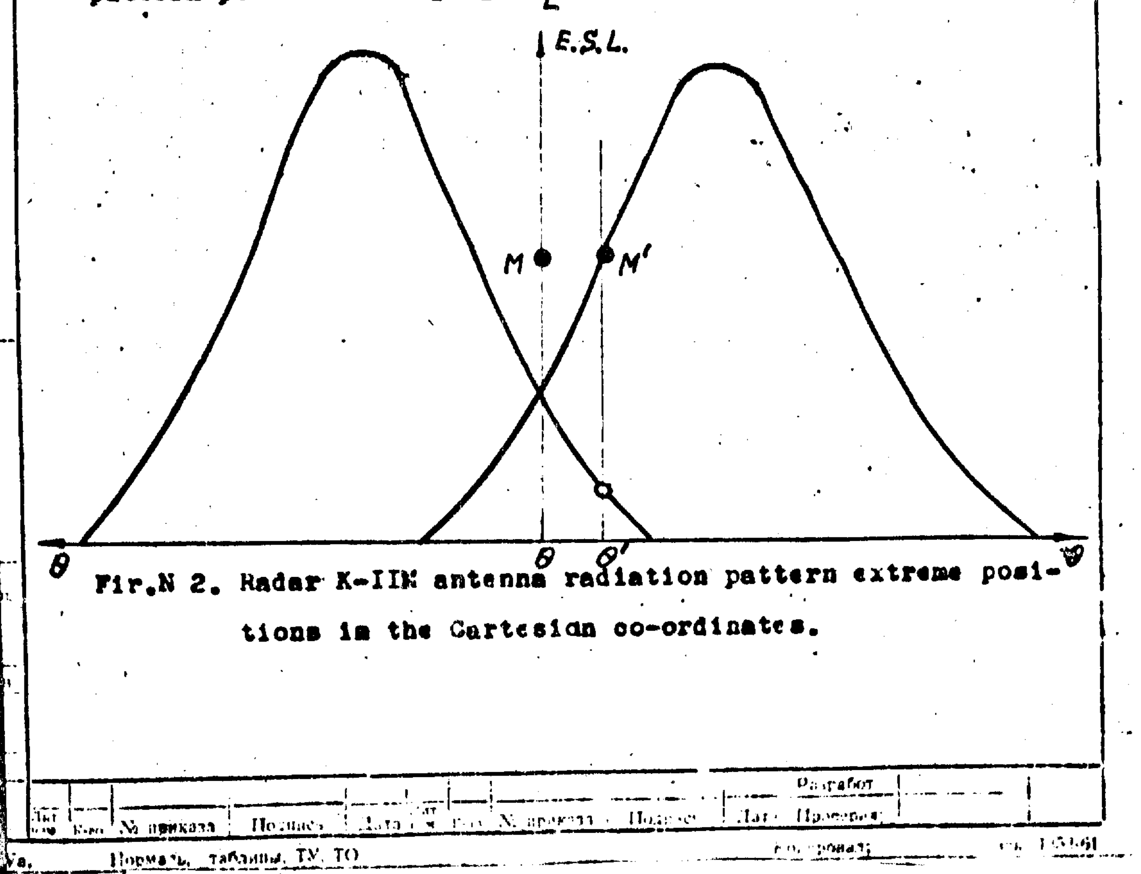
In this regime the Radar K-IM provides driving voltages to the autopilot.

The dependance of the voltage value on the missile deviation from the equisignal line is linear, and voltage polarity conforms to the missile deviation direction relative to the E.S.L.

The driving voltages actuate the control surfaces through the autopilot and return the missile to the equisignal zone.

Let us examine fig N 2. K-IIM antenna scanning beam section on the horizontal plane is shown on fig. N 2. If the missile position is on equisignal line the U.H.F. signal power remains invariable during the scan period.

In other directions (for example M direction) mixer "A" input signal power will change in accordance with radiation pattern position changing.



Fir.N 2. Radar K-IIM antenna radiation pattern extreme positions in the Cartesian co-ordinates.

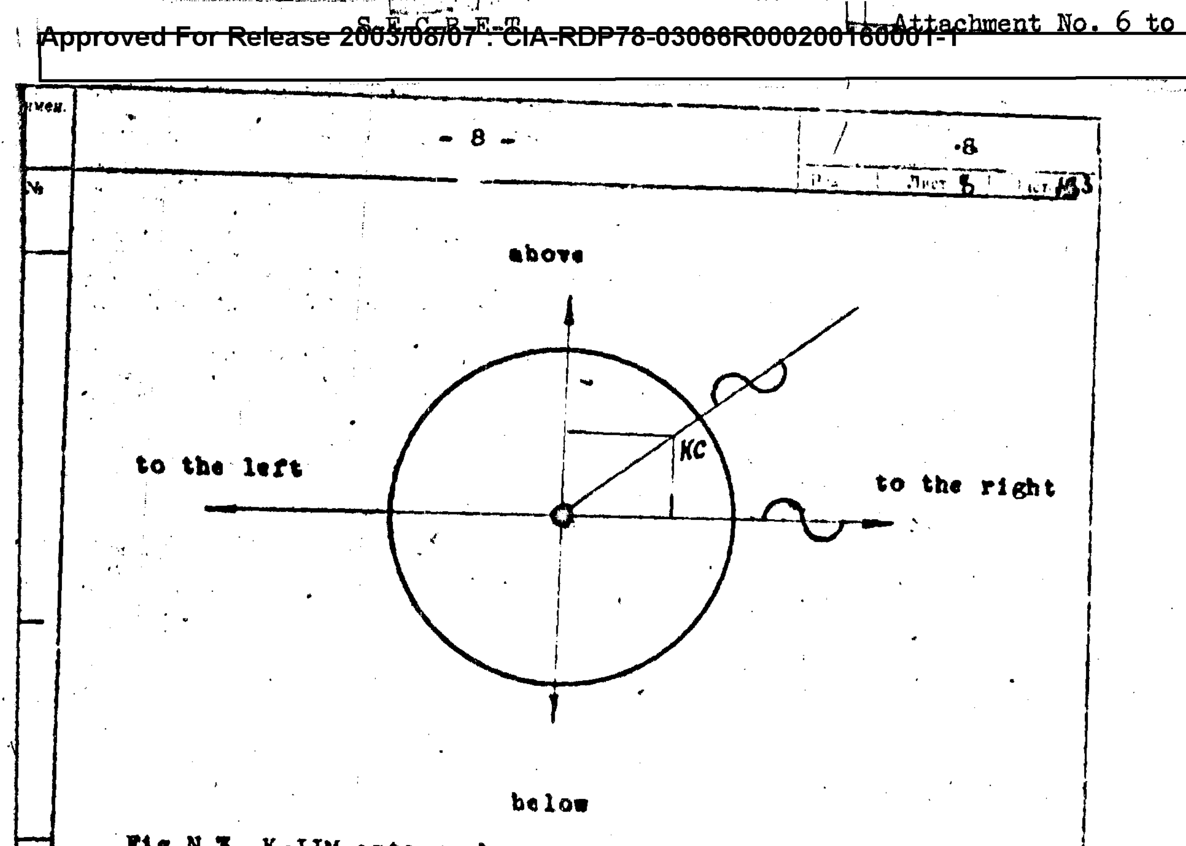


Fig.N 3. K-IIM antenna beam cross section on the plane, wich is normal to equisignal line.

So if the missile is in the point M (fig.N 2) and the K-IIM antenna beam is rotating, the electric field strength in the point M will be sinusoidal amplitude-modulated at the frequancy of the rotating beam. The modulation percentage is determined by the missile "KC" -to-E.S.L. deviation and increases with the angle "θ" increase. So, the modulation envelope is proportional to the angle deviation in this case. And for small angles "θ", which are operational angles, this responce may be considered linear. In addition, the amplitude of field strenght envelope is proportional to a middle level of U.H.F. signal in this point.

The envelope of A.M. input signal, produced by KI-6M unit of Radar K-IM, is Known as error signal.

The same linear deviation from the equisignal line at different distances between the missile and the Radar K-IIM, i.e. between the missile and mother-ship, produces different modulation percentage.

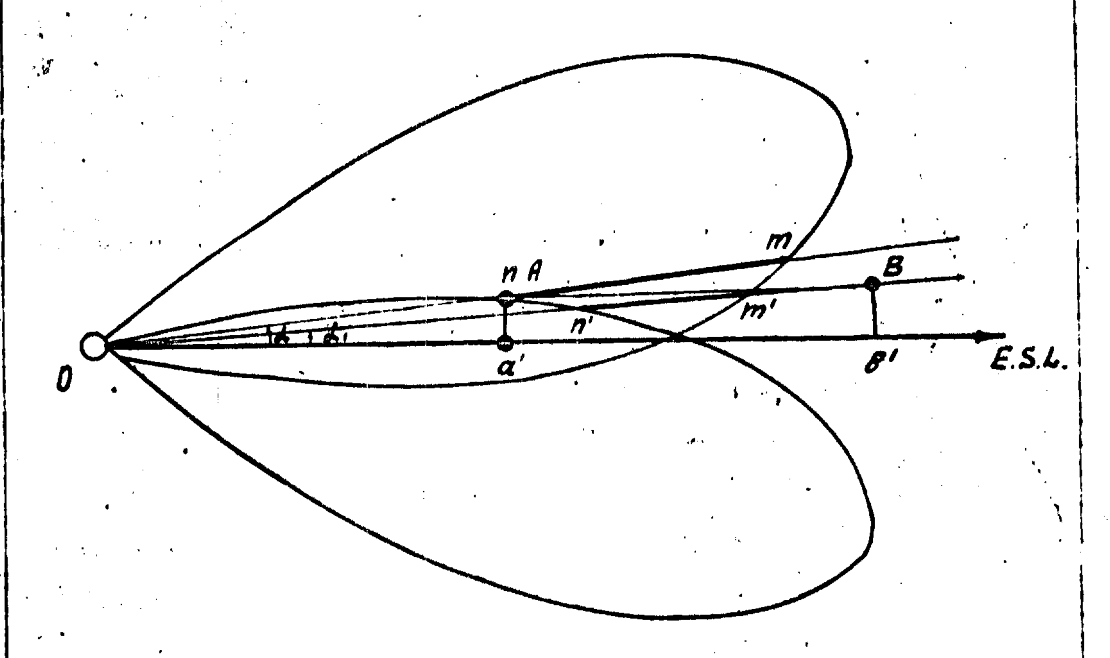


Fig. N 4.

Fig. N 4 shows, that the same "KC" -to-E.S.L.range deviation (a'A and B'B) produces nonequal changes of U.H.F. signal power, when Radar K-IIM is scanning (nm ≠ n'm').

It is obvious, that percentage of U.H.F. signal modulation and hence the error signal will be less at the missile-to-Radar K-IIM range being equal to OB.

With a view to obtain driving voltages proportional to "KC"-to-E.S.L. linear deviation at different distances between

the missile and the mother-ship a program, increasing driving voltage transconductance, is provided in the Radar K-IM unit KI-6M. Driving voltage-to-modulation percentage relation is known as transconductance of driving voltage.

The regime A transconductance increase is carried out by setting the range potentiometer, which varies the unit KI-6M detector gain in dependence on the time.

The moving of range potentiometer slide is carried out by means of the time-meter and lasts till missile flight stage.

The gain-to-time dependence is in accordance with the missile speed so that the driving voltage value does not depend on the angle deviation, but it depends on linear missile-to-E.S.L. deviation.

To exclude driving voltage transconductance dependence on U.H.F. signal average level (which depends on missile-to-mother-ship range) and to get driving voltages conforming to "KC" coordinates relative to E.S.L., the A.G.C. is provided in the synchronization channel. This A.G.C. maintains constant value of the videopulses in overall signal power band.

Driving voltage polarity, which is determined by the missile position in Radar K-IIM beam (left-right-above-below) is obtained by comparing error signal phase to Radar K-IIM reference voltage phase.

It's necessary in this case to relay Radar K-IIM reference voltages to the missile (i.e. to carry mother-ship axes of coordinate to the missile).

In every point of the space, where the missile is positioned

phase difference between the error signal and the reference voltage determines angle of vector, which inter connects this point and the equisignal line and lies on the plane normal to the E.S.L. (see fig N 3). Reference voltages are transmitted to the missile by means of the recourence frequency ("HO") modulation of pulses radiated by the Radar K-IIM. The sinusoidal modulation percentage is equal to I.I. %.

Regime "A" reference voltages are obtained from the reference generator, which is geared to the antenna K-IIM exiter and produces sinusoidal voltage to modulate Radar K-IIM U.H.F. signal reccurence frequency.

Fig. N 3 shows, that for every point of space lying on the beam cross-section plane in the same distances from the E.S.L. the field strength modulation percentage is constant and the phase difference between error signal and reference voltage determines the orientation of the point relative to the E.S.L. of the K-IIM antenna.

It's always possible to provide phase-shifting of the Radar K-IIM - Radar K-IM system so, that error-signal to reference synphasing will be carried out in the only definite missile position in scanning beam field. The error signal phase relative to reference voltage phase will be counted out unambiguously on condition that reference phase is constant at any direction of missile deviation. This requirement is met by Radar K-IIM transmitting antenna gyro-stabilizing. It excludes phase deviation when random mother-ship evolutions are happened.

So, A.M. envelope (or error-signal) and reference voltage

contain complete information of the co-ordinates of missile, to wit : error-signal amplitude is proportional to missile-equisignal line range ;

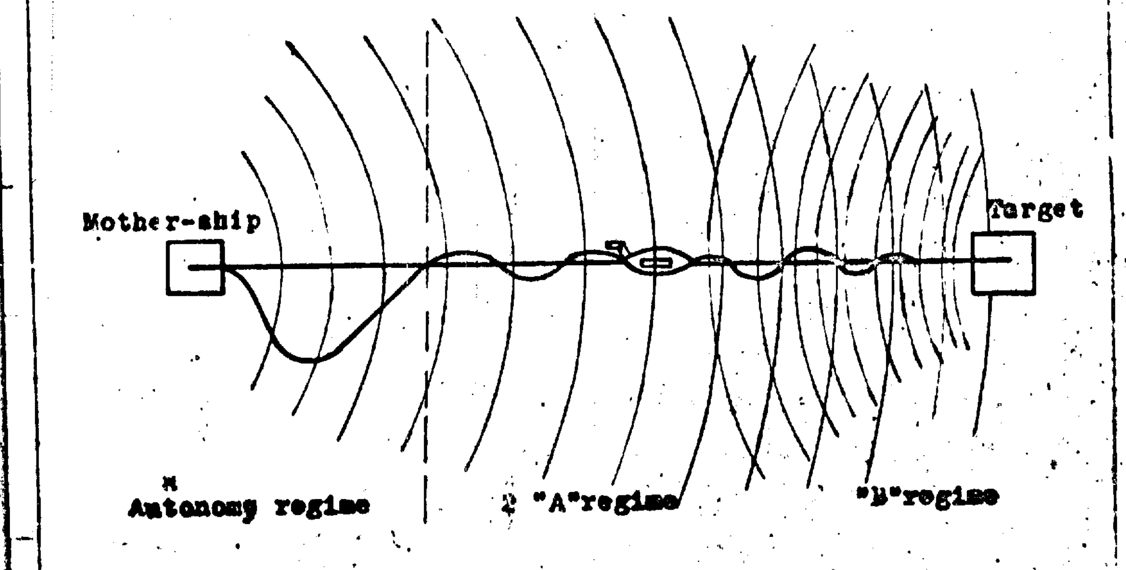
- phase difference between error voltage and reference voltage determinds angle orientation of the missile on the cross-section plane, the pole of which is on the Radar K-IIM antenna E.S.L.

It is necessary only to make suitable transformations to detect the missile co-ordinates.

The unit phase-detectors are transforming this information into driving voltages of the course and elevation channels.

3. "B" Regime

The Regime "B" starts from the moment of command N 2 operating and is continusing till the missile quidance stops.



Autonomy regime,"A" Regime,"B" Regime

Fig. N 5.

In this regime missile "KC" homing is carried out also in two channels (course and elevation) by means of the "B" antenna and the "B" receiver, wich receives signal reflecting from target (see.fig.N 5).

Command N 2 is initiated, when echo-pulse level becomes equal to a preset value, but no sooner that 200±8 secs. after dropping.

The signal amplitude modulation is provided by means of the antenna "B" scanning. Reference voltages are taken from the reference generator, which gears with the motor, rotating the antenna exiter. Rhase difference between the reference voltage and the video-pulses A.M. envelope is determined by the target orientation relative to the E.S.L., and the envelope amplitude is proportional to the angle deviation of the antenna "B" equisignal line from the target direction.

To exclude driving voltage transconductance dependence on the echo-pulses signal power, an A.G.C. is provided in K1-8M receiver. "B" regime driving voltages are produced and their effect on the missile autopilot is identical to one of the "A" regime. For the purpose of increasing noiseproof feature of the Radar K-1M in the "B" regime, the K1-8M unit is strobbed, i.e. it is opened only in the moment of echo-pulses arrival.

CHAPTER III

THE RADAR KI-11 BLOCK DIAGRAM

The purpose of the Radar units and its arrangement in the missile "KC"

The Radar K-III, arranged in the missile "KC", is made as separate units, which are interconnected and connected with the mother-ship through the distribution box KI-13M and by means of separate multiwire and coaxial cables.

The units KI-4aM, KI-46 M, KI-5MP, KI-6M, KI-7M, KI-9M and KI-10M are placed in the special damped framework, which preserves the units from sharp blows and shoks.

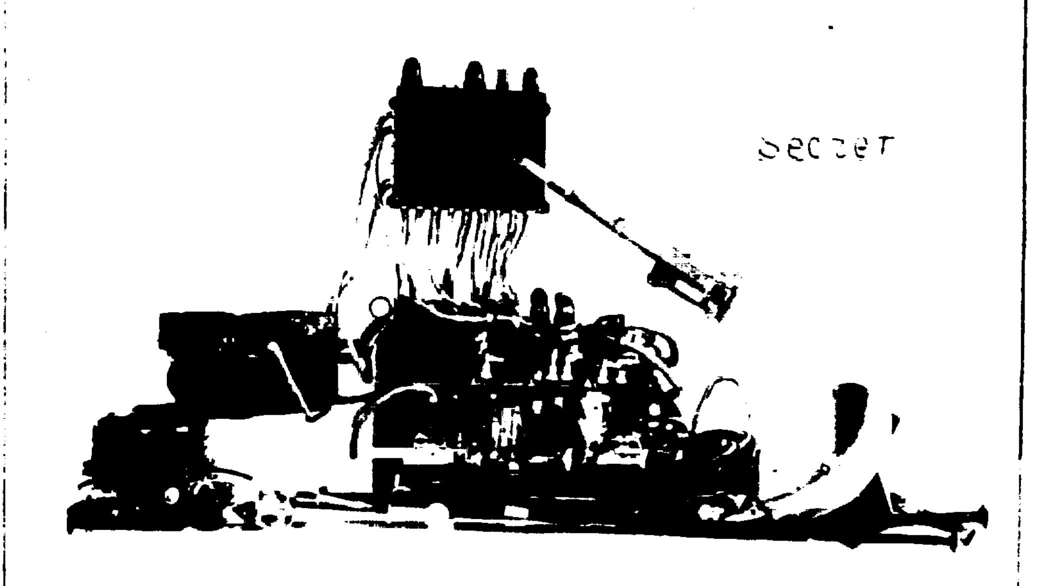
The unit-type construction of the Radar makes it easy to produce and tune industrially and permits replacement of separate units, when they are in operation.

The tuners, the control devices and the monitoring jacks, which are essential during the operation, are placed on the front panels of the units and inscribed accordingly.

The coaxial and multiwire cables and their sockets are marked to avoid wrong connection.

The framework with its units is installed in the top nose part of the missile "KC" on the special frame by means of the studs, which go through the framework claw dampers and are screwed by the nuts.

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Fig.N 6. Radar ??-?? general view

I. Unit ??-I?

The "A" antenna provides a pick up of the guiding signals, which are transmitted by the mother-ship Radar ??-IM. The antenna is placed in the back part of the "??" top fin dome.

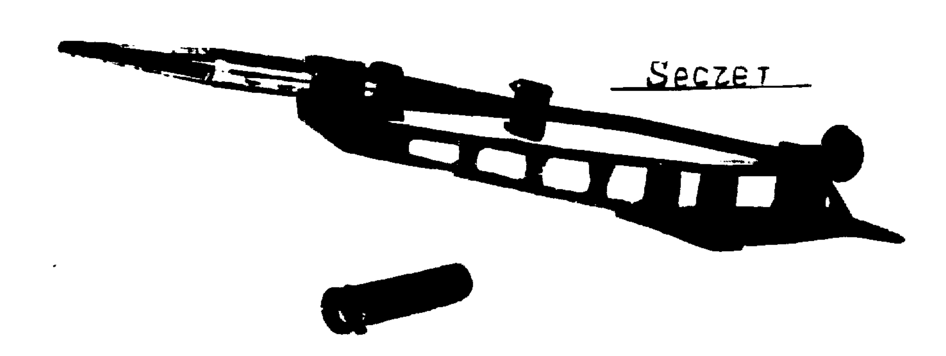


Fig.№ 7 - "A" - antenna K1-1M

2. Unit K1-3M

The waveguide channel is provided for transit the U.H.F. signals from the "A"-antenna to the mixer K1-4aM input. The waveguide is laid along the leading edge of the fin and along the right board of the body. The waveguide shape is determined by displacement of each section in the missile "EO" body.

The waveguide ends with a flexible section to connect with the unit K1-4aM in the nose compartment.

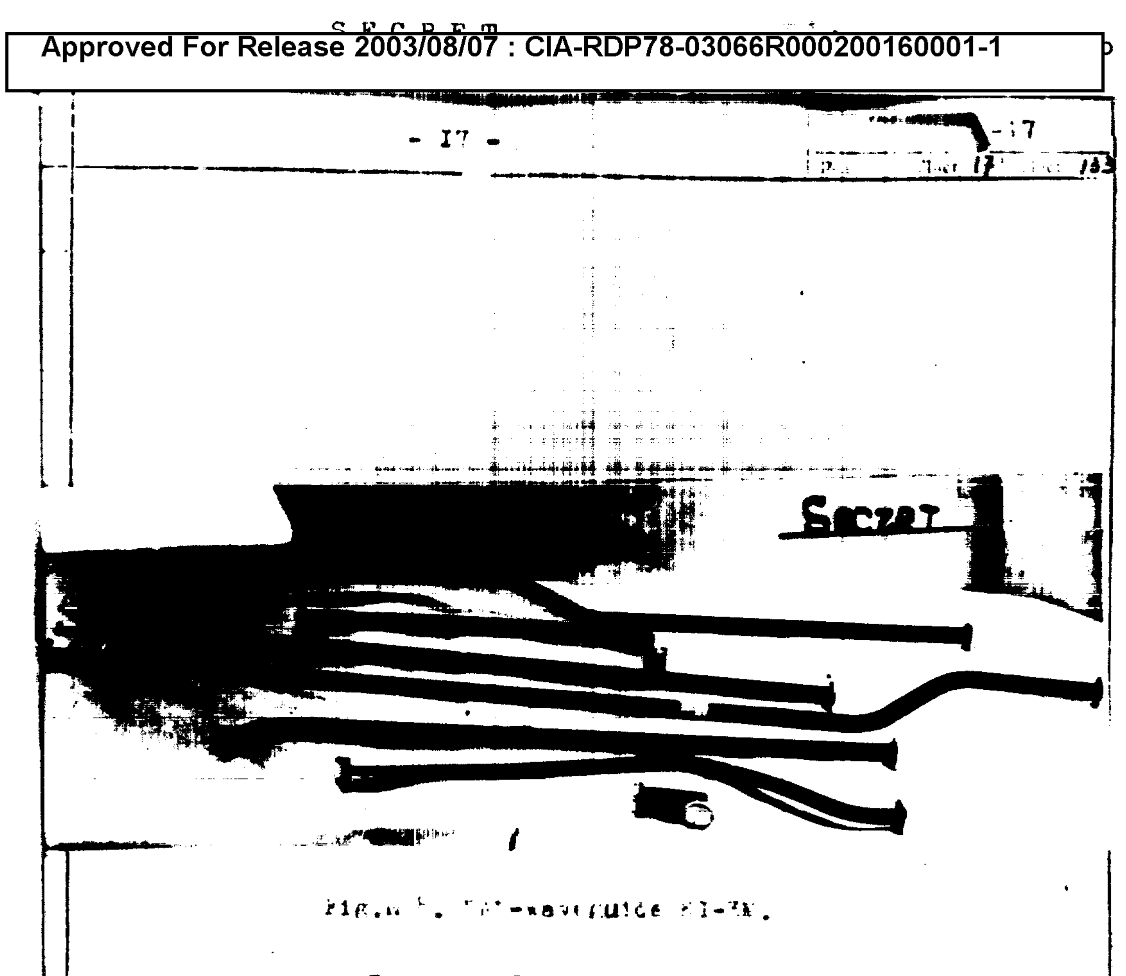


Fig.N . "F"-waveguide KI-7M.

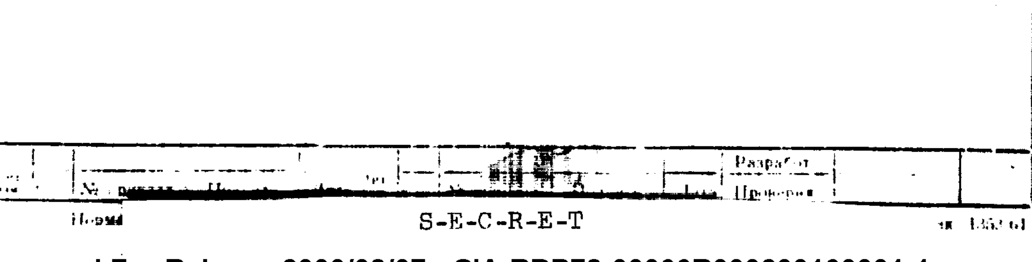
7. Unit KI-4M and unit KI-46M

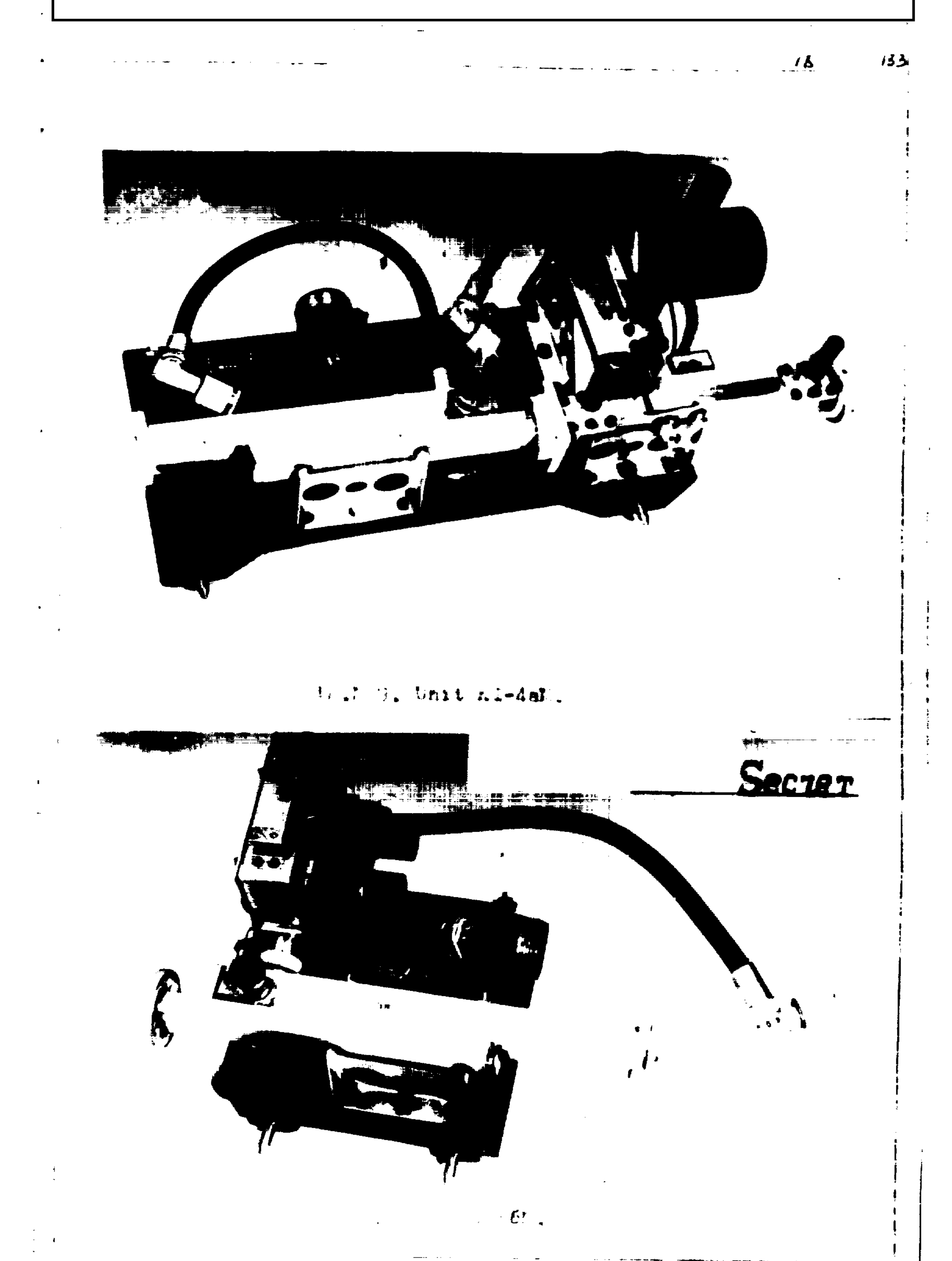
The A-mixer and the B-mixer are provided for:

a) converting R.F. signals into I.F. signals

b) R.F. decoupling between the antenna KI-IM and KI-7M. The decoupling excludes entering of the main signals transmitted by the Radar K-IIM into the homing receiver.

The units KI-4aM and KI-46M are placed on the right side of the damping framework. They have external tuners to tune the crystals, the klystron and the attennators.





Unit A1-4a

4. Unit KI-5MP

The unit KI-5MP with the unit KI-4aM form a superheterodyne receiver for the A-regime operation. The unit KI-5MP is provided for amplifying input R.F. signals, recurrence frequency and amplitude modulated and for separating from this signals:

a) the voltage controlling the klystron frequency (A.F.C. channel);

b) videopulses, amplitude modulated by an error-signal sinusoide (error-signal channel);

c) demodulated video-pulses of synchronisation, from which the reference voltages are separated (synchronization Channel);

The unit carries out the A.F.C. of the klystron.

The unit KI-5MP is placed in the damped framework pocket and has the following tuners;

- the error-signal amplitude tuner;
- the natural frequency tuner of the synchronisation blocking-generator;
- the tuner of the A.F.C.

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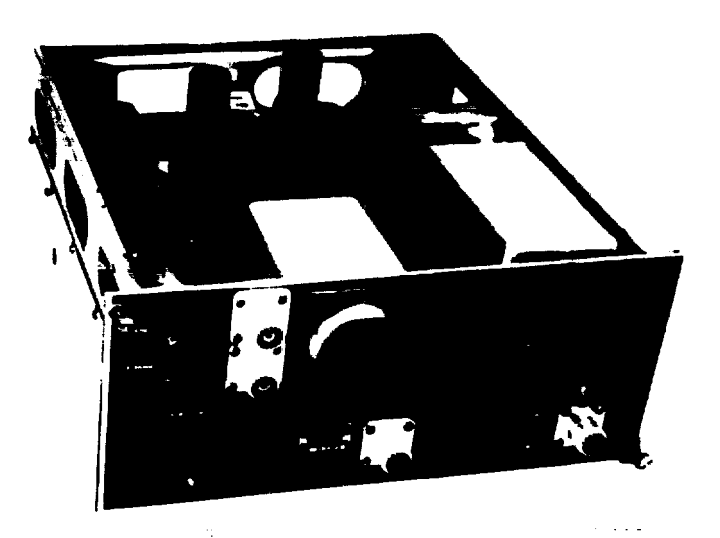


Fig.N II. Unit KI-5MP.

5. Unit KI-6M

The unit KI-6M provides the autopilot control and carries out the following functions:

a) separation of the A-regime reference voltages from the recurrent frequency modulated input pulses, which are fed from the unit KI-5MP synchronization channel output. The reference voltages are led to the tracking beacon.

b) separation of the error-signal from the A.M. video-pulses, which are supplied from the unit KI-5MP and unit KI-8M error-signal channel outputs. The error-signal is also led to the tracking beacon and to the monitoring jack.

c) produces the driving voltages of course and elevation channels, which control the autopilot.

d) produces the synchro-pulses to synchronize the units K1-9M and K1-12M;

e) interlocks the command N 2 during the 200 ± 8 secs. time period after dropping. The unit K1-6M is placed in the damped framework pocket and has the tuners:

a) driving voltages of course and elevation channels balancing,

b) A-regime and B-regime gain control;

c) control of the phase and amplitude of reference voltages.

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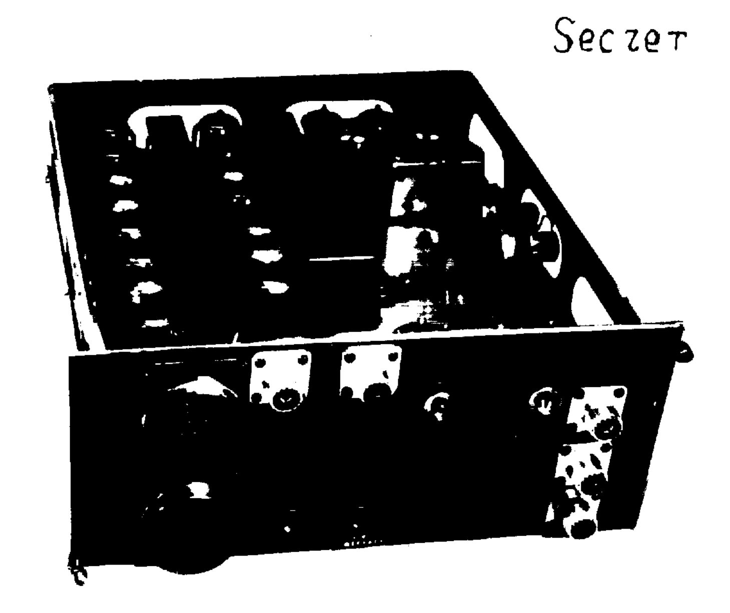


Fig.N 12. The unit K1-6M

6. Unit KI-7M

The "B" - antenna KI-7' is placed in the nose compartment of the missile "KC" and is connected with the "B" - mixer KI-46M by the flexible waveguide.

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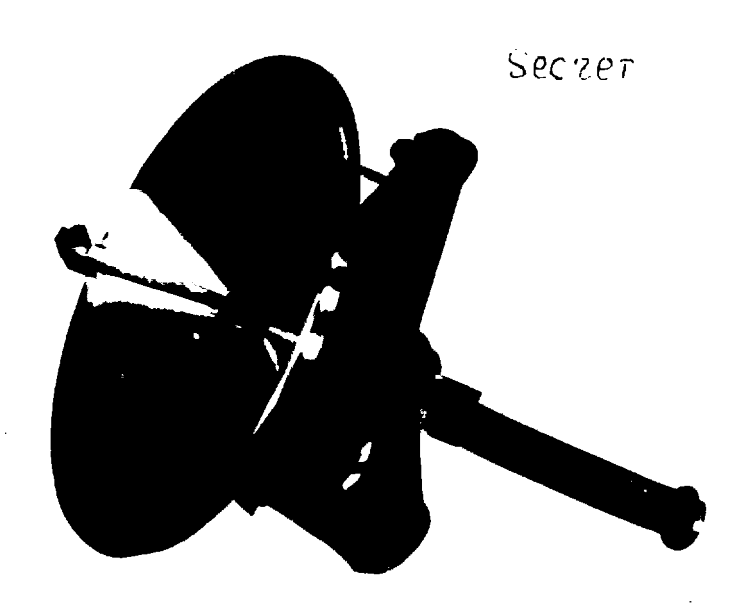


Fig.N 13. The unit KI-7M

7. Unit KI-8M

The unit KI-8M with the unit KI-46M form a superheterodyne receiver for B-regime operation. The unit KI-8M amplifies input R.F. signals and separates from them video-pulses amplitude modulated by the scanning frequency "Я" at feeding the unit KI-8M input. The unit KI-8M injects also output video- ... the unit KI-9M ... in the echo-signal

locking on and tracking and to produce the command ...

For the purpose of increasing the noiseproof feature the unit is strobbed by the unit KI-9M output positive pulses of 2 μsec length.

The unit KI-8M has the following external tuners:

a) manual gain control,

b) error-signal output pulse amplitude.

The unit KI-8M structure is made as two separate sub-units: the unit KI-8M placed on the unit KI-46M plate and the unit KI-66M placed in the framework pocket.

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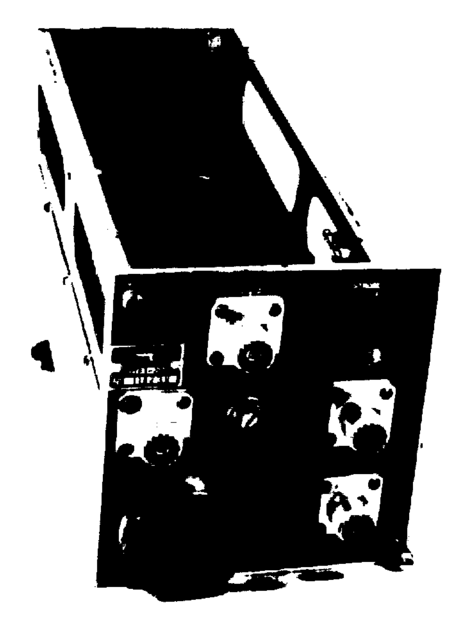


Fig.N 14. The unit KI-8M

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8. The [illegible]

The unit [illegible] provides [illegible]
tion of the echo- [illegible] from the [illegible].
The unit carries out the following functions:

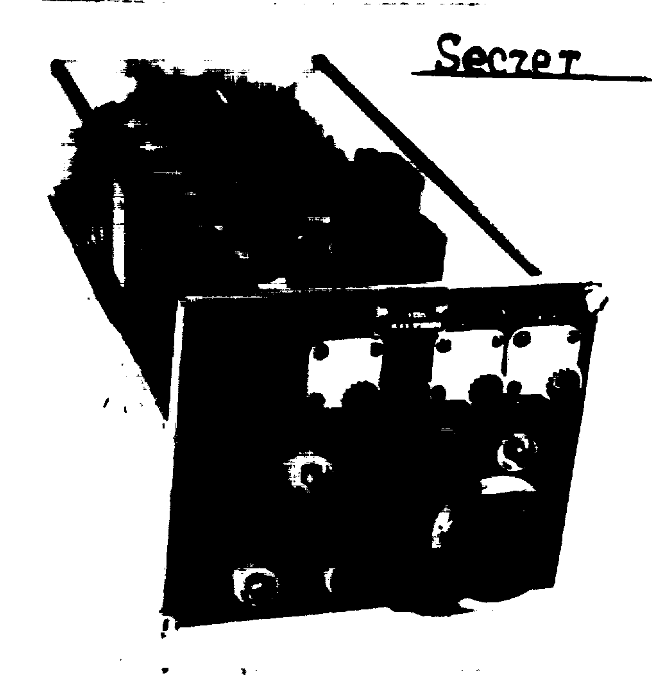
a) searching [illegible] within the range band
between 120 ± 20 μ [illegible] ± [illegible]
narrow strobe, which provides [illegible] of fusing,

b) locking on of [illegible] within the above-mentioned
band and [illegible] from [illegible] ± 20 [illegible]
to 1.6 ± 1. [illegible]

c) produces the [illegible].

The unit has the [illegible]

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S-E-C-R-E-T

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9. Rectifier KI-IOM

[illegible] KI-IOM provides transformation of the a.c. [illegible] voltage into d.c. voltages to supply the Radar KI-M units [illegible] the exception of the unit KI-12M).

[illegible] unit has tuners which control the regulated voltage values [illegible] v., [illegible] v. and [illegible] v. and are located on the front panel. The unit is [illegible] in the separate pocket of the damped framework.

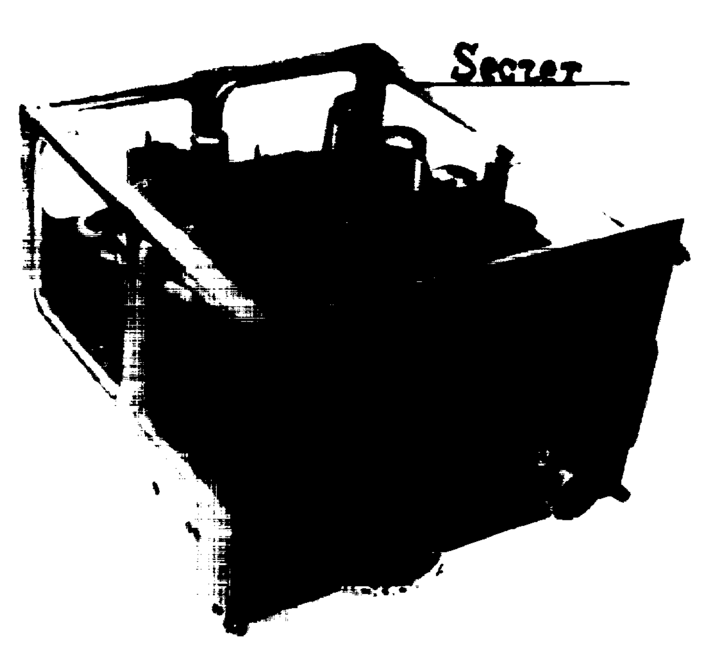


Fig. N [illegible]. The unit KI-IOM

[illegible] antenna KI-IIM

The unit KI-IIM is placed in the small dome, which is located above the missile [illegible] fin. The antenna KI-IIM [illegible] tracking bea[illegible] .F. signals to the mother-ship.

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Fig. [illegible] The unit [illegible]

11. The [illegible] [illegible]

The unit [illegible] provides [illegible] of the [illegible] P.F. pulses as a response to the [illegible] pulses. The average pulse time [illegible] sec in the "A" regime and can be varied [illegible] "Ю" - frequency error-signal amplitude.

The regime "B" time delay of [illegible] is constant and is no more than [illegible] time delay is practically absent.

The unit is placed on the [illegible] part of the large "KC" [illegible] dome.

[illegible] turned.

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[illegible]

[illegible] distribution box

The unit [illegible] provides

a) inter connection between the [illegible] separate units

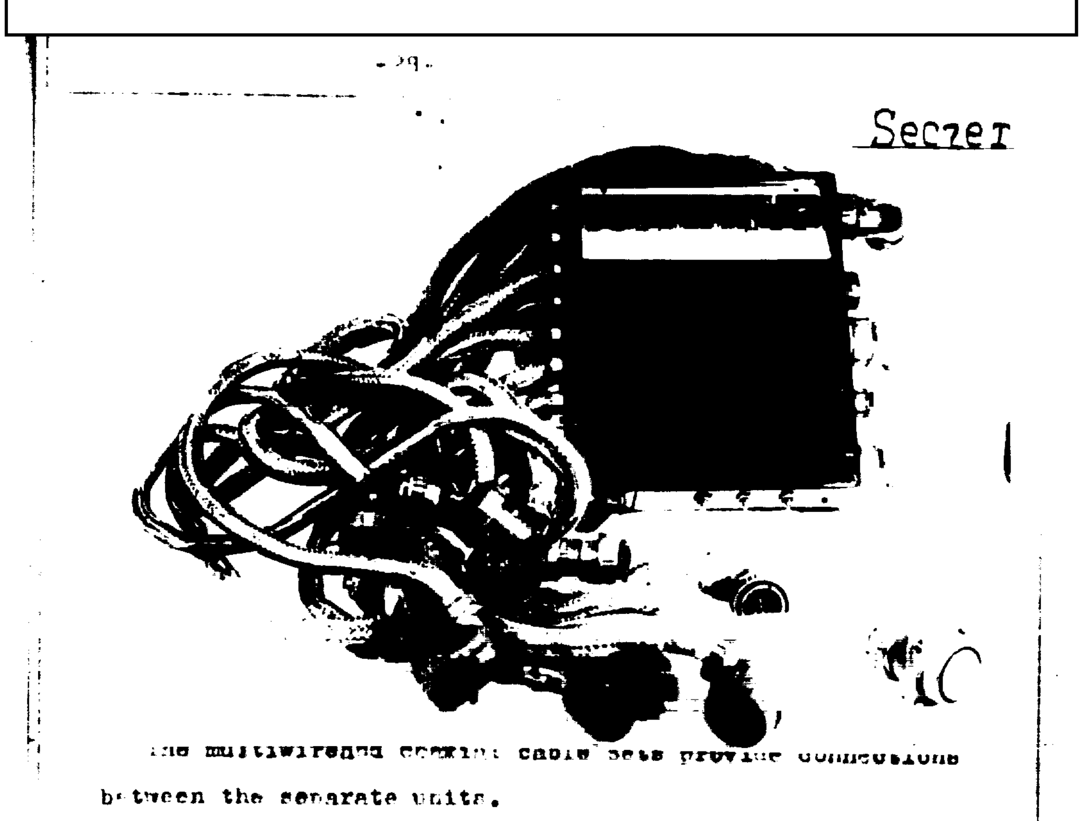
b) connection with the autopilot

c) connection between the Radar KI-X and the mother-ship equipment

d) connection with the monitoring board [illegible]

The unit is installed on the back wall of the damped framework and is fastened to it with four screws

The potentiometers, which control the output voltages of the [illegible] KA- 5 and [illegible], are placed on the unit [illegible] wall.



The multiwire and coaxial cable sets provide connections between the separate units.

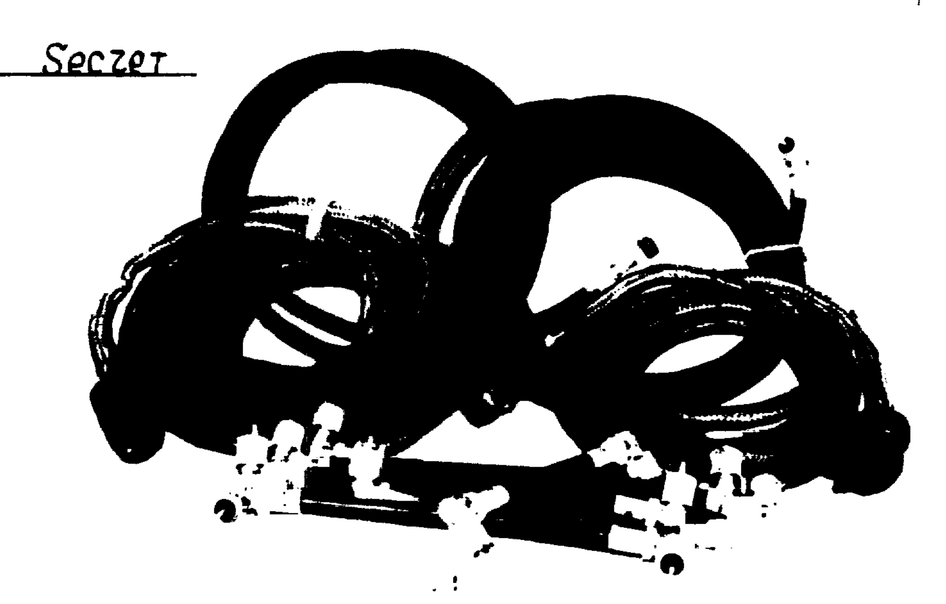


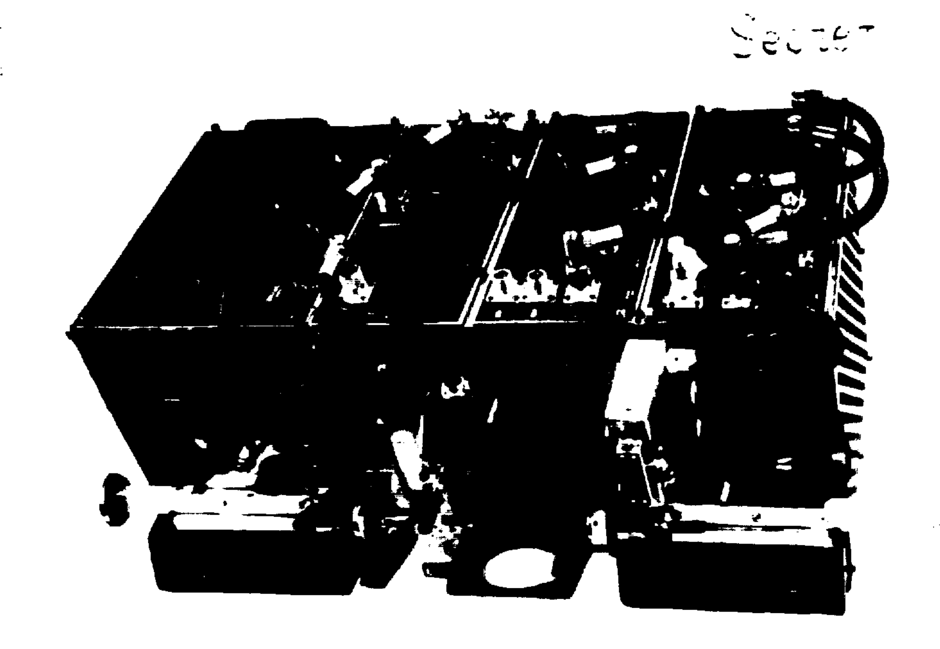
Fig. N [illegible]. [illegible] cable set.

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14. Damped framework

The damped framework is provided for arrangement and fastening of the units KI-4aM, KI-46M, KI-5MP, KI-6M, KI-8M, KI-9M and KI-10M. The shock-absorption provides normal operation of the units. The damped framework with its units is installed in the missile "KC" nose compartment on special shock-absorbers.

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CHAPTER IV

RADAR K-1M FUNCTIONAL DIAGRAM

Radar K-1M functional diagram is in Appendix N 1 (Book of Radar K-1M Elementary Diagram).

§ 1. A - antenna K1-1M

The unit consists of the following parts:

1) Waveguide adapter;
2) Round waveguide;
3) Dielectric rod.

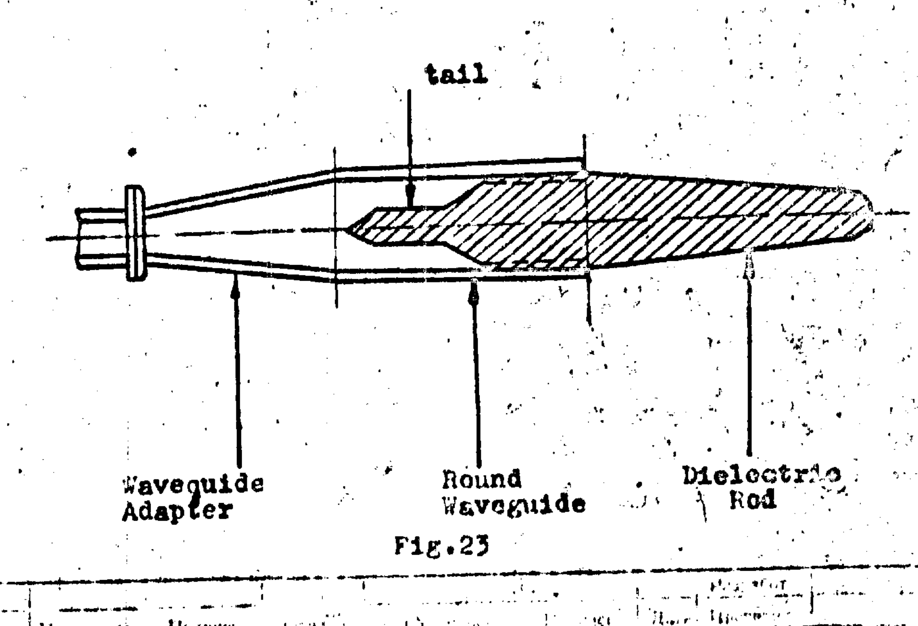


Fig.23

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U.H.F. rotating polarization electro-magnetic wave, transmitted by the Radar K-IIM antenna, is picked up by the dielectric rod. The tail of the rod transforms circular polarization wave into H_{11} mode of a linear polarized wave. The waveguide adapter transforms the H_{11} wave mode into the E_{01} wave mode and channels it to the K1-3M waveguide input. The antenna radiation pattern is shown on the fig.N 24. Half power beam width is equal to 30° ± 2°.

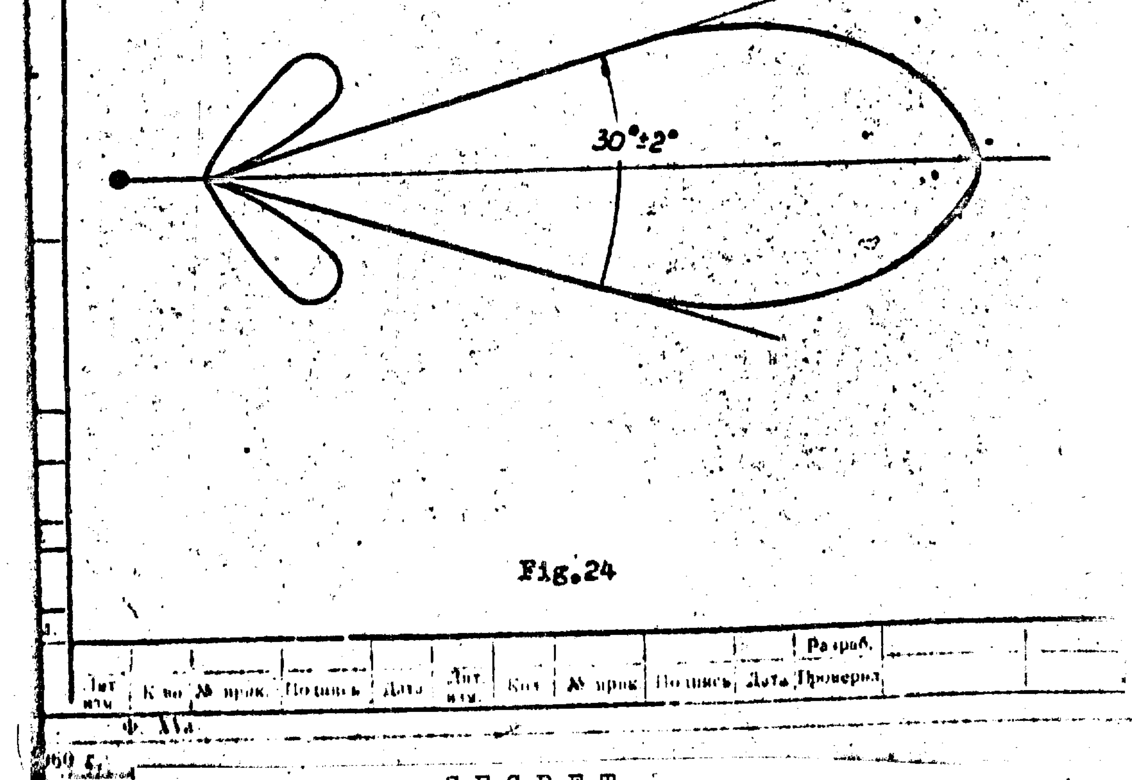


Fig.24

§ 2. Waveguide K1-3M

The Wavequide provides channelling of the U.H.F. pulse signal from the "A" antenna to the unit "K1-4a..." input. The Waveguide of the Radar K-1M, installed in the missile "KC", consists of 7 separate sections, which are interconnected and form a definite configuration. To decrease power loss the waveguide internal surfaces are silver-plated. Operational frequency band of the wavequide is 4k ± 60 mc. The Standing wave ratio of the waveguide is less than 2,5 and loss is less than 3 db.

§ 3. "A" mixer KI-4aM

U.H.F. signal, received with the "A" antenna, is channeled through waveguide to the crystal mixer. C.W. heterodyne signal is fed to the crystal mixer ... Heterodyne power level is adjusted with the atten... Klystron frequency is trimmed by A.F.C. of "A" ... The crystal detector mixes the input signal fre... with the klystron frequency and gives many vari... frequency combinations and their harmonics ... them to the receiver K1-5MP input through ... cable (plug N 30).

§ 4. "A" receiver ...

The unit consists of the following ...

1. Input circuit, which is common for three channels;

2. Synchronization channel, consisting of:

a) 4-stage I.F. Amplifier, which is used for the error-signal channel also (tubes: Л1,Л2,Л3,Л4);

b) Detector - Л12 (left half);

c) Video amplifier - Л12 (right half) Л-17 (right half) and Л18;

d) Cathode follower - Л19 (left half);

e) Blocking-generator - Л19 (right half).

3. A.F.C. channel, consisting of:

a) 6-stage I.F. amplifier - Л1,Л2,Л3,Л4,Л5,Л6 (tube Л6 serves as a clipping amplifier);

b) Frequency discriminator - Л7;

c) Video-amplifier - Л8 (left half);

d) Cathode follower - Л8 (right half);

e) Detector-Л9 (left half);

f) Cathode follower - Л9 (right half);

g) Transitron generator - Л10;

4. Error-signal channel, consisting of:

a) 4-stage I.F.amplifier - Л1,Л2,Л3,Л4;

b) Error-signal detector - Л12 (left half);

c) 2-stage video-amplifier - Л12 (right half) Л11 (left half);

d) Cathode follower - Л11 (right half);

e) A.G.C. detector - Л13 (left half);

f) A.G.C. cathode follower - Л13 (right [illegible]

g) A.G.C. diode clipper - Д17 (left half).

Various frequency pulses are fed to unit input through the cable N 30. Input circuits of the unit select I.F.signal among these pulses. After amplifying by 6-stage I.F. of A.F.C. channel and clipping pulses go to the frequency discriminator input. The discriminator reacts on the frequency value of the pulses. If the input frequency is higher than the intermediate frequency, output voltage of the discriminator is positive and if the input frequency is lower than the intermediate frequency, the output voltage becomes negative. This permits to control frequency of the klystron. Output discriminator pulses after amplification and rectification are fed to the input of the transitron generator, which generates sawtooth voltage and applies it to the klystron reflector, when the searching regime takes place. When the negative voltage, applied to the grid of the tube Д10, reachs - 4v, transitron oscillation is stopped and the tube begins operating as a direct-current amplifier (in the A.F.C. regime).

Let us examine two operational regimes of the A.F.C. system: search regime and autocontrol regime.

1. Searching regime

When there is large deviation between the difference frequency and the middle frequency of I.F. cascade the video-pulses are absent at the discriminator

Attachment No. 6 to

Fig. N 25

and transitron generator operates in regime of nature oscillation. In this regime a sowtooth voltage goes to klystron reflector. D.c. negative voltage also goes to the reflector from the voltage divider, i.e. there are the sowtooth and d.c. voltages on the reflector. The klystron oscillation frequency depends on the reflector voltage, and the sowtooth sweeps the klystron frequency in limits, which are determined by the sowtooth amplitude and the electron tuning range. Intermediate frequency will be sweeped with the klystron frequency sweeping. Fig. N 25 shows the dependence of the klystron frequency and power on reflector voltage and sowtooth.

Automatic control regime

The A.F.C. sweeps klystron frequency till the interme- diate frequency becomes lower than 41 Mc. At the moment discriminator output pulses take negative value. The discriminator output negative pulses stop transitron natu- re oscillation and change it in d.c. amplifier regime. At the moment A.F.C. regime starts. If intermediate frequency is decreased by means of random fluctuations of signal or heterodyne frequency, the transitron output negative volta- ge will decrease. In accordance with it the heterodyne frequency will decrease and the intermediate frequency will increase. In the case of I.F. increasing the negative [illegible] its [illegible] will also increase, and it will result in increase of the

transitron output negative voltage and, in accordance, an decrease of intermediate frequency. Dicriminator output pulse amplitude depends on I.F. deviation. If the intermediate frequency increases suddenly or decreases to a degree, the discriminator output pulses will take positive value or disappear. The A.F.C. will be returned again in the searching regime and the sowtooth voltage will be applied again to the klystron reflector. The sowtooth will "sweep" the heterodyne frequency in broad range and accordingly will sweep the intermediate frequency. In sweeping, the intermediate frequency will pass the value, at which the discriminator output negative pulses will be produced. After the discriminator output negative pulses reach a level enough to stop the nature oscillation of transitron, the A.F.C. circuit will change in automatical control of the klystron frequency regime.

Error-signal channel

The first 4-stage I.F. amplifier is common for error-signal and A.F.C. channels. After 4-th stage I.F.pulses, modulated with frequency "Ω", are going to error-signal detector. From detector load the pulses are going to I.F. band-elimination filter. After I.F. suppression amplitude modulated video-pulses are amplified in 2-stage

video-amplifier and through the cathode follower are going to the error-signal separation device (to socket N 27 of the K1-6M unit). Pulse middle level is remained constant by means of A.G.C. The video-pulse envelope amplitude is proportional to the input pulses percent modulation.

A. G. C.

There is delaied and amplified A.G.C. circuit. Error-signal video amplifier is an element of A.G.C. circuit. Filter A.G.C. time constant is suited to suppress the error-signal component "D" in A.G.C. voltage composition. So, A.G.C. reacts only on comparatively slow fluctuations of the input signal power.

Synchronization channel

The I.F. pulses from mixer, are amplified by four I.F. stages. The I.F. amplifier output signal is led to second detector input. The detector load negative video-pulse is led to the 4-stage video-amplifier. Synchronization output video-pulses are not to be amplitude modulated, so video-amplification stages should operated in clipping regime.

Last stage output video-pulses through cathode synchronizes the natural oscillation blocking

Blocking-generator eliminates the residual amplitude modulation of clipping pulses. It produces the synchronization pulses, shape and amplitude of which do not depend on the input pulse form and amplitude. The pulses are led ~~xt~~ to unit KI-6M socket N 26.

§ 5. Autopilot control unit KI-6M

Unit circuit may be functionally divided into four parts:

I. Reference separation channel, consisting of:

1. "single stroke" blocking-generator - Л1 (left half);
2. detector - Л1 (right half);
3. amplifier - Л 2;
4. phaseshifter - Л3 (left half);
5. phasesplitter - Л 3 (right half);

II. Error-signal separation channel, consisting of:

1. "A" third detector and A.G.C. - Л 9;
2. "B" third detector and A.G.C. - Л 8;
3. Selective amplifier - Л10 and Л 11 (left half);
4. Paraphase amplifier - Л 12 (right half);
5. Cathode follower - Л 11 (right half);

III. "Y" and "Z" driving voltage channel, consisting of:

1. reference voltage amplifier - Л4 (left half) and Л 13 (left half);

 Attachment No. [illegible]

2. two paraphase amplifiers - Л4 (right half) and Л13 (right half);
3. two clipping amplifiers - Л5 and Л14;
4. two phase detectors - Л6, Л7, Л15 and Л16;
5. two power amplifier - Л17, Л18, Л19 and Л20.

IV. Time motor, consisting of:

I. motor Д 5-ТР;
2. reducer;
3. cam contactor;
4. range potentiometer.

I. Reference voltage separation channel

The channel is intended for reference voltage separation from recurrence frequency modulated pulses and for producing of second reference voltage, which should be phase-shifted by 90° relative to first reference voltage. It is intended for giving away the synchronizing pulses too. Recurrence frequency modulated pulses are led to the socket N 26 from the unit KI-5MP synchronization channel output.

The pulses trigger the "single stroke" blocking-

S-E-C-R-E-T

generator, which maintains recurrence frequency modulation and constant shape and amplitude of pulses.

Blocking-generator cathode load positive video-pulses are led to socket N 25 to synchronise the KI-9M unit and to socket N 28 to synchronise the KI-I2MP unit. Besides the pulses are applied to the detector, which detects frequency "Ю" sinusoidal voltage from reccurence frequency modulated pulses. The detected voltages are led through the filter to the amplifier. After filtering and amplification the voltage is applied to the phase-shifter. The phaseshifter output voltage portion goes to the error-signal channel to compensate the reccurence frequency modulation influence on error-signal value.

The phaseshifter is provided for initial phase setting between the reference voltage and the error-signal.

The correctly phased unit must produce channel "Z" output voltage and channel "Y" zero output voltage, when the reccurence frequency modulation is in phase with the reference voltage. Then the reference voltage is led to the phasesplitter.

Two phasesplitter output orthogonal sine reference voltages (R.V.0° and R.V. 90°) are applied to the "A-B" regime relay. In regime "A" the voltages go to the course and elevation driving voltage channel through normally closed contacts of the relay P-I,

Regime "B" reference voltages are two orthogonal sine frequency " Я " voltages, which are led from the unit KI-7M reference generator, In regime "B" the "P-I" relay is switching on the reference voltages to the driving voltage channel input. In this case, the regime "A" reference channel does not operate, excepting the blocking-generator, which gives away synchronization pulses.

II. Error-signal channel

The unit "KI-5MP" (socket N 27) and the unit "KI-8M" (socket N 24) output A.M. pulses are applied to "A" detector and "B" detector, accordingly. The detectors separate out the error-signals, values of which are proportional to A.M. percentage of input pulses. The error-signal goes to the selective amplifier input through relay "P-I" contacts. In "A" regime the relay "P-I" winding is currentless and amplifier is tuned at "Ю" frequency.

When swiched on "B" regime +27 voltage is applied to the relay "P-I" winding, the selective amplifior is retuned at " Я " frequency and "B" detector output error-signal is given to the amplifior input. Selective amplifier output error-signal is led to the paraphase amplifier. Two antiphase voltages from the amplifier plate and cathode are given to grids of driving voltage channel "Y" and "Z" phase detectors.

Besides paraphase amplifier output voltage portion is led through the cathode follower to the "KI-I2MP" unit (tracking beacon signal).

III. Course driving voltage channel
("Y" channel)

The 0° reference voltage goes through normally closed contacts and is applied to the paraphase amplifier input, from which two antiphase voltages go to the limiting amplifiers. In the amplifiers the sinusoidal voltages are transformed into square wave voltages. The square waves feed phase detector tube plates.

Error-signal antiphase voltages are applied to the phase detector grids. The value and polarity of the phase detecter output pulsating volatge d.c. component depend on error-signal amplitude and phase shift between the error-signal and "0°" reference voltages. The pulsating voltage is filtered and applied to the power amplifier input. Power amplifier output d.c. voltage goes through distribution box (KI-I3M) to the autopilot.

IV. Elevation driving voltage channel
("Z") channel)

"Z" channel is completely analogous to the "Y" channel. Since 90° reference voltage is applied in this case, the channel output driving voltage will depend on error-signal amplitude and phase shift between error-signal and

90° reference voltage.

V. Time motor

The time motor varies the error-signal channel amplification in "A" regime from the moment of the drop-command. The amplification-time function is programmed by range potentiometer winding. In addition the time motor produces the command N I, command N 2 unblocking voltage and signal of start and end time motor position.

§ 6. "B" antenna - "KI-7M" unit

The unit has the following functions:

I. picks up the echo-signal and amplitude modulate them with scanning frequency " Я ".

2. Makes two orthogonal frequency " Я " sine voltages, which are phase shifted against each other by 90° (reference voltages).

3. Channels the U.H.F. modulated signal to the unit KI-4bM input.

§ 7. KI-6 M Unit

The unit carries out mixing of echo-signal with klystron signal, producing the frequency combination signals and channeling it to the unit KI-8M input.

§ 8. "B" receiver K1-8M Unit

The unit circuit may be devided into three parts:

- error-signal pulse channel for K1-6M unit,
- echo-signal pulse channel for K1-9M unit,
- A.G.C. channel.

The unit consists of:

1. I.F. preamplifier - tubes Л1 and Л2;
2. I.F. amplifier - tubes Л3, Л4, Л5, Л6, and Л7;
3. Second detector - tube Л8;
4. Video-amplifier - tubes Л9 and Л10;
5. Cathode follower - tube Л11 (right half);
6. Video-amplifier - tube Л11 (left half) and Л13 (right half);
7. Cathode follower - tube Л13 (left half);
8. A.G.C. detector - tube Л12 (right half);
9. A.G.C. cathode follower - tube Л12 (left half).

Amplitude modulated with " Я " frequency I.F. pulses go to two-starge pre-amplifier input through the socket Ш 34. After pre-amplification the I.F. pulses go to 5-stage I.F. amplifier. I.F. continuous tunning is carried out by the unit K1-5MP A.F.C.

After main I.F. amplification the pulses go to the second detector Л8. After detection A.M. video-pulses are amplified in two-stage video-amplifier and through the cathode follower (Л11 right half) are led to output Ф 24.

The cathode follower output video-pulse modulation percentage is equal to the unit input I.F. pulse modulation percentage.

In operating range the average signal level is maintained constant by means of the A.G.C. For the receiver blacking out the I47v bias is applied to the 5-th I.F. stage. The bias is taken away only after command N 2 unlocking. After unlocking the receiver is blacked out by stable negative bias, applied to priming and pentode grids. The receiver is opened only in the strobe moment. If the toggle switch "strobe - +" is in the position "+", the bias +I30v is applied to 5.th I.F. stage. In this case the receiver is opened always and does not depend on strobbing.

From the cathode follower ЛII (right half) video-pulses go to the echo-signal channel video- amplifier, consisting of two stages ЛII (left half) and ЛI3 (right half), and to the A.G.C. detector ЛI2 (right half). Amplified positive video-pulses are given to unit output socket "Ф23" through cathode followerЛI3 (left half). The ЛI2 tube plate (right half) negative voltage biases first 4 stage control grids of the main I.F. amplifier. For manual gain control the negative voltage is led to the A.G.C. circuit and controlled by the "M.G.C." potentiometer, which is installed on the unit front panel.

§ 9. Range unit (or autoselector) K1-9M

The unit circuit may be devided into two main parts:

I. Search and track device, consisting of:

1) buffer - Л9 (left half);
2) multivibrator - Л10;
3) differentiated pulse amplifier - Л9 (right half);
4) buffer - Л 11;
5) strobe blocking-generator and cathode follower - Л13;
6) half-strobe blocking-generator and cathode follower - Л12;
7) two coincidence cascades - Л4 and Л5;
8) difference detector and cathode follower - Л3 and Л2 (right half);
9) search starting tube - Л2 (left half).

II. Command N 2 producing device, consisting of:

1) coincidence detector - Л14 (left half);
2) clipping diode - Л14 (right half);
3) electron relay - tube Л15 and relays P1, P2, P3.

Searching and tracking device. Synchronization positive pulses are given to the unit input socket N 25 from the K1-6M ~~unit~~ unit. Through buffer the pulses trigger the "single stroke" multivibrator (Л10). Each synchronization pulse triggers the positive variable pulse. The

pulse length is 120 ± 17 μsec in searching regime and 120 ± 1,6 μsec in tracking regime. The pulse length is determined by multivibrator grid bias, which is led from cathode follower Л2 (right half) and from voltage divider.

Seaching regime multivibrator grid bias is the clipping and biassing sawtooth (see fig.N 26).

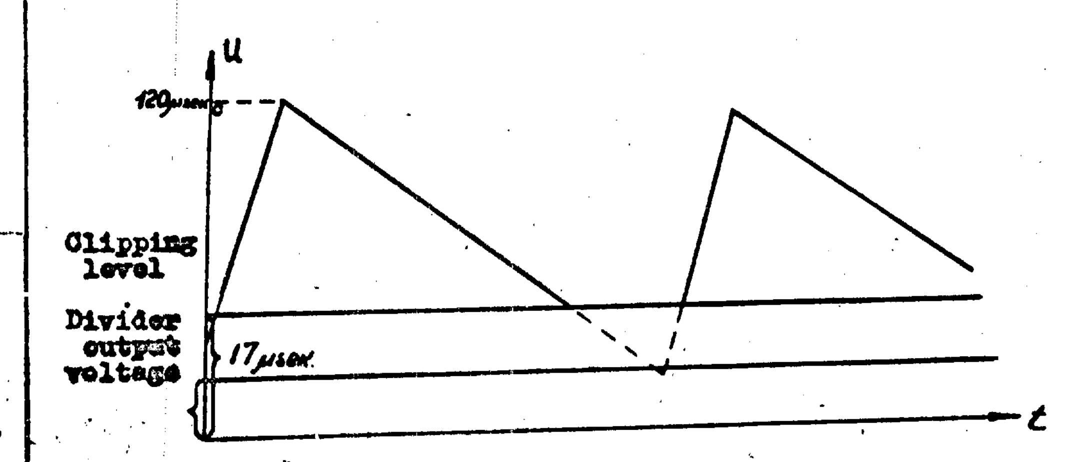


Fig.26. Searching regime cathode Л2 sawtooth

Sawtooth voltage is produced by controlling stage, which is a tranzitron in searching regime. The below clipping of sawtooth is provided by search starting tube Л2. The search starting tube (Л2 left half) and cathode follo-

wer (Л6 right half) have the common load. The slow dropping sawtooth is applied at the search starting tube grid. The Л2 cathode positive voltage follows the sawtooth form. The Л 6 tube, cut off by that positive voltage, will be open, when cathode and grid potentional will be approximately equal. When Л 6 tube will open, the Л 2 tube will be cut off by means of cathode load dropping and M.V. grid voltage will became constant, and accordingly multivibrator pulse length will became constant.

Constant voltage value, determined by divider position, may to vary the max and min levels of multivibrator grid sawtooth and accordingly to vary the multivibrator output pulse length from max to min value. Besides that, the multivibrator output pulse length may be varied by "search starting" potentiometer tuning, which regulate the trigger level of Л 6 tube (right half). The multivibrator variable pulses are going to the differentiating circuit and then to the amplifier. The positive pulses, coinciding with M.V. pulse front, are surpressed by means of the amplifier zero bias grid current. The amplifier output pulses, coinciding with the M.V. pulse rear edge, trigger the strobe and half-strobe blocking-generators.

The strobe blocking-generator produces the strobe-pulses with 80v ÷ 130 v amplitude and length approximate 2 μsec. The halfstrobe blocking-generator output pulses have amplitude 100 v ÷ 130 v and pulse length approximately 0.[illegible]

Since the strobe and half-strobe pulses are tied to M.V. pulse rear edge, the pulses will be variable delayed relative to the trigger pulse within the limits of 120 μsec to 17 μsec in searching regime. The half-strobe blocking generator output pulses go to coincidence cascades through the cathode followers:

The first one - to I-st coincidence cascade pentode grid and

the second one - to 2-nd coincidence cascade pentode grid through the delay-line (0,8 μsec).

The strobe-pulses are led through cathode follower Л13 (left half) to the KI-8M unit socket N 22. Besides the strobes are led to the command N 2 circuit.

When the echo-signal is applied to the unblocking and strobbing receiver input the positive video-pulse coinciding with strobe is going to the unit KI-9M input through the socket "Ф 23".

The video-pulses are applied to I-st and 2-nd coincidence cascades of the time discriminator and to the Command N 2 coincidence detector. The coincidence cascades are normally cut out by the control and pentode grid biasing. The nondelayed half-strobe is applied to the first coincidence stage pentode grid and the delayed half-strobe is applied to the second coincidence stage pentode grid. The echo-signal pulse is applied at the control grids of two coincidence stages. Let us examine the case when echo-pulse and nondelayed half-strobe coincide in time. In this case the first coincidence stage opens and the negative pulse is

produced at its plate. The pulse length depends on the overlapping area of echo-pulse and half-strobe. The pulse amplitude depends on the echo-pulse amplitude. The output pulse is applied to the right cathode of difference detector and cuts in the latter.

The charging circuit of the accumulator capacitor is cut in. The accumulator capacitor voltage increases and transitron control grid voltage also increases. As a result, the sowtooth steepness and accordingly the half-strobe speed will increase too. In the next moment the echo-signal will coincide with delayed half-strobe due to the half-strobe movement. In the coincidence moment the second stage cuts in and produces a plate negative pulse. The pulse provides negative charging of the accumulator capacitor "C5", and stepping of the control stage oscillation (i.e. transferring to the plate-grid coupled integrator regime) and, besides, reversing of half-strobe movement. As a result of the half-strobe reversing, some time latter the echo-pulse will occupy approximately symmetrical position between half-strobes. In that moment accumulator capacitor voltage will be near equal to zero.

From the moment, tracking echo-signal regime starts. If the echo-signal delay changes the half-strobes track the echo-signal due to control voltage changing, which through the controlling stage and the cathode follo-

wer is applied to controll grid of the multivibrator. Since the strobe and half-strobe pulses are synchronous, the strobe will open the receiver in the moment of echo-signal arrival. If an echo-signal level is high enough, command N 2 is produced after a target locking on (i.e. coincide stages cutting in).

For lower tracking range boundary reducing, command N 2 lock on the tube "Л6" by means of relay "P4". With that the lower boundary of echo-signal tracking range decreases from 17 μsec to 1.6 μsec, because the transitron sawtooth is not clipping.

Command N 2 device is provided to produce and give away the command N 2 and to obtain the command N 2 switching off time delay.

The device consist of:

- coincidence detector Л14 (left half),
- clipping diode Л14 (right half),
- electron relay Л15, P2 and P1.

The detector is normally blocked. When the strobe applied to plate Л14, and an echo-signal, applied to the control grid of Л14 are coincided (i.e. the target is locked on), the detector becomes unblocked and the negative voltage will apply to the electron relay control grid. This tube is normally unblocked, i.e. plate current is flowing through relay P-2 winding. The detector output negative voltage blocks the relay tube. Relay winding current is died and the relay operates.

As a result of relay switching the additional capacitor C53 will be connected in parallel with accumulator capacitor and feeding circuit of relay P-I; P-2 will be disconnected. The relay P4 contacts N 1 and N 2 close and ground the "search starting potentiometer" slider. The tube 6 will be blocked and the M.V. grid voltage will be the "sowtooth" without clipping from below".

The relay PI initiates the command N 2 (+27v) and transmits it to the external circuits.

For tracking echo-pulse by the strobe when echo-pulses are abruplty diminished a "memory" in the Command N 2 circuit (time delay of the command N 2 switching off) is provided. So, in echo-pulse diminishing the strobe delay time speed is kept constant during 3 sec. by means of large time constant of the coincidence detector RC circuit. On account of that, the command N 2 switching off (relay P2 operation) is realized only 2.5 + 3.5 sec after echo-pulse diminishing. The relay releasing time independence on echo-pulse amplitude is provided by the clipping stage, which maintains voltage of the relay grids approximately constant.

§ IO. Tracking beacon responder "KI-I2 MP"

The unit consists of:

I. Triggering pulse amplifier 2 (left half);

2. Multivibrator I;

3. Differentiated pulse amplifier Л2 (right half);
4. Blocking-generator Л3;
5. Power blocking-generator Л4;
6. U.H.F. generator Л5.

A possibility of the generator tube aging is provided. The unit K1-6M output positive triggering pulses are led to socket N 28. The pulses trigger the delay multivibrator through the amplifier.

The multivibrator produces positive rectangular pulses which last 170±10μ sec. After differentiating the pulses are led to the amplifier of differentiated pulses. When the unit K1-6M output frequency "Ю" error-signal is injected to the unit K1-12MP, multivibrator rectangular pulse length varies depending on the error-signal amplitude.

When the command N 2 (+27v) is applied to the cathode of a "single stroke" multivibrator, the multivibrator will be transfered to an amplification regime. The M.V. output pulse length becomes equal to 1 μ sec, approximately. After amplification the pulse, coinciding with the M.V. pulse front, is clipped while the pulse, coinciding with the M.V. pulse edge, triggers the blocking-generator, which produces positive pulses for triggering power blocking-generator. The power blocking-generator ("modulator") feeds the U.H.F. generator plate by rectangular pulses. Output pulses of the U.H.F. generator feeds the antenna

K1-11 through the cable N 31 and radiated in the mothership direction.

The U.H.F. output pulses are delyed relatively the unit K1-6M triggering pulses by the time 170±10 μ sec, when the "M" voltage is absent at the multivibrator input. In "B" regime the pulses are transmitted aproximately simultaneously with the unit K1-6M triggering pulses, the initial time delay is less than 10 sec.

Cable assembly

The cable assembly consists of eight coaxial cables NN 22, 23, 24, 25, 26, 27, 28, 31 and one multiconductor cable N 15.

The cables are provided for:

- cable N 22 connects K1-9M unit and K1-8M unit,
- cable N 23 connects K1-8M unit and K1-9M unit,
- cable N 24 connects K1-8M unit and K1-6M unit,
- cable N 25 connects K1-6M unit and K1-9M unit,
- cable N 26 connects K1-5MP unit and K1-6M unit,
- cable N 27 connects K1-5MP unit and K1-6M unit,
- cable N 28, consisting of two parts: 28/1 and 28/2, connects K1-6M unit and K1-12MP unit,
- cable N 31 connects K1-12MP unit and K1-11 unit,
- multiconductor cable N 15, consisting of two parts: 15/1 and 15/2, connects K1-12MP unit and K1-13M distribution box.

CHAPTER V

DESCRIPTION OF THE ELEMENTARY DIAGRAM

OF THE RADAR K-1M UNITS

§ 1. Description of the unit K1-1M

The antenna is a dielectric rod, jutting out the round waveguide. The rod serves for forming of the antenna radiation pattern.

The half power level beamwidth is 30°.

The rod cross-section increases gradually approaching to the wavequide. It's necessary to provide the matching between space and waveguide input impedance. The dielectric rod is threaded and screwed in the round waveguide.

The rod tail transforms the circular polarization wave into the H_{11} mode of wave of linear polarization, which is transformed into the H_{01} mode wave in the rectangular waveguide.

The rotating field frequency is equal to the radiation frequency.

The circular polarization field vector may be represented in form of, two linear polarization components, which are amplitude equal and 90° - phaseshifted in space and time.

The spatial phaseshift is provided due to the fact wave H_{11} polarization plane. Thanks to the fact, two linear polarized and spatial 90° phaseshifted waves are created.

The amplitude equality is provided when the angle between tail and mode H_{11} field vector is equal to 45° aproximately. The time phaseshift is provided by difference between the component propagation speed, which is conditioned by nonidentical propagation of the components. A wave propagation speed in dielectric is less than one in free space; so there will take place 90°-phaseshift at the certain value of the tail length.

Equality of the component amplitudes is reached by turning the tail.

So, the antenna makes possible U.H.F. wave reception, when electrical field vector is oriented on any plane.

The waveguide adapter transforms the H_{11} mode wave into the H_{01} mode wave.

Description of the unit K1-3M

The full unit K1-3M description is given in the chapter IV "Radar K-1M skeleton Diagram".

§ 2. Unit K1-4aM Elementary Diagram.

a) Mixer.

The mixer is manufactured as of an antiphased directional coupler, which consists from two waveguides soldered by brod side and narrow one, and a crystal holder for the DK-C4 crystal.

The antiphase directional coupler changes the propagation direction of the U.H.F. wave, going from one waveguide to another.

In that way, the heterodyne signal goes to the crystal mixer. Some portion of the heterodyne energy which is not passed through directional coupler holes to the mixer is absorbed by matched load, that is placed in the dead end of the lower waveguide. An input signal also goes to the crystal mixer. The crystal holder and cable capacitance as well as the input inductance form the resonance circuit, tuned at 40 Mc approximately.

The crystal mixes the input signal and the heterodyne signal and gives away the combination frequencies to the unit K1-5MP input.

The unit K1-5MP input circuit separates the intermediate frequency.

The antiphased directional coupler provides decoupling between signal and heterodyne circuits. The decoupling is carried out by a changing the propagation direction and absorbing the energy, which passes through directional coupler holes, by lower waveguide matching load. The 10 ÷ 17 db attenuation of heterodyne power, which goes to crystal mixes is provided due to the crosstalk attenuation. The crystal holder is a socket, into which crystal-plug with crystal is inserted. By moving and turning the crystal the tuning at lower standing wave ratio is carried out.

There is provided the best uncoupling and the least input signal power loss. The crystal - plug position is fixed with a nut screwed on the socket. From behind of the crystal the metal end cap is set. The end cap position variance makes possible the reducing of the standing wave ratio up to necessary value.

b) Klystron section

The klystron section is made as a "Magic T" (twin triplet). The Magic T is the junction of equal cross-section wave-guide bits, which is shown in the fig.N 27. It consists of the E-plane T-junction and H-plane T-junction.

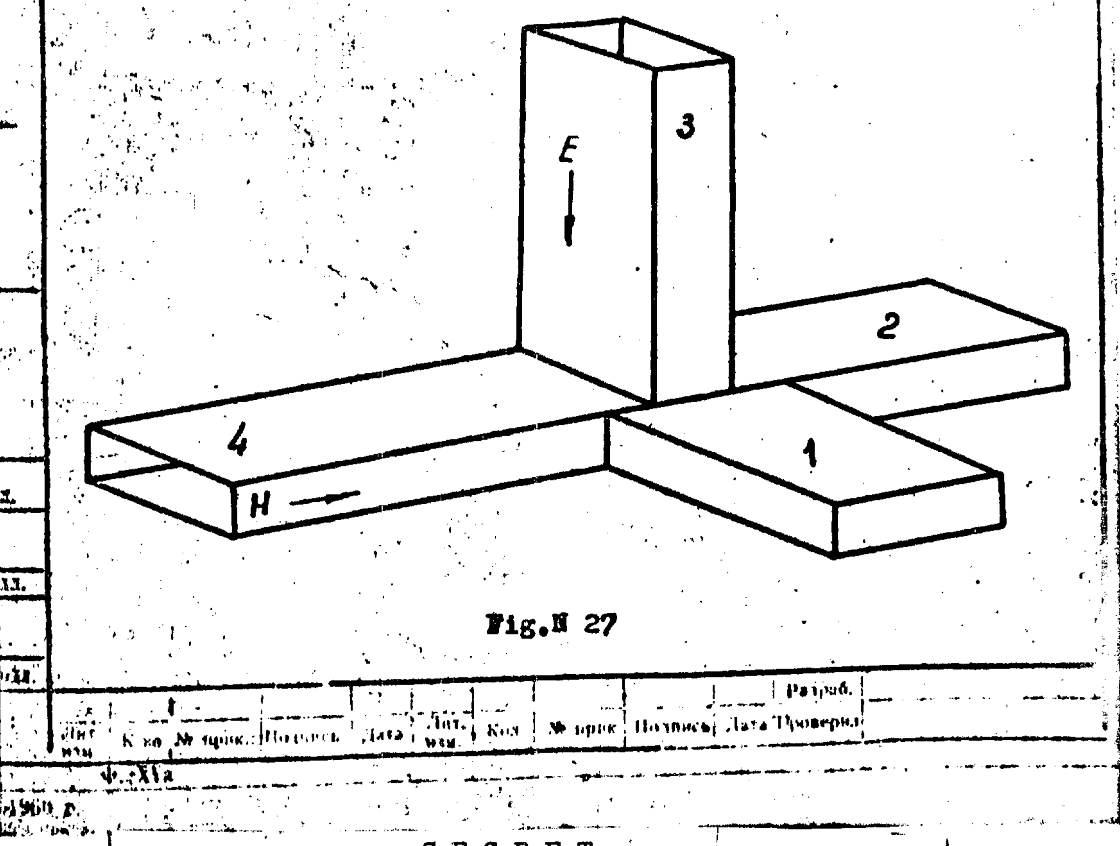


Fig.N 27

The matched twin triplet has the following property: U.H.F. energy donnot pass from the even arm to other even one and from the odd arm to other odd one, but it passes freely from the even arm to the odd arms and from the odd arm to the even arms (see.fig.N27). This property provides the uncoupling between the mixer arms NN 2 and 4 and provides also the heterodyne power equal dividing between arms N 2 and N 4. For triplet matching there is the arm N 1 absorbing load, which is made as a hetinax taper installed in the wavequide. The taper donnot intake the klystron energy in correspondence with the triplet property. The iris, the screw and the arm N 3 plunger serve as twin triplet tuners. By means of the iris and the screw a matching between the triplet and the arem N 3 is carried out. The plunger is provided for matching the klystron with the arm N 3 wavequide. The plunger tunes the heterodyne U.H.F. power output to arms NN 2 and 4 and is fastened in a position corresponding to max. heterodyne power output. The variable attenuators are in the side arms N 2 and N 4, which are connected with the "A" and "B" mixers. The attenuators adjust heterodyne power value, applied to the mixer (i.e. quiescent point of crystal is determined). The klystron holder is installed in the arm N 3. The heterodyne is the reflex klystron "K-38", to which cavity the +300 v is applied. The A.F.C. negative voltage of unit K1-5MP is applied to the reflector of the klystron. The variab

attenuator is also placed in the arm N 3, and adjusts the klystron power value, applied to the mixer crystals.

The antiphased directional coupler is installed on the broad side of the klystron arm N 3. Due to the fact the heterodyne energy is led partly to the coupler and so the klystron power monitoring is provided. The antiphased coupler output is covered by a cap.

§ 3. The unit KI-4bM elementary diagram

The "B" antenna KI-7M output U.H.F. signal is led to the unit KI-4bM crystal mixer. The unit KI-4aM klystron signal is led to the mixer through antiphased directional coupler. The mixer output signal is led to the unit KI-5M input, where the intermediate signal is selected by the unput circuit.

§ 4. The unit KI-5MP elementary diagram

The crystal mixer output I.F. signal is led to the unit KI-5MP input through the cable N 30.

I. The unit circuit

The unit input network is the band-pass filter (a kind of transformer-coupled circuit). The primary of cir... is formed by the inductance LI, the crystal mixer ca... citance, the connection cable capacitance and the stray capacitance. The inductance L2 with the grid circuit stray capacitance and tube ЛI input capacit...

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the secondary circuit. The secondary I.F. signal is applied to the control grid of I.F. amplifier Л1. The input circuit bandwidth is approximately 10 ÷ 16 Mc. The resister RI is provided for the crystal current constant component.

2. The A.F.C. channel

a) I.F. amplifier

The A.F.C. channel I.F. amplifier consists of five 6Ж1П stages. The first two are single tuned to 40 Mc stages, and the next three stages are stagger tuned:

the circuit L5 f = to 41 Mc
the circuit L6 f = to 39 Mc
the circuit L7 f = to 40.5 Mc.

The resistors R2, R5, R10, R14, R16 and R20 provide the circuits essential bandwidth by shunting of circuits. R3, R6, R7, RII, R15 and R19 are the stage cathode bypass resisters.

The pentode input capacitance is determined by interelectrode capacitance and a capacitance component, depending from an electron flow, by-passing the control grid. The component is the function of a tube transconductance. The tube transconductance variance changes the tube input capacitance and, accordingly with that, the preface circuit tuning. Since the unit KI-[illegible] I.F. amplification is controlled by the transconductance variance of the first tubes, the I.F. amplifier frequency-response curve middle will be also varied. To exclude the

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transconductance to tube input capacitence dependance, the cathode resistor do not shunt by a capacitor partially or totally. Since the unshunting cathode resistor a.c. component plate current drop voltage is applied to the tube input, the tube input impedance varyes in dependance of the its transconductance. The tube input capacitance may be done undependent from the transconductance by matching of an unshunting resistor value. For this purpose the resistor R6 is at the tube Л2 cathode and it provides the negative feed-back, which is necessary for I-F-frequency respose stability, when the gain is varying by means of A.G.C. variable voltage. The capacitors C6, CII, C16, C21, C26 are the bridging capacitors of the tubes. The capacitors C9, C14, C19, C24, C30 are the interstage transit capacitors. The resistors R9, R13, R17, R21, R22, R28 and the capacitors C8, C13, C18, C23, C28, C33 are the tube plate power supply filters. The tube filament power supply filters are formed by the chockes L14, L15, L16 and the capacitors C7, C15, C25, C31.

The resistor R27 determines the first stage operation regime and with capacitor C20 forms the screen grid power supply filter. This filter is necessary becaurse, when the unit and Radar are checking, the modulated sine frequency "10" voltage should be applied to the first stage screen grid through the capacitor C95. The additional A.G.C. negative bias is applied to the grid of the first 4 I.F. stages through the uncoupling circuits: R4, R5, R8, R12, C17 and C22.

The tube 6 stage is the I.F. amplifier ...

in clipping regime for excluding the " " frequency amplitude modulation of the input pulses. The Stage regime differs from the other stage regime by absence of bias and the screen grid voltage which is determines by the resistors R24 and R25. The capacitor C32 is filter capacitance. The stage frequency responce is determined by circuit L8, tuning at 40 Mc, and discriminator circuit.

The I.F. pulses are led to the discriminator through the capacitor C34. To take from the discriminator output the max. pulse amplitude, the I.F. signal is tapped from I/3 part of the coil L8. The tube 6X4П (Л 7) discriminator is made as a balancing network with the series frequency circuits.

The circuit, consisting of the inductance L9, the diode input capacitance, capacitors C36 and C38 and the stray capacitance, is tuned at 38.8 Mc. The secondary circuit L10 is performed similar to the primary and tuned at 42.8 Mc. The bandwidth of the circuits is within 5-6 Mc.

The I.F. amplitude clipped pulses are led to discriminator from the latter I.F. amplifier. The right half Л 7 plate or left half Л 7 cathode voltage value depends on the input signal deviation from a conformable circuit resonant frequency.

The voltage value will be larger in the circuit, which resonant frequency is nearer to an input signal frequency. The capacitors C38 and C39 are charged in the signal coming moment. The capacitor C38 "+" or "-" polarity charge network is: the left half tube Л 7 plate, the choke L9

and LII, ground the capacitor. The capacitor C39 charging network is: the tube Л7 right plate, the capacitor C39, ground, the chokes LII and LIO. In the intervals between input signals the capacitors will be discharged through the resistors R30 and R29 (discriminator load). The difference between the R30 voltage drop and the R29 voltage drop is an output signal of the discriminator. The output signal polarity is dependant on a sign of a signal frequency deviation from the I.F. value. The A.F.C. operation point is matched so that negative discriminator output video-pulses are used only. The pulse length is approximately 25 μ sec.

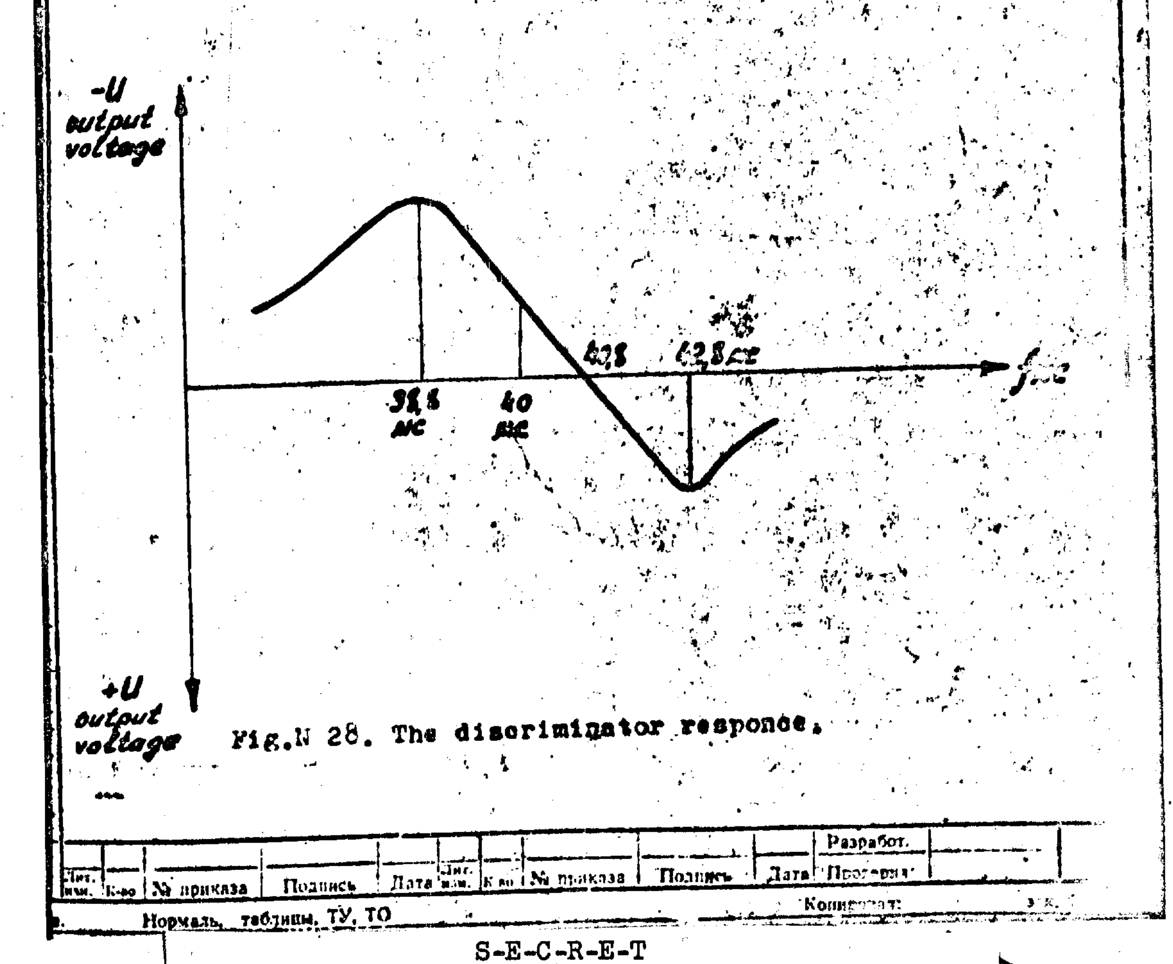


Fig.N 28. The discriminator responce.

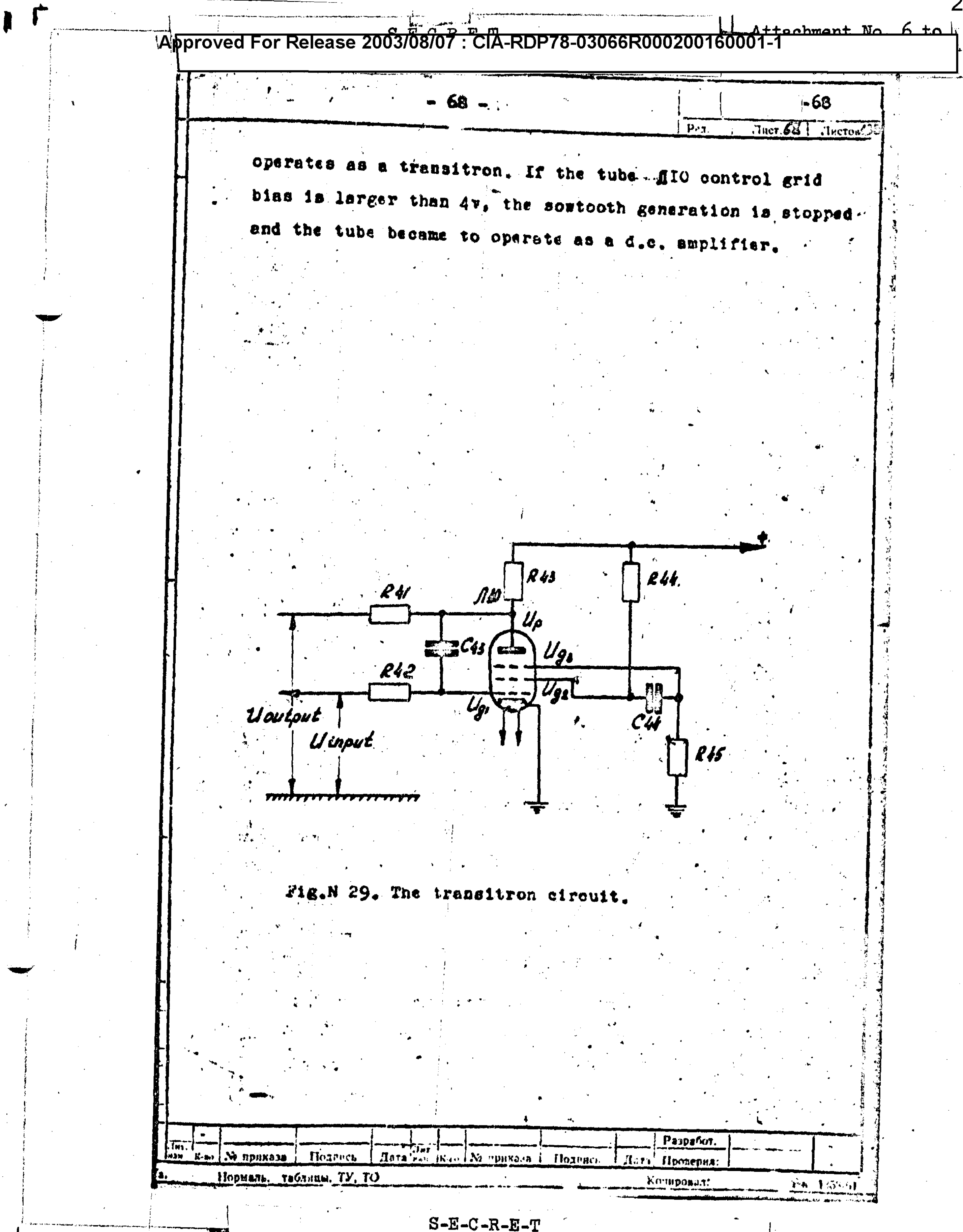
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To exclude 400c stray induction,the discriminator tube filament is biased by +25v relative to the cathode. The later is taken from the divider R90, R91. The capacitor C77 is high frequency by-passing capacitor. A discriminator current d.c. component by-passing network and next stage grid-leak are the choke L11. The capacitor C42 increases the A.F.C. operational stability.

The discriminator output negative pulses are taken from the load center point (between R29 and R30) and led to the video-amplifier Л8 (left half) input. The amplified positive pulses through the transit network C40, R34 are led to the cathode follower " Л8 (right half) grid and to the monitoring jack "Г7" ,which is provided for the discriminator responce monitoring. The cathode follower output video-pulses through the capacitor C41 are led to the rectification diode Л9 (left galf) and to the monitoring jack " Г1" (c.follower A.F.C.).The rectified positive voltage from the diode load R35 is led to the tube Л10. When positive pulse is at the tube Л8 cathode, the capacitor C41 is charging quickly through the diode and than it's discharging slowly through resistor R35.

The discharging time constant is adjusted so, that the capacitor is not dischar ging during time intervals between the pulses. So the negative aproximately constant voltage is obtained at the diode load. The voltage value depended upon an amplitude of the pulses led from the tube "Л8" cathode. When a negative voltage less than 4v is applied to the tube " Л10" control grid, the [illegible]

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operates as a transitron. If the tube Л10 control grid bias is larger than 4v, the sowtooth generation is stopped and the tube became to operate as a d.c. amplifier.

Fig.N 29. The transitron circuit.



Fig. 30. The transitron time correlations.

Let us to examine the circuit operation. The transitron operation principle is determined by a distribution of a pentode tube current in the dependence of a pentode grid potential between a plate and a screen grid. If a pentode grid voltage decreases and becomes negative one, the plate current also decreases and may be zero, since the screen grid current increases up to the max.value. In the opposite position all will be in reverse succession. I.e. the pentode grid serves as a control electrode and distributes the cathode current between the plate and the screen grid.

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Ф. XVа

The positive feedback between the screen and pentode grids through C44 is ample for circuit regeneration, when the control grid negative bias is less than -4v.

So if the pentode grid voltage is positive the current goes to plate. When a negative bias is applied to controll grid, the dynamic equilibrim of the circuit is broken out. Charged before C44 became to discharge through the screen-cathode space and resistor R45 and make across R55 a voltage dropping, which applies between the pentode grid and the cathode, biasing the grid (see fig.N 29).

The plate current is decreasing; this decreasing obtains the screen current increasing and a screen voltage dropping. After it is, the plate current will sharply increases up to zero. This process developes instantly till the plate current stops and the screen current became max. The pentode grid voltage became negative, since the screen voltage dropping is transmitted to the pentode grid through C44. Till the tube plate current cut off the C43 is charging. After some time a C44 discharging current decreases to a value, when a pentode grid voltage become sufficient for the plate current cutting on. The plate current became to increase, the screen current became to drop and, with it the screen voltage increases. This increasing by means of positive feed-back transmits to the pentode grid. The capacitor C44 became to charge. The positive (relative pentode grid) voltage, which is developed by the charging current across the R45, will increase the plate current

more and more. This process will continue till the plate current became equal to the max value and the screen current becomes equal to zero or the least value. After that the oscillation cycle will repeating. The transitron oscillation period depends on the time constant of the C44 charge and discharge. If the control grid negative bias is more than 4 v. the positive pentode-screen feedback is not ample for regeneration and the circuit is switched in the stable regime of d.c. amplifier. The tube Л10 plate control voltage divides by R40 and R41 and is fed to the cathode follower Л9 (right half) grid. The cathode load R36 control voltage through the switch "B-I", cable and socket N 29 is fed to the klystron reflecter.

When A.F.C. operates, the klystron reflector constant voltage is adjusted by the potentiometer R38 ("A.F.C."). When switched on the manual tuning, the cathode follower output is cut off by the switch "B-I" and the reflector voltage is obtained from the potentiometer R46. The divider, consisting of R81, R93 and C35, furnishes the A.F.C. sufficient operating conditions.

3. The Synchronization Channel

The I.F. amplifier is common for the synchronization error-signal and A.F.C. channels. An I.F. output signal is applied to the error-signal diode detector Л12 (left half of 6НЗП). The diode has the cathode load R47, C29. The choke L17 is the I.F. filter. The positive detector output pulse is fed to the video-amplifier Л12

(right half) grid through C47. The negative video-amplifier output pulses are fed to synchronization video-amplifier (right half of Л17) grid through C5. The resistor-coupled triode "6НI " synchronisation video-amplifier has the 3 amplification stages. The positive first stage Л17 (right half) output pulse is fed to the second stage Л18 (left half) grid through the network C72, R80. The negative plate load R82 pulse is fed to the third stage Л18 (right half) grid through C73. The positive plate load R84 pulse, through the network C76, R87 is fed to the cathode-follower Л19 (left half) grid.

The cathode follower output pulse is fed to a winding of the pulse transformer and synchronizes the blocking-generator Л19 (right half). From the blocking-generator cathode load the video pulses are led to the socket N 26 and to the monitoring jack "Г-8".

The R78, R82 and C70, C74, C75 are the plate power-supply filters. The R94 and C97 are the plate power-supply filter of the cathode follower Л19. The R55 and C95 are the blocking-generator plate power-supply filter and obtains the blocking generator d.c. regime.

The resistors R65, R92, R62 and the capacitor C94 determines a blocking-generator nature oscillation frequency. By means of R65 the blocking-generator nature oscillation period may be set longer than "Ш" - period by 80-100 sec. The synchronisation channel output video-pulses should not be amplitude modulated, so video-amplifier stages operate in clipping regime. But clipping is not providing

an absolute absence of A.M., which is provided by means of output blocking-generator. The synchronization channel output pulses are positive, its amplitude is more than 60v and its length is aproxomately 1.5 μsec.

4. The error-signal channel

The error-signal I.F. amplifier is a part of A.F.C. channel (Л1, Л2, Л3, Л4); its gain is aproximately 300 and its bandwidth is no less than 4.2 Mc. From the inductance L6, the I.F. pulses are simultaneously fed to the tube Л5 grid (A.F.C. 5-th I.F. stage) and to the Л-12 left plate (error-signal video-detector). From detector cathode load R47 the positive pulses are led to the video-amplifier input and the jack "Г-6" through the I.F. filter L17 and capacitor C47. The first stage output video-pulses are fed to the second stage input through the network C53, R52. From the second stage plate load R69 the positive pulses go to the grid of the cathode follower Л11 (right half). The cathode load potentiometer R86 slider output positive pulse goes to the error-signal output socket N 27 and the monitoring jack "Г3" ("e.f.e.-signal"), Besides that the resistor R86 positive video-pulse is fed to the A.G.C. input (tube Л13).

The resistors R58, R53, R57 furnishes the cathode follower tube (Л11, right half) regime, and determines the A.G.C. delay voltage. The capacitors C49, C52 with these resistors are formed the power-supply filters.

5. The A.G.C. loop

The A.G.C. circuit is a kind of delayed and amplified one. The delay is carried out by means of applying the negative bias to the left half of tube Л13 grid from the voltage-divider network, formed by R58, R53 and R57. The delay voltage is so, that A.G.C. became to operate when the U.H.F. mixer input signal power is approximately $5 \cdot 10^{-9}$ W. A rectified negative A.G.C. detector output voltage is fed to the control grids of the I.F. amplifiers Л1, Л2, Л3 and Л4 through the two-section filter formed by R63, R64, C92, C93. The jack "Г-4" provides the A.G.C. voltage metering.

The filter time constant is so that the error-signal frequency "Ω" component is excluded from the A.G.C. vol[illegible] So, the A.G.C. circuit reacts only on a slow [illegible] of input signal average power. The A.G.C. voltage is applied to the I.F. amplifier control grids through the decoupling networks: R4, C56, R8, C12, C17, R12, C22. The left half of the tube Л13 is a cathode follower. From the middle point of the divider, formed by R60 and R61, the A.G.C. voltage is led to the monitoring board through the unit "KI-13[illegible]". When the receiver input signal are sharply increased, the left half of the tube Л7 operates as a diode clipping the A.G.C. overshootings. The essential clipping level is adjusted by means of R71.

6. The unit power supply

The d.c. power supply is carrying out by the unit KI-10[illegible], which obtains the next voltages:

1. + 130v regulated,
2. + 300v unregulated,
3. - 300v regulated
4. - 147v regulated

The filament power supply is carrying out by 115 v 400c through the special transformer "Tp.-I", installed on the unit KI-5MP chassis. The all supply voltages are led into the unit by means of the cannon plug "Ш-7", through the filters, consisting of the chokes L26,L27,L28,L30,L24,L29 and capacitors C86, C89, C85, C84, C87, C80, C79, C81 and C78. The special winding is provided in the filament transformer for the feeding of the unit KI-4aM klystron filament. The klystron filament supply is led into the KI-4aM unit through the unit plug pins N 7 and N 13 and a special filters, consisting of chokes L18, L33 and capacitors C10, C27.

§ 5. The unit KI-6M elementary diagram

I. The channel of the reference voltage separation

The reference channel is provided for separation of the two 90° - shifted reference voltages from the A.M. input pulses. The positive synchronisation 0.5 ÷ 1.5 μ sec pulses, modulated with percentage 1.1% and frequency "40", are led through the socket N 26 from the KI-5MP unit. The pulses triggers the "single stroke" blocking-generator ЛI, which is normally cut off by means of a negative voltage from the divider RI, R2,

When the synchronization pulses go, the positive pulses are generated on the blocking-generator cathode load R4,R9. The amplitude 30÷60v pulses go from the resistor R5 to the socket N 25 and the jack Г-I. The amplitude 12÷40v pulses go through CI to the socket N 28. The blocking-generator cathode load full pulses go to the reference channel detector (ЛI in diode connection, which detects the frequency "10" component from the amplitude modulated pulse train. This component is the frequency "10" reference voltage.

When the synchro-pulses appear, the capacitor C4 is charged through the tube. Within the pulse intervals the capacitor is discharged through the resistor R8. The detector output voltage shapes a distorted sawtooth. Since the recurrence frequency is modulated with frequency "10", the output constant component repeats the sine shape of the recurrence modulation.

From the detector load R8 the separated reference voltage goes to the low frequency amplifier through the filter R9, C5, RIO, C6 and the coupling capacitor C7.

The resistance amplifier has a negative feedback. The grid resistor RII by-pass the current constant component. The resistor RI5 provides the constant grid biasing and the negative feed back. The resistor RI2 is an L.F. amplifier plate load; the capacitance C8 is a plate supply decoupling. From the first stage plate load a frequency "10" voltage is fed to the second stage control grid (right half of Л2) through the coupling capacitor CI4 and the resistor RI6. From the second stage cathode an "10" - frequency voltage is fed to potentiometer R170 through the coupling capacitor C22

to compensate the error-signal recurrence frequency modulation. The reference voltage goes from the right half of Л2 plate to the amplitude adjusting potentiometer RI9 ("Amplitude"). From the potentiometer RI9 slider the reference voltage is fed to the phaseshifting stage (Л3 left half). The phaseshifting stage is a paraphase amplifier loaded by the phaseshifting network R22, CI2. The output phase is depending on the potentiometer R22 position. So, the reference voltage phase may be shifted, i.e. the unit phasing may be carried out, by means of the potentiometer R22 ("Phase").

The reference voltage is fed from the phaseshifting stage to the amplifier Л3 (right half), which is loaded by the phasesplitter bridge: CI5, R24, CI6, R26, RI47. The bridge element values are matched so, that an arm middle point voltages are phase-different between themselves by 90° ("reference voltage 0°" and 90°). The precise 90° phaseshift is set by means of the potentiometer RI47. The resistor R25 is the left half tube ЛI3 gridleak in the "A" - regime.

The reference voltages (0° phase and 90° phase) are fed to the driving voltage channels through the regime "A" normally closed contacts I-2 and 4-5 of the relay P-I. The regime "B" frequency F Я reference voltages (0°phase and 90° phase) are taken from the unit KI-7M reference generator. This voltages go to the unit KI-6M input through the unit KI-I3M. From the plug Ш 5 pins I0 and II the reference voltages go through the divider R86, R85, R84, R83 to the relay "P-I" contacts 6-4 and 3-I and after that go to the driving voltage channels.

2. The driving voltage channels "Y" and "Z"

The channel "Y" is identical with the channel "Z" excepting the reference phase difference, which is equal to 90°. The 0° phase and 90° phase reference voltages are applied to the reference amplifiers (left half of Л4 - "Y" Channel and left half of Л13 - "Z" channel).

"A" - regime

The 0° and 90° phase frequency "Ю" reference voltages are applied through the relay PI contacts 2-1 and 5-4 to the amplifier control grids.

"B" - regime

The two unit KI-7M reference generator frequency "Я" output voltages, phaseshifted by 90°, are fed through the relay P-I contacts 3-I and 6-4 to the amplifier control grids if the command N 2 is locking on.

Let us examine the channel "Y" diagram only, because the channel "Z" is identical with it. From the reference amplifier plate load R83 (R120) the amplified voltages through the capacitor C42 (C49) and the resistor R92 (R123) is fed to the phaseinverter control grid. The phaseinverter or the paraphased amplifier is the right half tube Л4 (Л13).

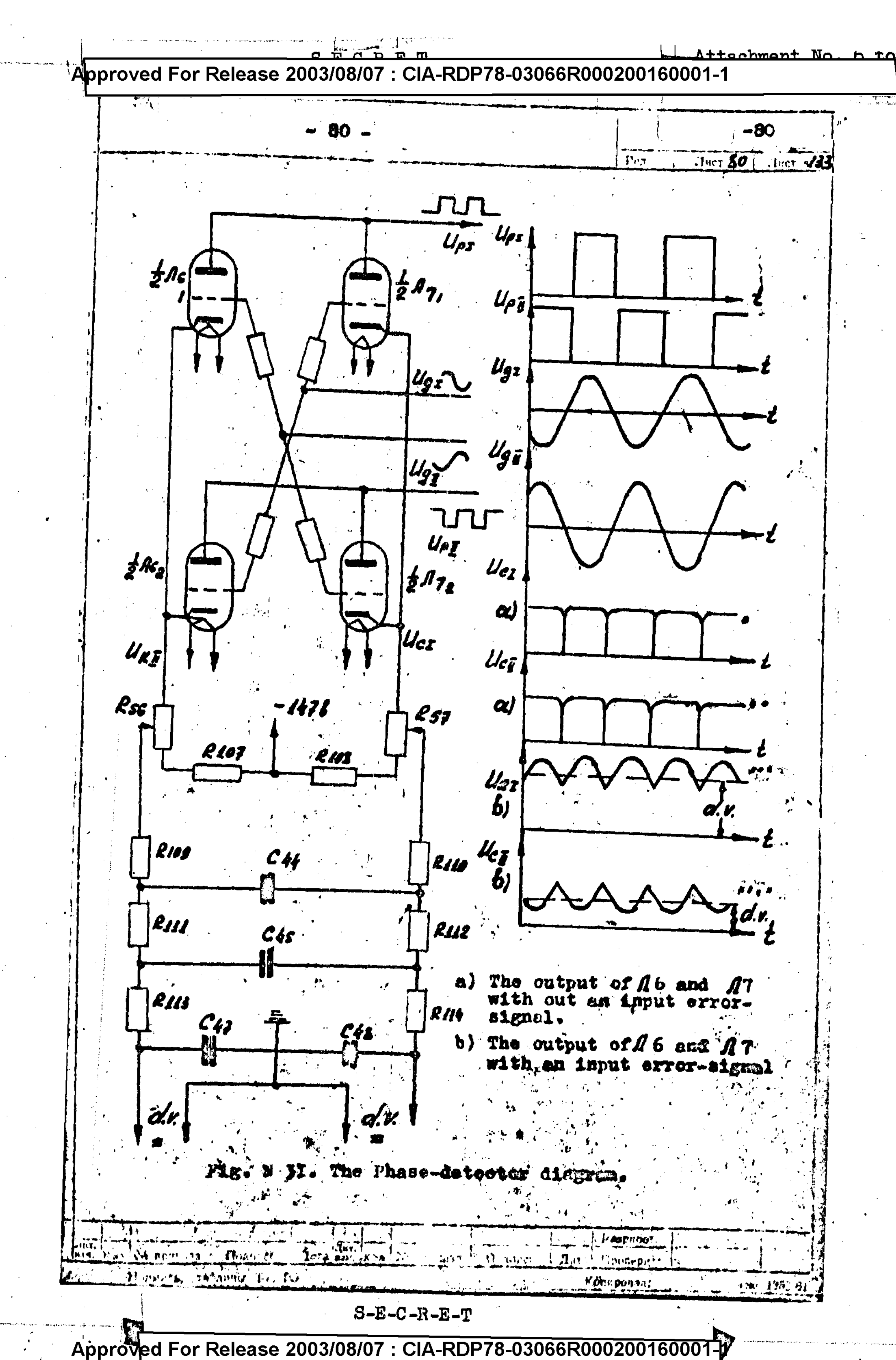
The two equal and antiphased reference voltages are taken out from the plate resister R91 (R122) and from the cathode resistors R96, R94, R95 (R124, R125, R126) and they are fed through the coupling capacitors C19 (C36)

and CI7 (C34) to the clipping amplifier grids. Besides that the 0° (90°) phase reference voltage goes from the resistor R95 (RI26) to the plug connection Ш 6 pin 6 (7) for the unit monitoring and tuning.

The clipper amplifier Л5 (ЛI4) operates in a cutoff regime from below and above. The input sinusoidal voltage transformates into the antiphased squarewave pulses, which are taken from the resistors R99,RI00 (RI30, RI3I). The pulses are applied to the plates of the commutating tubes. The resistors R98 and RI02 (RI33, RI29) provide the grid current limitation.

The phase detector circuit consists of the cathode followers, which plates are fed by the antiphased rectangular reference pulses. The antiphased error-signal sinewaves are applied to the control grids of the cathode followers. The pulse reference voltage feeding the above tube (½Л6/I; ½Л7/I) plates is antiphased with the one feeding the below tube (½Л6/2; ½Л7/2) plates. The error-signal voltages applied to the grids of the tubes ½Л6/I; ½Л7/2 and the tubes ½Л6/2, ½Л7/I also differ by I80°. Let us examine the circuit operation. The detector tubes commutate in turns: ½Л6/I and ½Л7/I or ½Л6/2 and ½Л7/2.

If an input error-signal is absent, a constant voltages U_{kI} and U_{kII} are obtained across the cathode loads as a result of rectification. When the error signal is at the phase detector input, the values U_{kI} and U_{kII} vary with dependance from a phaseshift between the reference voltage and the error-signal. In this case a cathode output pulsating voltages are obtained, and its constant component is



a) The output of Л6 and Л7 with out an input error-signal.

b) The output of Л6 and Л7 with an input error-signal

Fig. № 31. The Phase-detector diagram.

proportional to error-signal amplitude and Cos of phase shift angle between reference voltage and error-signal voltage. This rectified voltage goes to the power amplifier through the 3-section RC-filter, which suppresses the a.c. component.

The power amplifiers Л17 and Л18 (Л19, Л20) are a cathode followers. The tube halves are connected in parallel to increase the linearity range of the driving voltage dependence on the tube current. The driving voltages are fed to the autopilot from the cathode loads RII7 and RII8 (R149, R150). A cathode follower balancing is carried out by means of the twin potentiometer R56 (R128), when the phase detector input error-signal is equal to zero. The potentiometers are installed on the unit front panel with the "Balance Y". ("Balance Z") inscription. The power amplifier plate power supply is fed through the voltage dropping resistor RII5 (RI46). Since the operational summary cathode follower current is approximately constant, the plate voltage is not vary practically. The output driving voltages are led to the plug Ш6 pins IO-II and 12-13 from the cathodes of the tubes. The driving voltage loads of the channel "Y" and the channel "Z" are a resistors equal to I kohm.

3. The channel of the error-signal separating

The error-signal channel is provided to separate a signal which is proportionate to a percentage amplitude modulation of the input pulses and is not depend upon the pulse amplitude. The positive pulses, amplitude modulated with frequency "Ω" and recurrency frequency modulated with the

25X1

percentage I.I%, are applied to the regime "A" detector - A.G.C. (the variable-mutube Л9) through the socket N 27. The detector function is carried out by the grid-cathode space. The A.G.C. is essential for exluding the output signal dependance on the input pulse average amplitude. The envelope amplitude corresponds the input pulse average amplitude, when the A.M. percentage is constant. The detected constant component determines the operational point of Л9. So, the large pulse average amplitude detecting will case the large negative control grid biasing and decreasing of the tube gain. There is set the regime in which the output error-signal varyes less than 10% within the pre-set input pulse amplitude variance range. The regime is set by means of the tube Л9 screen voltage adjusting (by variance of a resistor R9 value). The negative feedback frequency "F0" voltage is applied to the control grid.from the error-signal channel amplifier phaseshifting network. This voltage suppreses the error-signal component, determined by the pulse recurrence modulation, which case the parasite variance of the error-signal amplitude and phase.

The compensation ratio is adjusted by the potentiometer R170. The tube Л9 plate load is the time motor range potentiometer R64; so the error-signal channel gain increases as a determined function of time, when the time motor is moving. The range potentiometer slider error-signal is fed to the potentiometer R57 "F0 -gain" through the coupling capacitor CIO. The error-signal amplitude and with it the regime "A" driving voltage transconductance may be varied by the potentiometer R57.

25X1

The potentiometer R67 slider error-signal goes to the selective amplifier input through the normally closed contacts 14 and 13 of the relay P-I. The selective amplifier (Л10 and Л11 left half), provided for the error-signal first harmonic selection, is an underexited R-C generator.

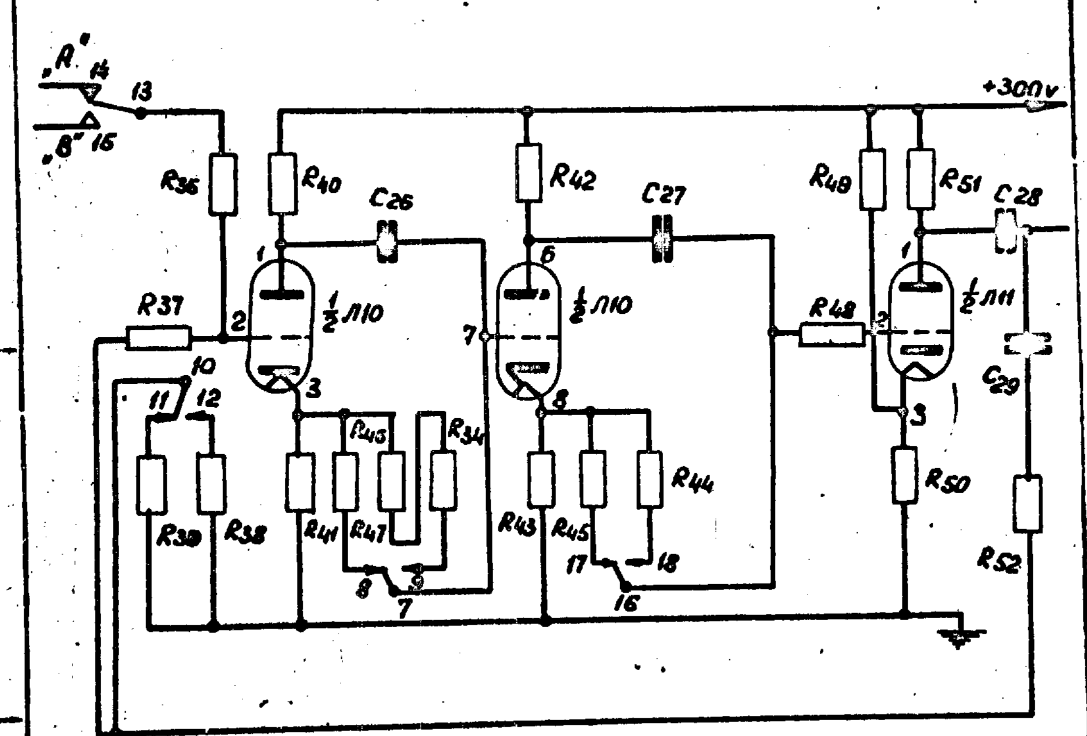


Fig.N 32. The selective amplifier

The selective amplifier is a 3-stage amplifier with a frequency discriminated positive feedback. The first two stage diagram is analoqous to the reference channel phaseshifter diagram. The third stage is an ordinary

resistor amplifier. Since the feedback voltage phase depends on a frequency, the amplifier phaseshifting elements should be set, so, that the feedback overall phaseshift is equal to 360° at the error-signal frequency "Ю" in the regime "A" and at the error-signal frequency "Я" in the regime "B". The first stage has the 60° phase shift owing to the phaseshifting network C26, R34, R46, R47, R41. The second stage carryes out the 90° phase shift, owing to the network R43, R44, R45, C27. The third stage carryes out the 180° phase shift. The feedback network C29, R52, R39, R38, carryes out 30° phase shift. approximately. To provide the precise 360° phase shift, the first stage phase shift is adjusting by the resistor R47 for the "Ю" - frequency and by the resistor R46 for the "Я" - frequency.

If a frequency is not equal to "Ю" in "A"-regime or to "Я" in "B"-regime, the overall phaseshift isn't equal to 360° and accordingly the positive feedback decreases. The selective amplifier frequency response is a resonance curve with at the "Ю"-frequency in the regime "A" or at the "Я"-frequency in the regime "B". The amplifier frequency response bandwidth depends on the feedback voltage value and adjusts by means of the feedback divider (R39 in "A" - regime and R38 in "B"-regime).

When the command N 2 looks, the selective amplifier is retuned from the "Ю" - frequency to the "Я" - frequency by means of the relay P-1, which switches the resistors of the pheseshifting network and the feedback divider. The resistors R36 and R37 serve for a decoupling of the input and feedback networks.

The resistors R49 and R50 Provide an essential bias of the tube Л11 grid.

A selected and amplified error-signal goes from the left half tube Л11 plate load R51 to the paraphase amplifier through the coupling capacitor C28. The two output antiphased voltages are taken out from the cathode and plate loads of the paraphase amplifior. The cathode and plate loads are so adjusted that both of the output voltages have equal amplitude.

The paraphase amplifier cathode output error-signal is fed to the cathode follower (Л11 right half) grid. The later gives away the tracking beacon signal through the plug connection "Ш -6" pin 8 and the K1-13M unit to the unit K1-12MP input. The same signal isa led through the same plug pin 9 for a selective amplifier tuning and an operation monitoring of the error-signal channel regime.

The "B" error-signal channel consists of the error-signal detector, the error-signal A.G.C. tube, the selective amplifier, the phaseinvorter (or paraphase amplifier) and

the cathode follower. The latter three are common with the "A" error-signal channel. The phaseshifting network is tuned at the "Я" - frequency by the relay P-1 in the "B" regime. The socket N 24 videopulses, amplitude modulated with Я -frequency, are applied to the detector Λ8. The detector and A.G.C. circuit operates analogically to the "A" detector and A.G.C. circuit.

The capacitor C23 charging time constant determines by an internal resistance of the grid-cathode space of the tube Λ8, and the discharging time constant determines by the resistor R32 value.

The error-signal "Я" detected voltage is amplified by tube Л8 and led to the potentiometer R68 through the capacitor C25. The tube regime is adjusted by the resistor R73 and the divider R79, R73 so, that the output error-signal variance is less than 15%, when the input pulse amplitude varyes in the pre-set limits.

The potentiometer R-68 ("Я" - gain") serves for normal adjusting of the error-signal gain in the "B"-regime.

The error-signal goes from the potentiometer slider through the relay P-1 closed contacts 15 and 13 to the selective amplifier input.

Лит	К во	№ врок.	Подпись	Дата	Лит	Кол	№ врок.	Подпись	Дата	Разраб. Проверил

4. Time motor

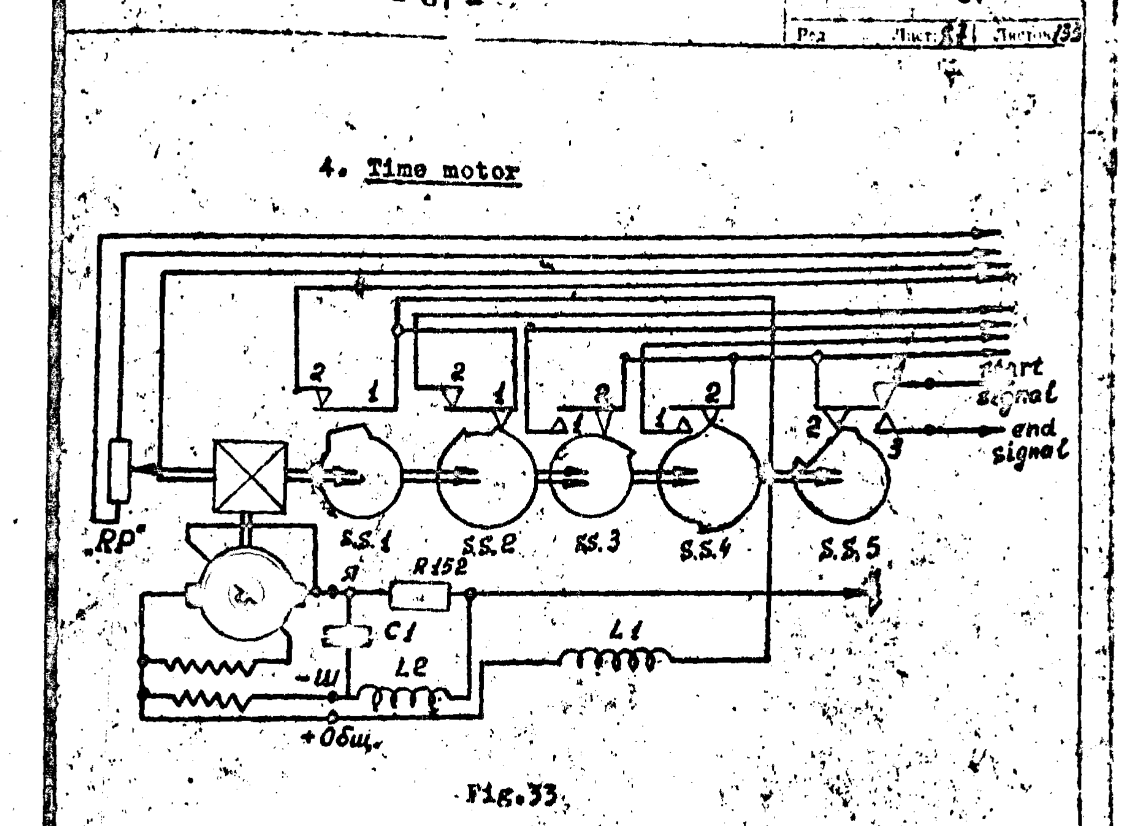


Fig.33

When the time motor is in start position the spring set 2 contacts 1 and 2 are closed the spring set 5 contacts 1 and 2 are also closed and the start signal is on the plug connection Ш6 contract N 16.

The range potentiometer R64 slider is in the isolated starting position.The spring set 1,3,4 contacts 1 and 2 are open.

When the voltage +27v is applied to the plug "Ш5" pin 13 ("drop command"), the time motor starts moving.

The motor rotating is geared through the reducer to the cam spindle and with it to the slider of the range potentiometer

The spring set 5 contacts 1 and 2 became opened and the start signal is put an end in the 3 sec. time.

The range potentiometer slider moves from the above to below (accordingly to the elementary diagram - to the Л9 plate).

After 39 sec., the spring set 3 contacts 1 and 2 are closed and with it the voltage +27v appears on the plug "Ш5" pin 9 (i.e. the command N 1).

After 198 sec, the spring set 4 contacts 1 and 2 are closed and the voltage +27v appears on the plug "Ш5" pin 15 (command N 2 unlocking signal).

When the range potentiometer reaches the end position (i.e. latest turns of the potentiometer), the spring set 5 contact 2 and 3 closed and with it is produced the "end signal", (+27v) which is led to the plug Ш6 pin 15. In the same time, the sping set 2 contacts 1 and 2 became opened, the spring set 1 contacts 1 and 2 became closed and the time motor is stopped.

To return the time motor in the starting position, the voltage +27v should be applied to the plug connection "Ш5" pin N12.

§6. The unit K1-7M

The unit consists of:

1. The reflector and the exiter.
2. The retary joint.
3. The flexible waveguide section.

Fig.34. The antenna K1-7M

The antenna is a paraboloid 340mm in diameter, fed by a rear waveguide feed at its focus (F=132 mm). The head of the feed (exiter) is a forked and back bended waveguide. To obtain the conically scan, the exiter head is displaced from the reflector axis by means of a waveguide curving.

The feed picks up an electromagnetic waves, focuced by the reflector and exites the H_{01} wave in the feeding waveguide. The rotating joint consists of the two wavequides, one normal to another, which are jointed by means of the coaxial line. The coaxial is coupled with the stationary waveguide by means of the ball probe, and with the rotary waveguide by the coupling loop.

The rotary waveguide H_{01} mode transfarmes into the co[illegible] line EH mode symmetrical wave.

The ball probe installed in the stationary waveguide [illegible] the H_{01} mode wave in it.

The rotary connection is made in the outer conductor of [illegible] coaxial. To exlude the U.H.F. energe leakage, the half-[illegible] "choke" is provided.

The [illegible] flexible corrugated brass made waveguide provide[illegible] the energy transition, when the unit K1-7M is slightly moving relatively to the framework.

The unit K1-7M scanning device is provided to obtain [illegible] conmically scanning of the beam in a space and to produce the two sinuseidal 90° phaseshifted voltages (reference voltages). This voltages are produced by the reference generator. The rotating is obtained by the motor "[illegible]", which has a centrifugal governor in an exiting circuit.

The unit K1-7M fastening device is an aluminium frame, which has three hinge bearings with bolts to fasten the unit in the correspondance threading holes of the misile "[illegible]".

When is a voltage +27 v on the plug connection pins 1 and 2, the motor is fed.

The centrifugal governor of the motor provides a rotation speed constancy, when the power supply varies.

The motor spindle is geared with the exiter spindle and the reference generator rotor by means of the reducer with the transmission ratio 1:2, so the reference generator operates synchronous to the beam rotating.

The two sinusoidal reference voltages, led to the plug pins NN 3,4,5 and 6, are equal to 30±2v.

§ 7. The unit K1-8M elementary diagram

The unit input circuit is the coupling with the single diode mixer by means of the inductance L1 and L2. The inductances with the capacitor C72 and the stray circuit and cables capacitances form the I.F. tuned resonance circuits. The capacitors C2, C3, C4, the inductances L3, L4 and the resistor R5 form the crystal current line filter.

The I.F. pre-amplifier is taken away from the unit K1-8M chassis and placed into the unit K1-4M plate. This spacing improves the noise-figure of the receiver.

The I.F. pre-amplifier consists of two stages "6Ж1П", triode connected. The first stage is a grounded cathode circuit. The second stage is a grounded grid circuit. To neutralize the first tube grid-plate capacitance, the inductance L6 is, which besides that, bypass the second stage current constant component.

To neutralize the second stage cathode-plate capacitance, there is the inductance L8, which with the same capacitance form the I.F. resonance circuit.

The Л1 plate inductance L7 with the circuit capacitance and the tube internal capacitance form the I.F. tuned circuit

S-E-C-R-E-T

The L9 circuit is connected into the Л2 plate line and shunted by the resistor R4. This circuit is connected to the I.F. amplifier input circuit by means of the short coaxial cable.

These two circuits with the capacitors C15, C15 and the cable capacitance form the I.F. bandpass filter. For the purpose of self bias, the cathode resistors RI and R3 and the capacitors CI and C8 are.

To provide the operational stability the decoupling filters are (the plate filter L10, C9, C10, the filament filters L12, C12 and C13). Besides that, there is the every tube filament filters: consisted of L5, LII, C5, C11.

The main I.F. amplifier consists of five 6Ж1П stages Л3, Л4, Л5, Л6, Л7). The tubes are parallel fed and have the circuits in the Grid networks.

The whole of the I.F. amplifier consists of the two staggered triples. The I.F. preamplifier and the 2 first stages the main I.F. amplifier form the first triple: the next 3 stages form the second triple.

The I.F. amplifier circuits are tuned to:

I. The I.F. preamplifier with the first circuit of the I.F. main amplifier f = 40 Mc;
2. The I.F. main amplifier first stage ff = [illegible]
3. The I.F. main amplifier second stage ff = [illegible]
4. The I.F. main amplifier third stage ff = [illegible]
5. The I.F. main amplifier fourth stage ff = [illegible]
6. The I.F. main amplifier fifth stage ff = 40 [illegible]

S-E-C-R-E-T

When the Radar is operated in the regime "A", the negative voltage -I47v goes from the plug connection Ш3 pin N I5 and from the divider R26, R27 to the screen grid of the I.F. last stage; so the receiver is cut off.

When the regime "B" is switched on, the voltage -147v is taken away from the plug Ш3 pin N I5, since the unit KI-9M relay P-I operates. So the divider R26, R27 negative noltage is applied to the Л7 screen grid only. In the moment, when the unit KI-9M strobepulses, having amplitude 80v - I30v, go to the socket Ф-22, the Л7 screen voltage become positive, so the receiver opens.

When tuned and adjusted, the receiver may be open by applying a positive voltage (+I30v) to the Л7 screen grid by means of switching the toggle switch in the position "+".

The A.G.C. negative biasing is applied the to the controll grids of the first 4 I.F. stages. through the filters CI8, R8, C23, RI2, C28, RI6, C33, R20.

The coils LI4, LI6, LI8, L20, L22 and the capacitors CI9, C24, C29, C34, C39 form the filament filters , of tubes. To avoid the 400c induction to the I.F. circuits, the filament wiring is carried out by a shielded conductors. The resistors R7, RI0, RI5, RI9, R23 and the capacitors CI7, C22, C27, C32, C37 provide a tube self biasing.

The C2I, C26, C3I, C36, C42 are inter stage coupling capacitors. To provide an operational stability, the RC filters are in the plate networks of the I.F. amplifier. An I.F. signal pulses go from the last I.F. stage to the detector Л8, which is diode connected. The plate and the

screen grid are jointed and grounded through the resistor R28, bridging by the capacitor C45."

The network R28, C45 determines a tube potential distribution and increases the detector efficiency. The plate of the detector is a tube control grid.

The capacitor C46 and coil L25 are an I.F.filter.

The positive output pulses are taken from the detector load R30 and applied to the control grid of the first video-amplifier through the capacitor C47.

The two stage video-amplifier (Л9 and Л10)is a resistor-coupled wide-band amplifier with a positive feedback through the coupling network R34, C49.

The negative feedback is carried out through the resistors R32 and R36. This circuiting has no requirement to big value of the cathode and screen bridging capacitors.

The positive feedback between the 1-st and the 2-nd stages increases a gain ang compensates a gain decreasing occasioned by the negative feedback. When the frequency became high, the impedance of the network R34, C49 decreases and with it the positive feedback and the gain increases. So the capacitor C49 compensates the steep slope of a frequency response curve. For the purpose the compensating coil L31 is placed in the plate load of the video-amplifier second stage.

The 2-nd video-amplifier output positive pulses are fed to the cathode follower grid (Л11 right half). The A.M. posi-

tive video-pulses go from C.F. load potentiometer (R-48) to the unit output socket N 24 by a coaxial cable.

The error-signal amplitude may be adjusted by the potentiometer R-48.

The C.F. overall load (R39 and R48) output pulses are led to the control grid of the A.G.C. plate detector (Л12 right half).through the coupling capacitor C52 and to the Л11 left haef grid through the capacitor C58.

The negative delay voltage is applied to the A.G.C. detector grid (Л12) from the divider R41, R42.

The Л12 plate load is shunted by capacitor C54.

The network R49, C57 is a plate filter.

To vary the delay voltage, the divider negative voltage is led into the A.G.C. line, The voltage may be variated by the "M.G.C." potentiometer R47 and monitored at the jack "Manual G.C.".

When an input pulse is larger than the delay voltage, the tube Л12 is cut in.

The Л12 plate output voltage is applied to the control grids of the tubes Л3,Л4,Л5 and Л6.

The Л12 left half is a cathode follower and it serves for the A.G.C. monitoring.

The A.G.C. output voltage may be monitored at the jack Г-1 (A.G.C.) on the unit K1-8M front panel.

The tube Л11 left and the tibe Л13 right half are two stages of the video-amplifier, which inject the pulses to the

K1-9M unit.

The tube Л13 left half is a cathode follower; the load R62 output pulse goes to the socket N 23.

The plate compensating coil L29 of the Л13 right half improves the pulse shape.

The control grid biasing of the cathode follower and of the first video-amplifier is obtained from the voltage divider R66, R65, R64.

The unit K1-8M d.c.power supply is provided by the recti-fier K1-10M, which produces the following voltages:

1) +130v regulated;
2) +300v unregulated;
3) -147v regulated.

The unit K1-8M filament power supply is carried out by the special transformer "TP-1" from the 115v 400c source. The all feeding voltages are led into the unit K1-8M by the connection plugs Ш2 and Ш3.

§ The unit K1-СМ elementary diagram

1. The seaching regime

When the Radar is switched on, the autoselector (or range unit) starts a searching over the range band. The input pulses going into through the socket N 25 have an amplitude within 35v ÷ 60v and a pulse duration within 0.7÷1.0 μ sec.

The input synchro-pulse triggers the multivibrator Л10 through the buffer (Л9 left half), which is normally cut off by means of a negative bias from the divider R77, R78. When the synshro-pulse is injected the tube Л9 left half cut in and produce the plate load negative pulse.
The miltivibrator (M.V.) left half is normally cut in, the right half is normally cut off by means of a voltage, which the left half current develops across the common cathode load R86.

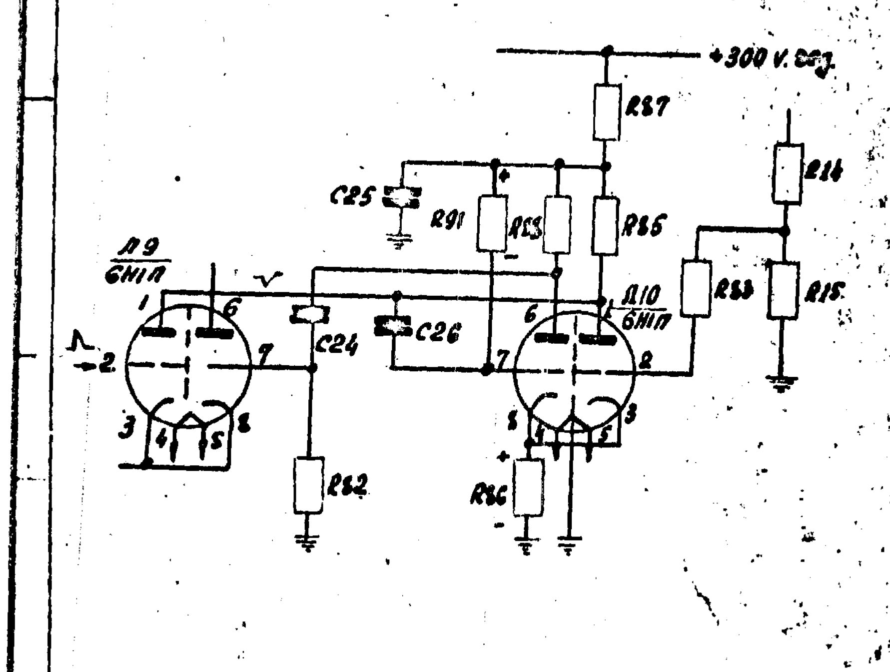
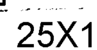


Fig.35. The multivibrator diagram



U_{input}

U_a

Right half of Л IO

U_g

Left half of Л IO

U_p

Left half of Л IO

U_c

Output of Л IO left half

Output of Л IO right half

Fig. N 36. Time relationship of multivibrator.

When synchro-pulse is injected, the buffer plate negative pulse, transmitted through capacitor C26 decreases a potential of the left grid and, with it, the current of left half. So, the cathode drop will be decreased and a current appears through the right half. The plate drop is transmitted at the left half grid and the left half become to cutting of. The avalanche-type process develops, as a result of which, the left half become cut off and the right half become cut in.

When the right half is cut in, the capacitor C26 become to discharge across the network, consisting of the right half, the resistor R86, the power source and the resistors R87, R91. The negative resistor R91 drop voltage is applied to the left half grid and cuts out the left half. Since, the discharging current is exponentially decreasing the left half grid voltage become to increase.

The process lasts till the capacitor voltage become equal to a value essential for turnover of the multivibrator. The higher voltage is applied to the control grid of the Л10 right half, the longer capacitor C26 recharge time is needed, i.e. the longer positive pulses will be made across the left plate lead R88.

Since the transitron sawtooth is applied to the M.V. right grid, the pulse length will be variating accordingly with the sawtooth low.

The multivibrator output pulses go to the differentiating circuit R82, C24. The differentiated M.V. pulses are fed to the control grid of the amplifier (Л9 right half). After the differentiating the positive pulses correspond to the M.V. pulse front edge and the negative pulses correspond to the M.V. pulse rear edge. The positive pulses are partially suppressed by means of zero biasing of the amplifier, and with it, a grid current. The positive pulses corresponded to the rear edge of the M.V. pulses are separated at the plate load R81, and than fed to the buffer Л11 control grids.

The buffer Л11 (6Н1П) is normally cut off by means of the divider R92, R93 negative biasing.

The positive pulses cut in the buffer and the positive pulses appear at the plate load R97 and at the windings of the pulse transformers; the later trigger the strobe blocking-generator and the half-strobe blocking-generator.

The tube Л12 (6Н1П) left half is a half-strobe blocking-generator, which output pulses go to the cathode-follower Л12 (right half). The loads of the cathode follower are the delay line Л3-1 and the resistor R98.

The cathode follower output "nondelayed" half-strobe is applied to the pentode and screen grids of the first coincidence stage Л4 (6Ж2П).

The delay line output "delayed" half-strobe is applied to the pentode and screen grids of the second coincidence

stage Л5 (6Ж2П). The time delay of the delayed strobe equal to 0.8 ÷ 1.0 μsec.

The strobe blocking-generator Л13 (6Н1П) is triggered by negative pulses from the Л11 left half.

The strobe duration is approximately 2 μsec. Than The strobe is applied to the cathode follower Л13 (left half) grid. The cathode follower output pulses are fed to: the command N 2 coincidence stage Л14 (left half) and to the socket N 22. The resistor R103 strobe is led to the monitoring jackГ2.

In seaching regime the M.V. pulse lenght is periodically variated from longer value to shorter value and it carry out the variance of a spacing between the synchro-pulse and the half-strobe (or strobe).

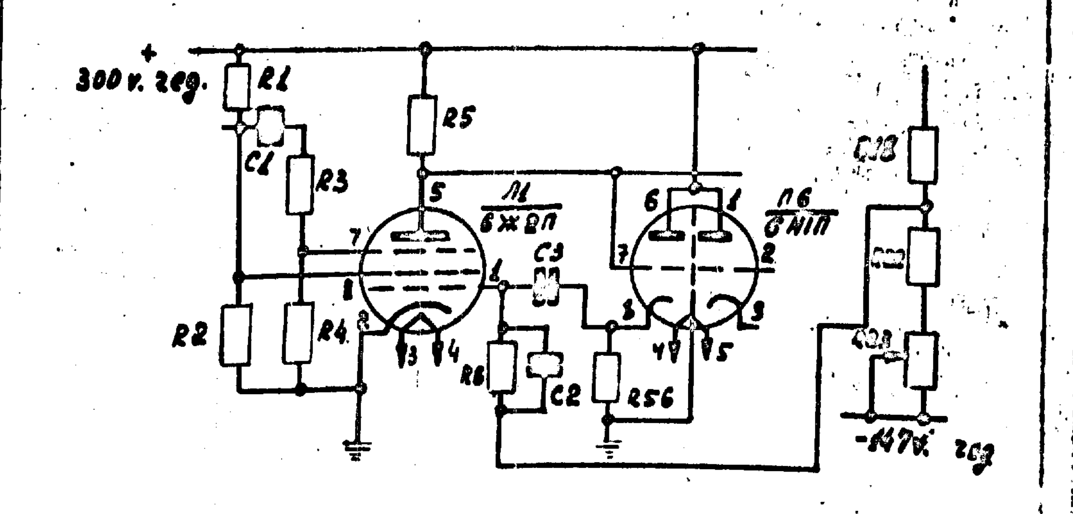


Fig.37. The transitron generator diagram.

The M.V. pulse length variance is carried out by means of the controlling cascade Л1 and Л6 left half. The cathode follower Л6 (left half) with the capacitor C3 are a negative feed back network, which connects the plate and the grid of the tube Л1.

In seaching regime the controlling cascade operates as a transitron generator and produces the "sawtooth", which is fed to the grid of the cathode follower Л2 (left half).

Let us examine a transitron operation (see fig.N 39).

Let us assume, the Л1 plate voltage is decreasing and the controll grid voltage is increasing (the fig.N 39) space a-b).

When difference between plate voltage and cathode voltage will be small, there will be redistribution of a tube current between the plate and the screen grid so, that a screen current became to increase and, with it, became to increase a voltage drop across the resistor R1. The capacitor C1 became to discharge through the screen-cathode space and resistors R3 and R4. The C1 discharging current develops the negative voltage across the resistor R4, which is applied at the pentode grid and cut off the tube Л1 plate current. It leads to an increasing of plate voltage and control grid voltage and, with it, to the screen current increasing still more. Than became the regeneration (the fig.N 39 point "b"). The capacitor C3 became to charge by power supply through the R5, the cathode follower

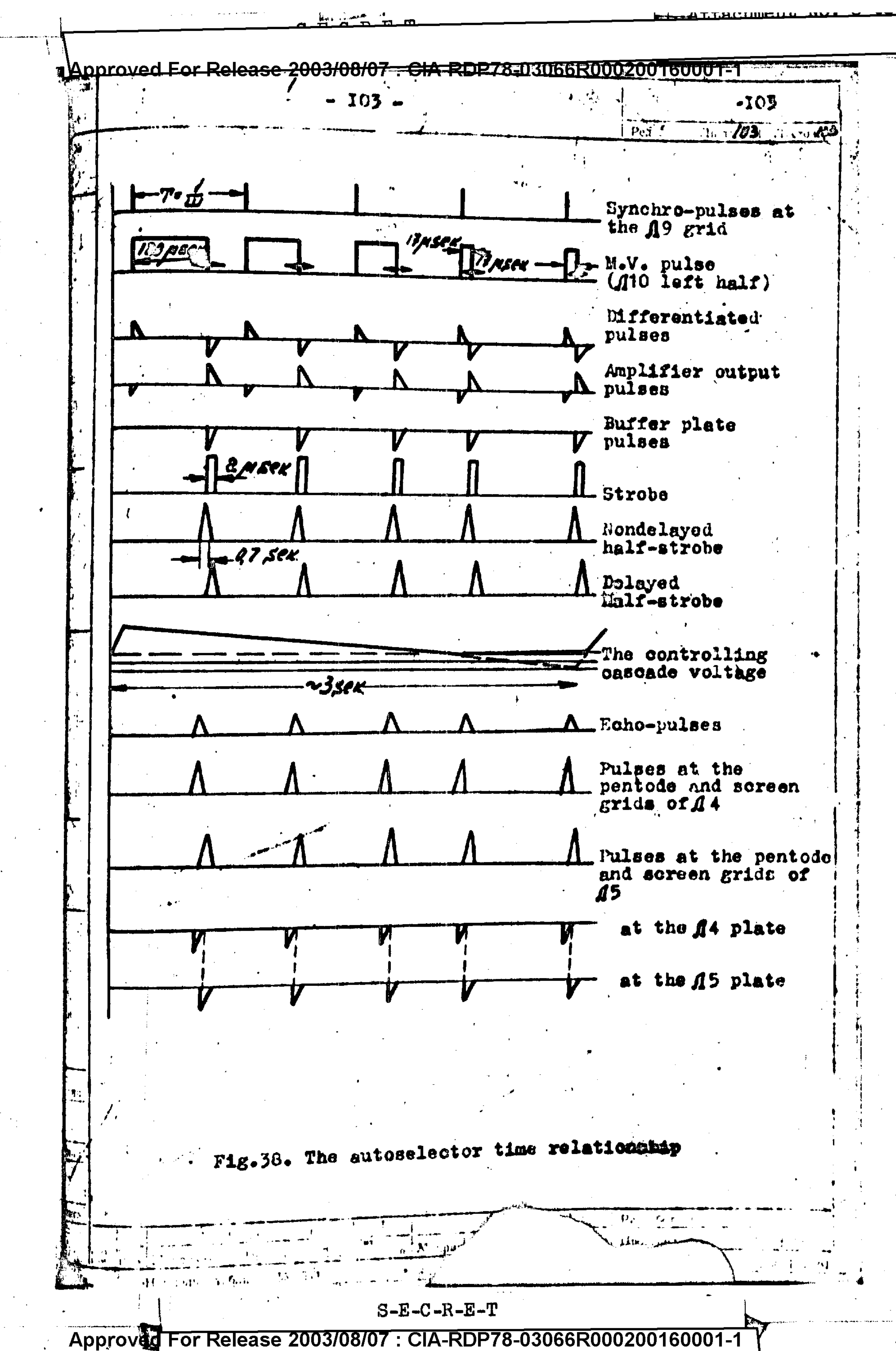


Fig.38. The autoselector time relationship

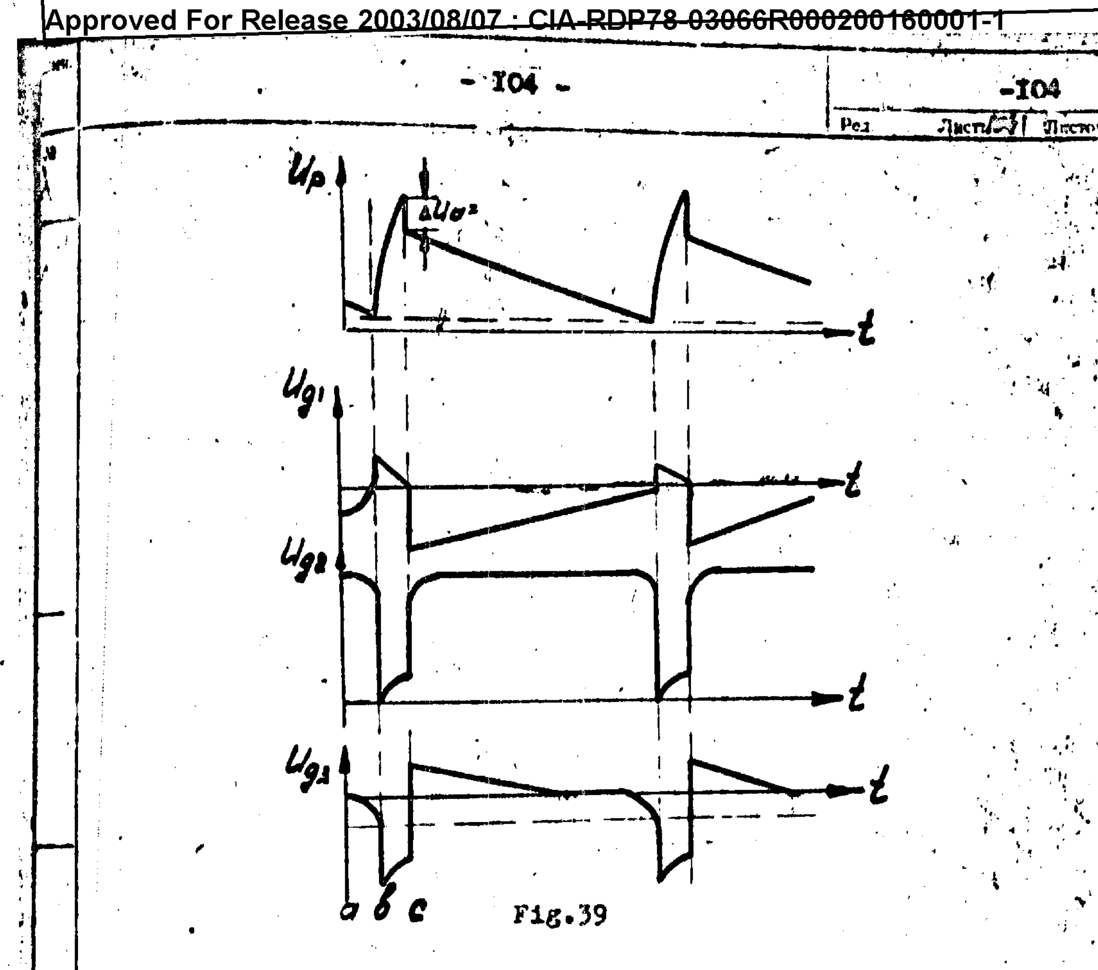


Fig.39

grid-cathode space and the tube Л1 grid-cathade space to the plate voltage value.

The Л1 plate voltage is increasing (relatively the cathode voltage value). The pentode grid nerative voltage is decreasing with the capacitor C1 discharging. When the pentode grid voltage became near to the cathode potential. the plate current appears (the fig N 39, point "c") and devepops the voltage drop across the resistor R5, which is applying to the control grid of Л1. It leed to a new redistribution of the tube current, the plate current sharply increases, the screen grid current sharply decreases.

and with it, the screen grid voltage increases, the capacitor CI became to charge again, and the pentode grid voltage became positive. As a result the plate current increases still more.

In the moment of plate current jump (point "c") the control grid voltage becomes suddenly negative and practically equal to the tube cut off value, i.e. the capacitor C3 discharging network consists of the power source and resistors R56, R23, R22, R6 only. As the capacitor C3 is discharging, the control grid voltage is increasing, the plate current is increasing also, and the plate voltage is decreasing. If the negative feedback between the plate and the tube I control grid will be absent, the process will be a kind of avalanche-type increase of the plate current till the screen current drops to zero and the plate voltage decreases extremely.

Owing to the strong negative feed back, the plate current increase process flows more slowly. The plate voltage decreases slowly also. When the plate voltage is near to the cathode voltage (fig.39 point "d") new redistribution of current is happened. The regeneration starts and the process will repeat. The capacitor C3 charging is carried out in sawtooth back stroke time (C3 charges till the I plate voltage will be reached). The back stroke time is determined by the time constant CI (R3+R4).

The forward stroke time is determined by the time constant of the network C3 (R6+R56+R22+R23).

The transitron output sawtooth period is determined with the generator control grid biasing which is obtained by

means of the potentiometer R27 ("search speed").

The tube Л2 left half ("search start tube") serves for transmitting the control cascade output signal to the M.V. control grid. In searching regime, the cathode follower output sawtooth is clipped from below by means of the grid current of the tube Л6 (right half), which has a common load R14, R15, R8 with the tube Л2 left half. So the searching start point or the minimum searching limit may be changed by means of the potentiometer R53. Besides that the maximum limit of searching may be changed by means of the potentiometer R12, which provides the biasing of the M.V. control grid and, with it, the M.V. pulse length. The potentiometers R12 and R53 are placed on the unit front panel with inscriptions: "search range" and "search start".

The command N 2 device

The input echo-signal goes through the socket N 23 and the capacitors CII and C50 to the coincidence stage Л14 (6H1П left half). The tube is normally cut off by means of a negative biasing from the divider R107, R108, RIII and zero plate voltage. When the echo-pulse is applied to the grid and the strobe-pulse is applied to the plate, the tube is cut in, and with it, the negative voltage is developed across the load R105. The later charges the capacitor C51 negatively through the resistor R106. When the echo-pulse amplitude became enough, the voltage cut off the tube Л15 and with it, the relay P-2 winding became currentless. As a result of that, the contacts I and 2 became open and the relays PI and P4 became currentless.

Which causes the following switchings:

I) The relay P-I through the closed contacts 1 and 2 feeds the command N 2 signal (+27 v.) to the plug connection Ш4.

2) Since the relay R-2 contacts 6 and 7 are open and the contacts 4 and 5 are closed, the slider of the potentiometer R21 "search speed" is disconnected with the "accumulator" Capacitor, when the large capacitance C53 is connected in parallel with the capacitor C6.

3) The relay P4 through the closed contacts I and 2 cut off the cathode follower Л6 (right half).

The clipping diode Л14 (right half) limits the tube Л15 grid negative voltage to provide the relay P2 release time independance from the echo-pulse amplitude. The clipping is carried out by means of the diode cutting in, when the negative voltage of the capacitor C51 (or at the tube Л15 grid) became equal to definite value.

The time constant of capacitor C51 discharging through the resistors R105 and R106 provides the tube Л15 cutting off during 2.5 ÷ 3.5 sec (the command N 2 cutting off delay time) after the echo-signal disappearing. As a result the command N 2 is not cutt off during 2.5 - 3.5 sec after the echo-signal disappears.

Tracking regime

The range autotracking regime consists in the strobe delay time changing, depending on the echo-signal delay time relatively to the syncho-pulse. In tracking regime the time discriminator becomes to operate and the controlling cascade operates in the d.c. amplifier regime

S-E-C-R-E-T

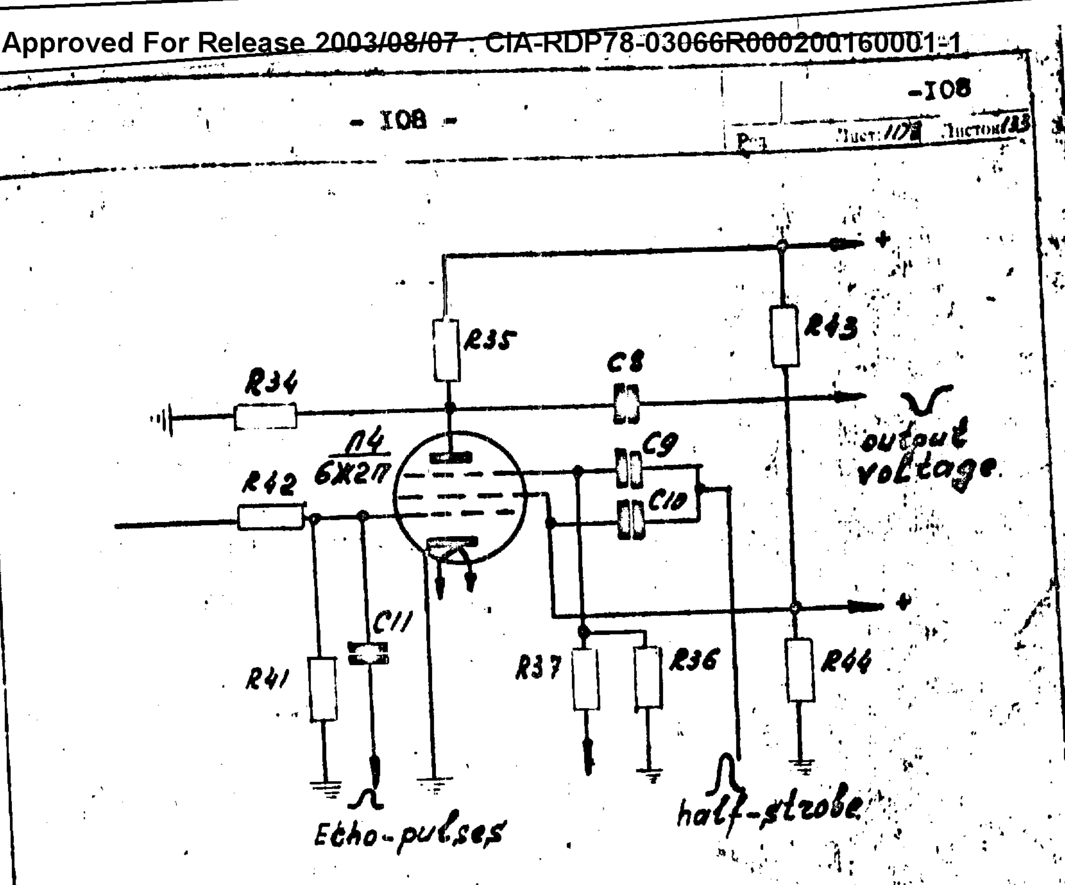


Fig.N 40. First coincidence stage.

The coincidence stages are the type 6Ж2П tubes Л4 and Л5. Both of the stages are normally cut off by using the negative biasing of the pentode and control grids from the dividers R42, R4I, R36, R37, R46, R47. The divider R43, R44 positive voltage supplies the screen grids. The echo-signal is applied to the control grids from the socket N 23. The positive half-strole pulses are applied to the pentode and screen grids.

The difference detector is a type 6 X 2П double diode tube Л3. Both of the diode are normally cut off. The righ one is cut off by the plate voltage approximately equal to -50v; the left one - by the cathode voltage equal approximately to +I00v. Let us eximine two time disposition

of the half-strobes relatively the echo-signal.

The first case is when the echo-pulse coincides with the nondelayed half-strobe and is not coincide with the delayed half-strobe. As a result of this disposition, the tube Л4 output negative pulse will be produced. The pulse amplitude depends on the overlapping area of the signal-pulse and the half-strobe. The coincidence pulse cut in the detector right diode. As a result the "accumulator" capacitor C6 will be charging positively. The capacitor C6 voltage depends on the coinsidence pulse amplitude. The cathode followel Л2 (right half) grid and cathode potentials became to in crease. The increasing (is) transitted to the controlling cascade ЛI input. The ЛI plate current increasing speed became to increase and, with it, the half-strobes became to move more rapidely.

If the echo-pulse coincides with the delayed half-strobe, the left diode is cut in and the capacitor C6 will be charging negatively. The negative voltage, transitted to the controlling carcade input, decreses the ЛI plate current increasing speed; it carryes out the transitron generation stopping. The controlling cascade became to operate in the d.c. amplifier regime.

The "accumulator" voltage which is a result of the echo-pulse tracking dynamies, is amplified by the controlling cascade, cathode followed by the Л2 left half and applied to the multivibrator ЛI0 control grid.

The M.V. pulse length and with it, the strobe delay time are depended on the M.V. grid voltage.

-II0

The echo-signal placed aproximately simmetrical relatively the half-strobes and the accumulator voltage is near to zero value, since the discharging current is equal to the charging one. In the tracking regime the echo-signal delay time is decreasing continuosly and the C6 voltage is within 0.3-0.5v.

The "accumulator" voltage adjusting is carried out by the potentiometer R23. The potentiometer installed on the front panel and inscripted as "accumulator voltage".
When command N 2 is locked on , the capacitor C6 potential should be set equal to zero to compensate the nonidentity of the tubes and the circuit element of the time discriminator (tubes Л3, Л4 and Л5).

When the echo-signal is lookedon and the command N 2 is cut in, the relay P2 disconnects the slider of the search speed potentiometer P2I from the cathode follower Л 2 (right half) grid and connects in parallel with the capacitor C6 the large capacitance C53. As a result the "accumulator" time constant is increased greatly. Thanks to that , when the echo-pulse disappears, the cathode follower Л2 grid potential slow increasing is provided (by means of the accumulator capacitor recharging) and with it, the half-strobe moving is going on with the same speed and to the same direction.
So the time constant increasing provides the speed memory of the echo-signal tracking.

The unit power supply

The unit power supply is carried out by the voltages:

+300 v unregulated,
+300 v regulated,
-147 v regulated.

The filament power supply is carried out by the isolated filament tranformer, placed in the unit KI-I3M. The all voltages are led into the unit through the plug connection Ш 4.

§ 9. The unit KI-IOM elementary diagram

The unit KI-10M output voltages are:

I) +300 v. unregulated, loaded by 63 ma;
2) +130 v. regulated, loaded by 152 ma;
3) +300 v. regulated, loaded by 92 ma;
4) -300 v. regulated, loaded by 13 ma;
5) -147 v. regulated, loaded by 26 ma.

The 115 v 400c. primary fed the transformers Tp-I and Tp-2. The first one carry out the high voltage to feed the plates of the kenotrons and the regulator tubes. The plate transformer has a primary winding taps, which provides the high voltage variance, when the unit is adjusted.

The transformer Tp-I secondary voltage goes to the four fullwave kenotron 5Ц4М rectifiers. The capacitance-inductance П type filters are at the outputs of the rectifiers. The +300 v unregulated voltage is taken out immediately after the filter and its value may be changed by the series resistor RI. The resistor R2 is for the

safety sake, since it take out a residual charge from the capacitors CI and C2 after the rectifier switching off.

The -I47v voltage is taken out from the stabilovolt СГ3П (Л I3), which is placed at the -300v regulated rectifier output. The -I47v value is determined by the stabilovolt СГ3П (Л I3) characteristic. The regulating circuits of the all rectifiers are identical. Its operational principle consists in voltage absorbing by the controlling tube, which is in series with the load.

The +300v and -300v voltage regulator circuits consists of the type 6 HI3C tube Л 7 the type 6Ж1П tubes Л8 and Л II and the СГ3П stabilovolts Л9 and ЛI2. The tube Л7 an absorbing tube, the tubes Л8 and ЛII are d.c. amplifiers, and the СГ3П type Л9 and ЛI2 are a reference voltage source. The +I30v, regulator tubes Л4 (6 HI3C type) and Л5 (6Ж1П type) carry out the same functions as they are in the previous rectifiers. The tube 6HI3C both triodes are connected in parallel to provide a large load current passing.

The stabilovolt "Л9" voltage divided by R35 and R36 is using as a reference voltage source of that rectifier.

The operational principle of the voltage regulator

When the output rectified voltage vary, the d.c. amplifier grid voltage also vary, since it is a difference between the part of the output voltage and the constant reference voltage of the stabilovolt СГ3П . This difference voltage is amplified by the tube 6H13C and applied to the

controlling tube 6H13C grid to change the its voltage drop. Let us examine the case, when the 115 v primary voltage is increased. It causes the rectified voltage increasing and with it an amplifier grid negative bias decreasing and the amplifier plate current and the plate drop voltage increasing. As a result of that the negative biasing and with it, the internal resistance of the controlling tube will be increased. The controlling tube internal voltage drop increases by the value equal to the voltage increasing before the regulator, i.e. the later will be compensated.

When the primary voltage decreases, the rectified voltage decreases the d.c. amplifier grid negative biasing increases, the plate current and drop decrease and the controlling tube 6H13C grid negative biasing decreases. As a result of it the internal resistance and the voltage absorbing of the tube 6H13C will be decreased by the value, equal to the rectified voltage decreasing.

When the load current decreases or increases, the rectified voltage also increases or decreases or decreases and the regulator circuit operates just as it was described above. The voltage rated values +300 v -300 v, +130 v are set by means of an amplifier tube grid biasing variance, which is carried out by the variable resistors R13, R22, [illegible] To improve the rectified voltage stability an unregulated voltage parts are aplied also to the d.c. amplifier grids from the voltage dividers R4, R16, R25. So the input voltage is also influences upon the d.c. amplifier grid. The influence process is analogical to the described above one.

To decrease the output voltage pulsation the capacitors C6, CII, CI6 connect the positive terminals and the d.c. amplifier grids. To avoid a self-oscillation of the d.c. amplifiers the large capacitance C7,CI2 and CI7 are placed at the output.

To obtain an operational stability the resistors R9, R7, RI7, R26 are placed in the grid networks of the controlling tubes and the capacitors CIO, CI5, CI8 shunt the stabilovolts.

The resistors RI9, R28, R20, R29 serve as a ballast resistance of the stabilovolts and provide the normal current of the stabilovolts ЛI2 and Л9. To avoid the switching on interelectrode breakdown the controlling tubes are shunted by the resistors R5 and R34. The capacitors CI9 and C20 are provided to decrease the output pulsation.

§ 10. The unit KI-IIM description

The antenna KI-IIM description is given in the chapter VI.

§ 11. The unit KI-I2M elementary diagram

I. The triggering pulse amplifier.

The positive triggering pulses, which have an amplitude less than 8v and pulse duration 0.6-I.0 μ sec, go to the amplifier Л2 (left half) grid. The negative amplifier output pulses go to the multivibrator ЛI left plate. The plate receive their operation voltage through the filter R6, C4.

25X1

2. The delay multivibrator

The double triode Л1 is a single stroke multivibrator, which is triggered by the pulses, applied to the plate. The M.V. makes the positive pulses with length equal to I70 μ sec. The M.V. pulse length may be variated by the resistor RI4, placed in grid network. When the frequency "Ф" voltage is applied to the M.V. grid, the operation regime changes so that pulse length is variated within ±20 μ sec relatively the initial delay time.

In "B" regime the command N 2 (+27v) is applied at the M.V. cathode by the relay R2. This voltage cut off the Л I left half, when the righ half became to operate as an amplifier. The output pulse length became equal approximately I μ sec.

3. The differentiated pulse amplifier

The M.V. output pulse is diffentiated by the network C7, RI2. After differentiating the positive pulses are clipped out by a grid current of the amplifier, since the biading is equal to zero. The negative pulses are amplified and fed to the normally cut off blocking-generator Л3 grid.

4. The preliminary blocking-generator.

The tube Л3 left half is a blocking-generator. The plate receive its operating voltage through the filter R2I, CI4. The tube Л3 is cut off by positive voltage applied to the cathode from the divider R2, HI. The positive amplifier output pulses applied to the blocking-generator grid cut in the tube and trigger an oscillation.

25X1

The blocking-generator output pulses are delayed by the M.V. pulse length. The tube Л3 right triode is a diode clipping the negative pulses. The diode is boaded by the cathode resistor R26. This circuiting improves the blocking-generator output pulse shape and is a decoupling between the pre-blocking-generator and the power blocking-oscillator modulator.

5. The blocking-oscillator/modulator/.

Thr power blocking-generator Л4 (ГИ-30) carry. out the modulation of the U.H.F. generator. The blocking-generator is normally cut off by means of large negative biasing (across the resistor R23). When applied positive amplitude I20v - I50v pulses at the grid, the blocking-oscillator is cut in.

The output pulses amplitude and length are determined by the tube ГИ-30, the pulse transformer and other circuit elements. The tube is supplied by the high voltage rectifier which is made as a Lature circuiting with the tubes 2Ц2С The plate voltage is approximately wqual to 2500v , the screen grid voltage is within 800v - 850v ; both the voltage are obtained from the voltage-divider network formed by R30, R3I and RI7.

6. The U.H.F. oscillator

The oscillator tube Л9 is the metal-ceramic type ГИ-I3C tube grounded grid circuited. The oscillator plate circuit is a cavity resonator. The grid circuit is a short-circuited section of a coaxial line. The cavity circuit has two tuners provided frequency and conpling tuning. The frequency tuning is carried out by means of the rod with the disk-shaped end,

which is led into the cavity. The disk position variance provides the plate circuit capacitance variance and, with it, theoscillator frequency tuning.

The antenna coupler is also a rod type. The rod is besides that an inner conductor of the output coaxial live with the short-circuited stub. The controlling of the antenna coupling may be carried out by leading in of the rod and also by changing the short-circuited stub length. The oscillator tube is plate modulated. When the modulating pulses are absent the plate voltage is equal to zero. So,an oscillation is only when the modulating pulses are applied to the tube plate. The U.H.F. pulse length is determined by the mod... pulse lebgth on the whole. The modulating pulse amplitude provided an intensive oscillation must be equal to I600... approximately.

The antenna KI-IIM loads the oscillator and is co... with it by type PK-47 U.H.F. cable, which length is ... The unit KI-I2MP is supplied by the A.C. II5v 400 c... The voltage feed the primary winding of the transfo... which is placed in the unit. As it was menshion... h.v. rectifier is a kind of Lature circuiting. The... supply of the other tubes (except the diode cl... R.F. oscillator) is carrird out by the ordin... rectifier 6Ц5С. To decrease the pulsation, the... filter C2I, R27, C22. The M.V. and amplifier tu... supplied by the 250v - 280v voltage. The pre-bl... receives its 350 - 380v voltage from the same rect... before the filter.

To obtain the nominal value of the carrier frequency, the oscillator tube shoud be heated preluminary. So before the unit switching on the heating voltage is applied to the tube ГИ-I36 filament. The +27v. is given to P-I through the plug Ш-I5 pin I2. The relay P-I commutates the filament supply from the transformer filament winding to the plug Ш-I5 pin II and ground. The heating voltage (II - II.5 v) is at the pin II. After the I5-minute heating the +27v is taken away, the relay releases and commutes the tube ГИ-I35 filament to the transformer again. After that the unit is ready for operating.

§ I2. The unit KI-I3M elementary diagram.

The main part of the unit KI-I3M diagram consists of the junction cables and the seventeen plug connection. The nine plug connection are provided for bonding with the Radar units (Ш I ÷ Ш8, I5). The motor-alternators MA-250M and MA-500M making the A.C. II5v 400C voltage are jointed with the plugs Ш-I7 and Ш-I8. The Radar is power supplied by the missile-born 27v source through the plug Ш-I4 and by the unit KI-IOM five rectified voltages through the plug Ш-9. The Radar may be connected with the mother-ship monitoring board DK-I7M and with the bench board K-I09 by means of the plugs Ш-II and Ш-I3 accordingly. The plug connection Ш-I2 serves for conpling АПК-5ВК. There is two type ЕМ 4500.002 relays in the unit. The relay P-I disconnects the -I47v network, when the command N 2 look on or there is a mother-ship monitoring with the command N 2 imitation by means of the board DK-I7M.

The relay P2 provides switching of the motor-alternators MA-250M and MA-500M. The switch "BI" is connected with the relay P2 winding . When the Radar should be switched on by means the board K-I09 switch it is necessary to set the toggle-switch "BI" in the position "On". So the motor-alternators will be switched on by giving the +27v from the board K-I09 to the relay winding.

It the board K-I09 is not using, the switching of the motor-alternators is carried out by using the toggle-switch "B" only.

The switch "B2" provides the unit KI-7M switching on. The switch "B3" provides the unit KI-I2MP switching on. The variable resistor RI carry out the precise setting of the MA-500M output voltage. Since RI is in the exitation winding net work, its value variance governs the MA-500M output voltage.

The variable resistor R2 carry out an analogical function with a relation to the motor-alternator MA-250M. The transformer TP-I provides the unit KI-9M tube filament supply.

The unit preservation from on accidental failture and shorting of the conductors is carried out by the safety fuses in the +27v and II5v networks.

CHAPTER VI

STRUCTURE OF THE UNITS

1. Unit K1-0

The unit K1-0 (shock-absorbing framework) is a rectangular cast-in frame, having pockets for installation of the units K1-5MP, K1-6M, K1-8M, K1-9M and K1-10M. An aluminium bottom sheet is fastened to the framework by means of 14 screws. To fix the units in the framework, washers are fastened on the framework bottom; studs of the units are introduced into the washer sockets.

To fasten each unit, bushings with the thread M4 are provided at the framework top part. The front and rear sides of the framework are covered with dural holed shield.

On the right side of the framework there is a boss with four fixing bushes to install the units K1-4aM and K1-4бM. On the same boss two brackets are fastened to prevent the units K1-4aM and K1-4бM from mechanical damage. There is four floating bushings with thread M6, destined to fasten the units K1-4aM K1-4бM in the right wall of the framework.

The frame of the framework has four bosses on its corners (on the right and left sides below). The bosses are to be mounted on the shockabsorbers AD-8. Fastening of the frame must be carried out by means of bolts M6x20. The Radar grounding crosspiece thimbles are to be placed under the bolt heads.

On the right wall of the framework the Radar designation is fastened with two screws.

In installing the framework in an object "KC", multy-layer foamed rubber dampers are to be installed on the upper framework corners to prevent displacement of it along the axes "X" and "Y".

2. Unit K1-1M

The dielectric rod, the waveguide adapter and the waveguide are fastened on a special bracket. The bracket is cast integral with the base. The base has four holes by means of which the unit is fastened to the "KC". A metal cap prevents the dielectric rod from damage.

The cap must be removed when the unit KI-IM is to be installed !

The waveguide ends with round flange, having thread. There are locking screws on its ferrule to fasten rigidly the antenna to the bracket.

3. Unit KI-3M

The unit consists of copper weldless pipes (24 mm x 10 cross-section). All the sections are interconnected by means of flanges, fastened with four screws. On one side of each connection an ordinary flange is provided, on other side a choke-flange is provided. The connection of this type staves off U.H.F. energy loss in the joint. Top part of the waveguides is painted to prevent from moisture effect. A circular rubber gasket is placed in the choke-flange socket for the same purpose. There are some unpainted belts on the waveguides. The belts are destined for the furrels, fastening the waveguide to the "KC" body. One section of the unit is made in a pleated form to prevent the units KI-4aM and KI-3M from damage, when the Radar K-IM, installed on shockabsorhers, is subjected to vibration.

4. Unit KI-4aM

The unit is made from waveguide pieces having the same cross-section. The mixer section input and output as well as lateral arms of the doble triplet end with flanges. The mixer section output ends with choke-flange and the klystron section output ends with plane flange.

A klystron holder is installed in arm 3 of the doble triplet. A klystron holder is manufactured in form of cast cylinder with a cap.

The mixer section has a special pocket to install the crystal. The signal drainage is carried out by means of a U.H.F. cable, which ends with an angular plug. The mixer chamber and the klystron section are fastened to a common metal plate on brackets. A Г-shaped bracket is placed on the same plate. The bracket serves for plug-connector fastening. The plug-connector is used to feed filament and plate voltages to the klystron K-38. The plate is fastened to the framework by means of studs and screws.

5. Unit KI-4bM

The unit is made from two waveguide pieces, having the same cross-section and connected so that the wide end of one piece is matched with the narrow end of other piece. The pieces are connected electrically by means of two slots of the antiphase coupler.

The crystal holder consists of an binding assembly, connected directly to the mixer housing, and contacting assembly, insulated from the housing (d.c.implied). The crystal holder ends with an angular contact, which is used for connection of the crystal mixer to the I.F. preamplifier. The input and output of the mixer section end with waveguide flanges.

The mixer section is fastened to a metal plate on brackets. The plate is fastened to the framework by means of studs and screws.

6. Unit K1-5MP

The unit K1-5MP is a brass chassis, on which are located all the circuit components. On one side of the chassis the unit tubes are fastened, on other one the mounting elements are fixed. The unit tubes are protected with shield-holders and are located in four rows in accordance with the unit channels. There are tuning plungers on the tube side; the plungers are used to tune IFA circuits. Besides, filament transformer with its filters is fastened on the tube side. The trasformer is covered with a shield. Monitoring jacks, located on the chassis, are used in tunin the unit. All the unit components, fastened on the tube side of the chassis or on the unit front panel, have appropriate engraved inscriptions.

The plug cknnector Ш7 is located on the unit front panel. The connector Ш7 serves for voltage supply to the unit and for connection of the unit K1-5MP to other units. On the unit front panel the cable Ф30 plug input jack, the plug output jacks of the synchronization channel Ф26, channel ES Ф27 and channel A.F.C. Ф29 are located too.

On the unit front panel the switch B-1 and axes of the
"cycle of blocing g." "manual"
potentiometers "период Б.Г.синхр.", "ручн. ", " " and
"manual" "A.F.C"
"A.F.C"
"АЧК" are mounted. The potentiometers "ручн. " and "АЧК" are used to tune the unit simultaneously with the unit K1-4aM.

To facilitate the unit output voltage monitoring, monitoring jacks are mounted in parallel with the UHF output jacks.

The unit has two shields to avoid stray couplings between the channels. The shields divide the unit in 3 compartments. Each compartment serves for one channel mounting. The unit cover is fastened with screws, which ensure reliable contact between the chassis and the cover. The unit is fastened in the frame by means of special screws and studs.

7. Unit K1-6M

The unit chassis is made from dural and has the following dimensiones: 250x295x120. On the top side of the chassis tubes, capacitors (МБГП type), potentiometers R170, R147, relay P3, pulse transformer BM-4-720-001, filament transformer and other components are mounted.

The time-motor is fastened on the chassis from above.

On the front panel the following potentiometers are mouted:

1. "Баланс "у" - Balance "y"
2. "Баланс " " - Balance "Z"
3. "Ампл.опорных напряжений " - "Reference voltage amplitude".
4. "Фаза опорных напряжений " - "Reference voltage phase"
5. "Усиление Д " - "Amplification "D"
6. "Усиление Я" - Amplification "Я"

The knobs of the potentiometers "Amplification "D" and "Amplification "Я" have limbs with divisiones.

On the front panel of the unit the plug connectors Ш-5 and Ш-6, UHF plugs Ф24, Ф25, Ф26, Ф27 and Ф28, as well as the monitoring jack "КП синхр." "CF synchr" are mounted.

The unit mounting side is protecled with a cover, which is fastened by means of screws on each side and by using special screws from below.

8. Unit K1-7M

The unit K1-7M structure is described in the elementary diagram description.

9. Unit K1-8M

The unit is mounted as an assembly, consisting of two subunits: K1-8aM and K1-8бM.

The unit K1-8aM is located directly on the unit K1-... plate. The unit is a completely shielded box.

The input circuit is mounted directly at the crystal and is connected to the latter by means of a UHF plug.

The output cable is built-in in the chassis, other end of the cable has a plug to be conneted to the unit K1-8...

The unit K1-8бM is a brass chassis, on which all the circuit components are mounted. On one side of the chassis the unit tubes are fastened, on other one the mounting elements are fixed.

The unit tubes are protected with shields. On the tube side there are plungers of the IFA circuits and the filament transformer is mounted. The monitoring jack Г5. is to be used to tune the unit only.

All the unit components, mounted on the tube side of the chassis or on the unit front panel have appropriate engraved inscriptions.

On the front panel of the unit K1-8M the plug connector Ш-2, control potentiometer MVC, monitoring jacks ES, AGC, MVC and swithe B1 are mounted. There are the following plugs too:

1. Output to the unit K1-6M ф24
2. Output to the unit K1-9M ф23
3. Input ф20
4. Strobe input from the unit K1-9M ф22.

The unit chassis is divided with a crosspieceshield, separating mounting side.

On one side of the chassis IFA stages and the second detector and video-amplifier stages (to the unit K1-6M), which are separated with a shield are placed in line.

On other side of the chassis AGC stages, video-amplifier stages (to the unit K1-9M), filament transformer and a shielded compartment of the feeding filters are placed.

The mounting elements are protected with a cover, fastened with screws. A guide stud, fixing the unit in the framework compartment, is located on the rear side of the unit.

10. Unit K1-9M

Unit chassis dimensiones are 285x135x49. Tubes, capacitors (type МБГП and КБГ-МН), relays PC-13 and PCM-2, delay line, type BM-4-720-001 pulse transformers etc. are mounted from above.

The main mounting elements are located on the chassis from below. The following potentiometers are fastened on the front panel:

1. Control "диапазон поиска R12 - "search range" R12.
2. Cintrol "скорость поиска" R21 - "search speed" R21.
3. Control "напряжение накопителя" R23 - "accumulator voltage" R23.
4. Control "начало поиска " R53 - "search starting" R53.

The plug connector Ш4, UHF plugs Ф22, Ф23, Ф25 and monitoring jacks "strobe" and "accumulator voltage" are mounted on the front panel.

The mounting side of the unit is protected with a cover which is screwed to the side walls of the chassis.

11. Unit K1-10M

Two plug connectors Ш9 and Ш-35 and the output voltage control potentiometers are located on the unit front panel. All potentiometers have engraved inscriptions.

The plug connector Ш-9 receives all the voltages, produced by the unit. The same cable feeds the voltage ~ 115v, 400c, from the motor-alternator MA-500M to the plyg connector.

The plug connector Ш -35 is to be used only for monitoring of the rectified voltages.

A chassis is fastened perpendicularly to the unit front panel. All the unit components: tubes, capacitors, resistors and chokes are located on this chassis. The tubes are fastened by means of special tube-holders.

The unit frame has П-shape ribs, which ensure appropriate regidity.

12. Unit K1-11

The antenna is an open end of the waveguide, (72x34 cross-section), corness of which are cut off simmetrically. A metal rod (ø 5 mm) is located in the outlet hole of the waveguide perptndicularly to its wide walls. Nip between the edge and the rod axis is 10,5 mm. The antenna feeding is carried out by means of a coaxial lead, one end of which ends with the exciter and other one ends with a standard 50 ohon UHF plug for the cable PK-47.

There is a hole in the wide wall of the waveguide. The hole ensures access to the exciter.

13. Unit K1-12MP

The unit K1-12MP is a hermetically sealed instrument. The sealing is necessary to ensure normal pressure within the unit, when it is elevated at an altitude. When the pressure drops, a breakage is possible: the unit max.voltage is 2600v.

Within the unit housing the following components are fastened:

UHF generator chamber, two UHF plugs, sealed plug connector. The UHF plug Φ28 serves for the unit K1-12MP tuning, other plug Φ31 serves for UHF cable connection (the cable is connected to the K1-11 radiating antenna). The plug connector Ш-15 feeds voltages, necessary for the unit.

The capacitors, pulse transformers, power transformer, relays PC-13 and P3C-6 and tube sockets are fastened on the chassis.

The unit cover is fastened with 6 screws, which are screwed in the unit honsing. To ensure sealing 2 rubler rings are glued in the unit housing, leather gaskets and rubber gaskets BШ-15 are put on the plugs.

13. Unit K1-13M

The unit is a flat, rectangular box having removable top cover. All mounting elements are placed inside the unit. Plug connectors, variable resistors and switches are placed on the lateral walls.

Some plug connectors are manufactures as a cable lead; they are fastened at the ends of short cables, which go out of the box through bushings. Variable resistor axes are mounted on the top wall and have screw-driver slots. Each resistor has engraved inscription, which indicates motor-alternators and voltage to be controlled.

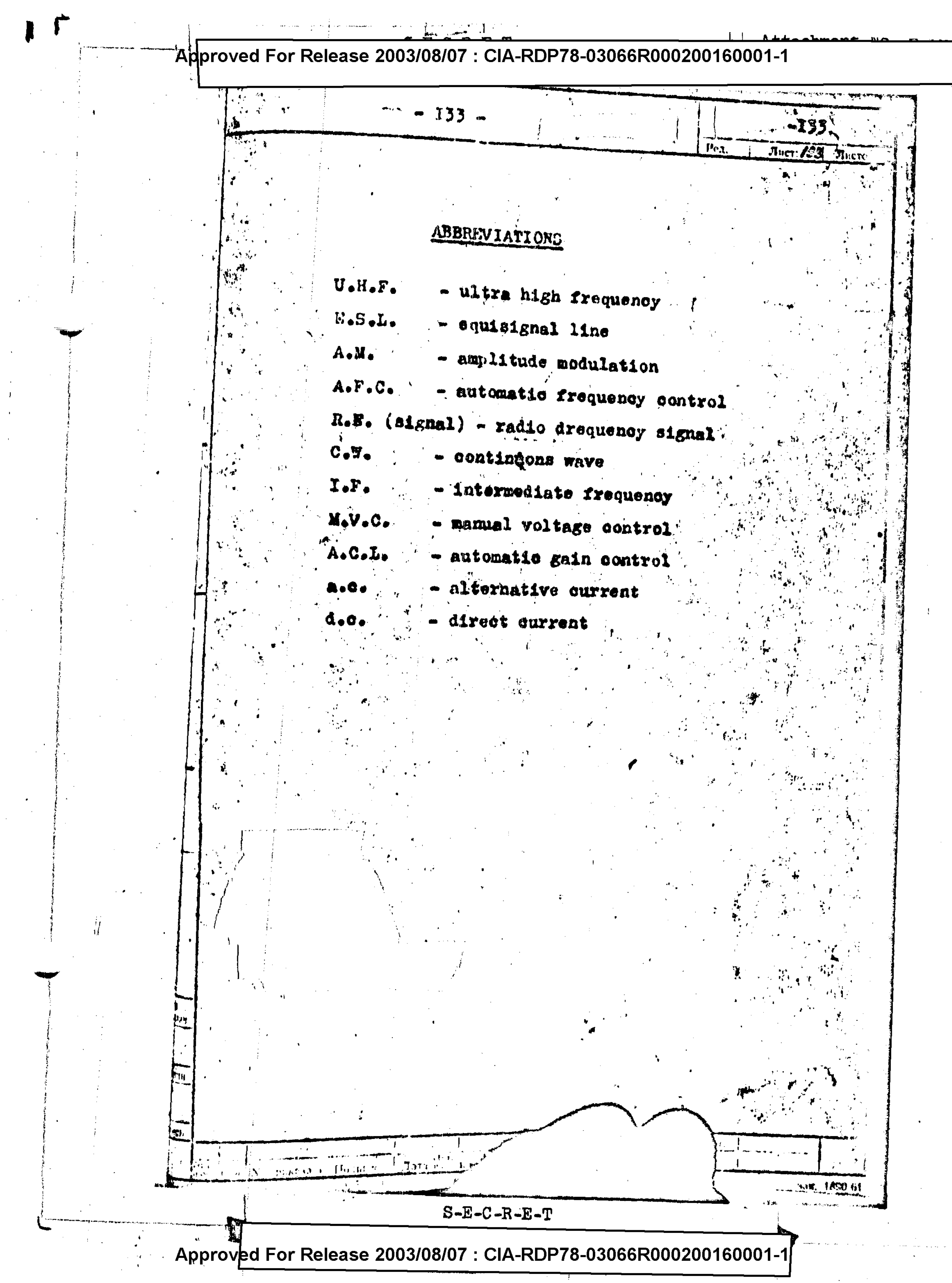
The switches BI,B2 and B3 are mounted on the bottom wall and have marks, corresponding to switching on or switching off of the motor-alternators or units.

All the bunched connecting wires, relays and filament transformers are located on the box bottom.

The fuse plate is placed in the upper part of the box; two mounting panels are located below. The box is protected from above with a top cover, having a little hatch against the fuse plate.

S-E-C-R-E-T

LIST OF PHOTOS

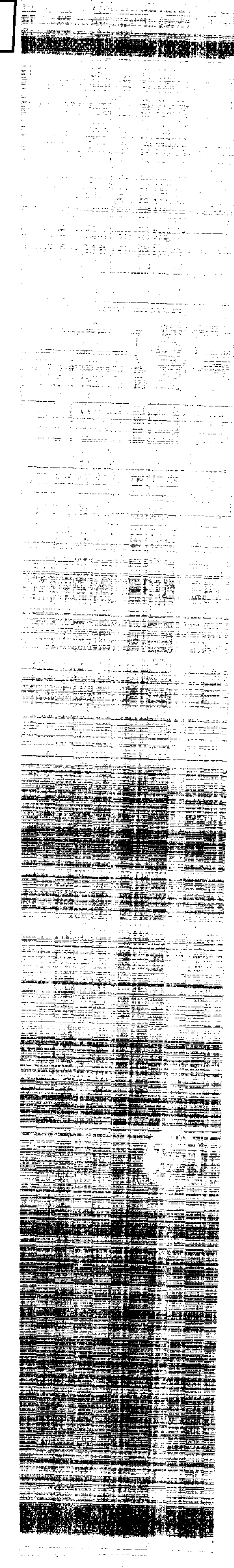


ABBREVIATIONS

U.H.F. - ultra high frequency
E.S.L. - equisignal line
A.M. - amplitude modulation
A.F.C. - automatic frequency control
R.F. (signal) - radio drequency signal
C.W. - continuons wave
I.F. - intermediate frequency
M.V.C. - manual voltage control
A.C.L. - automatic gain control
a.c. - alternative current
d.c. - direct current

S-E-C-R-E-T

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